

ADIS16354 ANOMALIES

This anomaly list describes the known bugs, anomalies, and workarounds for the ADIS16354.

Analog Devices, Inc., is committed, through future silicon revisions, to continuously improve silicon functionality. Analog Devices tries to ensure that these future silicon revisions remain compatible with your present software/systems by implementing the recommended workarounds outlined here.

ANOMALY STATUS

| Reference Number | Description | Status |
|------------------|--|--------|
| er001 | STATUS register not clearing when read | Fixed |

DETAILS AND WORKAROUNDS

STATUS Register Not Clearing When Read [er001]

| | |
|-----------------------|--|
| Background | The STATUS register contains various diagnostic error flags, which clear when read. |
| Issue | The STATUS register clears when Address 0x3C is read but not when Address 0x3D is read. This issue affects parts that have a date code of 0805 or earlier. |
| Workaround | For affected parts, use Address 0x3C during a read sequence to clear the flags. |
| Related Issues | None. |

Rev. 0

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