TXFE / MXFE FMC
EVALUATION BOARD

TXFE: AD9082 AD9082-FMCA-EBZ
MXFE: AD9081 AD9081-FMCA-EBZ

AD9082
JUNE 13 2019

DO NOT INSTALL
T1S T1T
C3S C3T C2T C1T C1S C2S
R4T R3T R3S R4S
ADC0 ADC1
DC BLOCK -- PAGE 1 OF 1

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DC BLOCK -- PAGE 1 OF 1

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C1 0.1UF
C2 0.1UF
C3 0.1UF
C4 0.1UF
C5 0.1UF
C6 0.1UF
C7 0.1UF
C8 0.1UF

DCBLOCK_B<7..0> DCBLOCK_A<7..0>
MAIN CLOCK PATH -- PAGE 1 OF 1

[Diagram of clock path]
ADC CLOCK DRIVER BALUN -- PAGE 1 OF 1

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LDO ANALOG 2P0V -- PAGE 1 OF 1

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**Diagram Description:**

- **Vin:** Input voltage
- **Avdd2:** Rail-to-rail output voltage
- **En:** Enable input
- **Byp:** By-pass
- **Ref:** Reference
- **Ref Sense:** Reference sense
- **Vin:** (Multiple instances)
- **Vin:** (Multiple instances)
- **Vout:** Output voltage
- **Vout Sense:** Output sense
- **In:** (Multiple instances)
- **Out:** (Multiple instances)

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**Notes:**


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