



\* FOR ASSEMBLY

BUFFER BOARD	DIP-8 BOARD	
DC1141A	DC1141B	U1 ON DIP-8 BOARD
-A	-A	LTC6930CDCB-4.19
-B	-B	LTC6930CDCB-5.00
-C	-C	LTC6930CDCB-7.37
-D	-D	LTC6930CDCB-8.00
-E	-E	LTC6930CDCB-8.19

DIV PIN SETTING	÷1	÷2	÷4	÷8	÷16	÷32	÷64	÷128
DIVC, DIVB, DIVA	000	001	010	011	100	101	110	111

### CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: PHILIP K.

DESIGNER:



1630 McCarthy Blvd.  
Milpitas, CA 95035  
Phone: (408)432-1900  
Fax: (408)434-0507  
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

## BUFFER BOARD

SIZE  
A

DWG NO.

DC1141A-1 \* LTC6930CDCB

REV  
A-1

DATE: Wednesday, April 16, 2008

SHEET 1 OF 1