

# HIGH DOSE RADIATION TEST REPORT AD9246S

*August 2016*  
Generic

## Radiation Test Report

Product:	AD9246S
Gamma:	0, 100k
Gamma Source:	Co60/TM1019 Condition A
Dose Rate:	129 Rad/s
Facilities:	VPT RAD
Tested:	7/19/16

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		I_avdd_DC_AV1.8_DV2.5		mA	I_drvdd_DC_AV1.8_DV2.5		mA	power_DC_AV1.8_DV2.5		mW
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	132.08821	131.54703		0.09928	0.20589		238.00699	237.29940	
	11	127.17855	126.89778		0.17497	0.13184		229.35881	228.74561	
	12	134.38232	135.33041		0.16439	0.13021		242.29915	243.92025	
	13	131.45426	131.74886		0.16439	0.14567		237.02864	237.51213	
	14	130.78044	131.18164		0.10254	0.21403		235.66113	236.66203	
	Min	127.17855	126.89778		0.10254	0.13021		229.35881	228.74561	
	Max	134.38232	135.33041		0.17497	0.21403		242.29915	243.92025	
	Mean	130.94889	131.28967		0.15157	0.15544		236.08693	236.71001	
	Std. Dev	2.96016	3.45634		0.03307	0.03967		5.32063	6.21999	
	Mean - 3 Sigma	122.06841	120.92066		0.05237	0.03642		220.12503	218.05003	
	Mean + 3 Sigma	139.82938	141.65868		0.25077	0.27446		252.04883	255.36998	
	Limit:	<200mA			<5mA			<425mW		

		Iq_avdd_125M_AV1.8_DV2.5		mA	Iq_drvdd_125M_AV1.8_DV2.5		mA	power_125M_AV1.8_DV2.5		mW
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	241.85872	242.01091		83.54940	84.90901		644.21922	647.89218	
	11	237.84912	238.47494		83.40641	85.01278		636.64442	641.78681	
	12	244.78190	244.71273		84.71020	81.15055		652.38291	643.35930	
	13	241.49007	241.91488		80.68286	79.44360		636.38926	634.05579	
	14	240.91879	241.36882		81.89543	85.35115		638.39239	647.84175	
	Min	237.84912	238.47494		80.68286	79.44360		636.38926	634.05579	
	Max	244.78190	244.71273		84.71020	85.35115		652.38291	647.84175	
	Mean	241.25997	241.61784		82.67373	82.73952		640.95225	641.76091	
	Std. Dev	2.84061	2.55646		1.75626	2.90839		7.62727	5.74169	
	Mean - 3 Sigma	232.73813	233.94847		77.40495	74.01434		617.93544	624.53584	
	Mean + 3 Sigma	249.78181	249.28721		87.94250	91.46470		663.96905	658.98598	
	Limit:	<350mA			<175mA			<910mW		

		Iq_avdd_OEoff_AV1.8_DV2.5		mA	Iq_drvdd_OEoff_AV1.8_DV2.5		mA	power_OEoff_AV1.8_DV2.5		mW
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	241.84896	242.06868		1.78304	1.88330		439.78572	440.43189	
	11	237.80437	238.39192		1.81193	1.90218		432.57767	433.86093	
	12	244.62321	244.70866		1.85278	1.77791		444.95374	444.92036	
	13	241.49251	241.88557		1.79598	1.76579		439.17647	439.80852	
	14	240.80567	241.34521		1.82186	1.89827		438.00485	439.16708	
	Min	237.80437	238.39192		1.79598	1.76579		432.57767	433.86093	
	Max	244.62321	244.70866		1.85278	1.90218		444.95374	444.92036	
	Mean	241.18144	241.58284		1.82064	1.83604		438.67818	439.43922	
	Std. Dev	2.79812	2.58849		0.02393	0.07430		5.07611	4.52293	
	Mean - 3 Sigma	232.78709	233.81737		1.74884	1.61314		423.44987	425.87044	
	Mean + 3 Sigma	249.57579	249.34831		1.89244	2.05894		453.90650	453.00801	
	Limit:	<300 mA			<5mA			<560mW		

		Iqz_avdd_AV1.8_DV2.5		mA	Iqz_drvdd_AV1.8_DV2.5		mA	power_PWDN_AV1.8_DV2.5		mW
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.09684	0.10017		0.07928	0.07846		0.37250	0.37644	
	11	0.09522	0.09584		0.07878	0.07907		0.36834	0.37018	
	12	0.15854	0.14497		0.07924	0.07961		0.48347	0.45996	
	13	0.09167	0.09557		0.07995	0.08005		0.36489	0.37215	
	14	0.11041	0.11110		0.07894	0.08062		0.39608	0.40154	
	Min	0.09167	0.09557		0.07878	0.07907		0.36489	0.37018	
	Max	0.15854	0.14497		0.07995	0.08062		0.48347	0.45996	
	Mean	0.11396	0.11187		0.07923	0.07984		0.40320	0.40096	
	Std. Dev	0.03081	0.02323		0.00052	0.00066		0.05531	0.04187	
	Mean - 3 Sigma	0.02153	0.04218		0.07767	0.07786		0.23727	0.27535	
	Mean + 3 Sigma	0.20639	0.18156		0.08078	0.08181		0.56912	0.52656	
	Limit:	<5mA			<5mA			<25mW		

		Iq_avdd_StndBy_AV1.8_DV2.5		mA	Iq_drvdd_StndBy_AV1.8_DV2.5		mA	power_StndBy_AV1.8_DV2.5		mW
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	41.65576	41.59888		0.07848	0.07859		75.17658	75.07444	
	11	41.37532	41.24398		0.07808	0.07798		74.67078	74.43410	
	12	41.97469	41.89860		0.07813	0.07869		75.74977	75.61420	
	13	41.73535	41.65454		0.07968	0.07960		75.32283	75.17716	
	14	42.07186	41.98682		0.07944	0.07932		75.92795	75.77457	
	Min	41.37532	41.24398		0.07808	0.07798		74.67078	74.43410	
	Max	42.07186	41.98682		0.07968	0.07960		75.92795	75.77457	
	Mean	41.78931	41.69599		0.07883	0.07890		75.41783	75.25001	
	Std. Dev	0.31011	0.33250		0.00085	0.00072		0.55902	0.59967	
	Mean - 3 Sigma	40.85898	40.69849		0.07629	0.07674		73.74077	73.45100	
	Mean + 3 Sigma	42.71963	42.69348		0.08137	0.08106		77.09490	77.04902	
	Limit:	<100mA			<5mA			<200mW		

		SPI_minPeriod_AV1.8_DV1.8		ns	CLK_tHi_AV1.8_DV1.8		ns	CLK_tLo_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	40.00000	40.00000		2.25000	2.25000		7.75000	8.00000	
	11	40.00000	40.00000		2.00000	2.00000		8.00000	8.00000	
	12	40.00000	40.00000		2.25000	2.25000		7.75000	7.75000	
	13	40.00000	40.00000		2.25000	2.25000		7.75000	7.75000	
	14	40.00000	40.00000		2.25000	2.25000		7.75000	7.75000	
	Min	40.00000	40.00000		2.00000	2.00000		7.75000	7.75000	
	Max	40.00000	40.00000		2.25000	2.25000		8.00000	8.00000	
	Mean	40.00000	40.00000		2.18750	2.18750		7.81250	7.81250	
	Std. Dev	0.00000	0.00000		0.12500	0.12500		0.12500	0.12500	
	Mean - 3 Sigma	40.00000	40.00000		1.81250	1.81250		7.43750	7.43750	
	Mean + 3 Sigma	40.00000	40.00000		2.56250	2.56250		8.18750	8.18750	
	Limit:		>40ns			<16ns			<16ns	

		SDIO_setup_AV1.8_DV1.8		ns	SDIO_hold_AV1.8_DV1.8		ns	CSB_setup_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	11	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	12	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	13	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	14	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	Min	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	Max	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	Mean	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	Std. Dev	0.00000	0.00000		0.00000	0.00000		0.00000	0.00000	
	Mean - 3 Sigma	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	Mean + 3 Sigma	0.00000	0.00000		1.00000	1.00000		-0.25000	-0.25000	
	Limit:		<5ns			<3ns			<5ns	

		CSB_hold_AV1.8_DV1.8		ns	DCO_Tpd_AV1.8_DV1.8		ns	D0_Tpd_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.50000	0.50000		5.40000	5.40000		6.00000	5.90000	
	11	0.50000	0.50000		5.40000	5.40000		5.90000	6.00000	
	12	0.50000	0.50000		5.50000	5.50000		6.00000	6.00000	
	13	0.50000	0.50000		5.50000	5.50000		6.00000	6.00000	
	14	0.50000	0.50000		5.50000	5.50000		6.10000	6.00000	
	Min	0.50000	0.50000		5.40000	5.40000		5.90000	6.00000	
	Max	0.50000	0.50000		5.50000	5.50000		6.10000	6.00000	
	Mean	0.50000	0.50000		5.47500	5.47500		6.00000	6.00000	
	Std. Dev	0.00000	0.00000		0.05000	0.05000		0.08165	0.00000	
	Mean - 3 Sigma	0.50000	0.50000		5.32500	5.32500		5.75505	6.00000	
	Mean + 3 Sigma	0.50000	0.50000		5.62500	5.62500		6.24495	6.00000	
	Limit:		<3ns			<6.5ns			<7ns	

		D1_Tpd_AV1.8_DV1.8		ns	D2_Tpd_AV1.8_DV1.8		ns	D3_Tpd_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	6.00000	5.90000		6.00000	5.90000		6.00000	5.90000	
	11	5.90000	6.00000		5.90000	6.00000		5.90000	6.00000	
	12	6.00000	6.00000		6.00000	6.00000		6.00000	6.00000	
	13	6.00000	6.20000		6.00000	6.00000		6.00000	6.00000	
	14	6.10000	6.00000		6.10000	6.00000		6.10000	6.00000	
	Min	5.90000	6.00000		5.90000	6.00000		5.90000	6.00000	
	Max	6.10000	6.20000		6.10000	6.00000		6.10000	6.00000	
	Mean	6.00000	6.05000		6.00000	6.00000		6.00000	6.00000	
	Std. Dev	0.08165	0.10000		0.08165	0.00000		0.08165	0.00000	
	Mean - 3 Sigma	5.75505	5.75000		5.75505	6.00000		5.75505	6.00000	
	Mean + 3 Sigma	6.24495	6.35000		6.24495	6.00000		6.24495	6.00000	
	Limit:		<7ns			<7ns			<7ns	

		D4_Tpd_AV1.8_DV1.8		ns	D5_Tpd_AV1.8_DV1.8		ns	D6_Tpd_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	6.00000	5.90000		6.00000	5.90000		6.00000	5.90000	
	11	5.90000	6.00000		5.90000	5.80000		5.90000	5.80000	
	12	6.00000	6.00000		6.00000	6.00000		6.00000	6.00000	
	13	6.20000	6.20000		6.00000	6.00000		6.00000	6.00000	
	14	6.10000	6.00000		6.10000	6.00000		6.10000	6.00000	
	Min	5.90000	6.00000		5.90000	5.80000		5.90000	5.80000	
	Max	6.20000	6.20000		6.10000	6.00000		6.10000	6.00000	
	Mean	6.05000	6.05000		6.00000	5.95000		6.00000	5.95000	
	Std. Dev	0.12910	0.10000		0.08165	0.10000		0.08165	0.10000	
	Mean - 3 Sigma	5.66270	5.75000		5.75505	5.65000		5.75505	5.65000	
	Mean + 3 Sigma	6.43730	6.35000		6.24495	6.25000		6.24495	6.25000	
	Limit:		<7ns			<7ns			<7ns	

		D7_Tpd_AV1.8_DV1.8		ns	D8_Tpd_AV1.8_DV1.8		ns	D9_Tpd_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	6.00000	6.40000		6.00000	6.30000		6.00000	6.40000	
	11	5.90000	5.80000		5.90000	5.80000		5.90000	6.00000	
	12	6.00000	6.00000		6.00000	6.00000		6.00000	6.00000	
	13	6.00000	6.20000		6.30000	6.20000		6.30000	6.20000	
	14	6.00000	6.00000		6.00000	6.00000		6.00000	6.00000	
	Min	5.90000	5.80000		5.90000	5.80000		5.90000	6.00000	
	Max	6.00000	6.20000		6.30000	6.20000		6.30000	6.20000	
	Mean	5.97500	6.00000		6.05000	6.00000		6.05000	6.05000	
	Std. Dev	0.05000	0.16330		0.17321	0.16330		0.17321	0.10000	
	Mean - 3 Sigma	5.82500	5.51010		5.53038	5.51010		5.53038	5.75000	
	Mean + 3 Sigma	6.12500	6.48990		6.56962	6.48990		6.56962	6.35000	
	Limit:		<7ns			<7ns			<7ns	

		D10_Tpd_AV1.8_DV1.8		ns	D11_Tpd_AV1.8_DV1.8		ns	D12_Tpd_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	6.00000	6.40000		6.00000	6.40000		6.00000	6.40000	
	11	5.90000	6.00000		5.90000	6.00000		5.90000	6.00000	
	12	6.00000	6.00000		6.00000	6.00000		6.00000	6.00000	
	13	6.30000	6.20000		6.30000	6.20000		6.30000	6.20000	
	14	6.00000	6.00000		6.00000	6.30000		6.00000	6.30000	
	Min	5.90000	6.00000		5.90000	6.00000		5.90000	6.00000	
	Max	6.30000	6.20000		6.30000	6.30000		6.30000	6.30000	
	Mean	6.05000	6.05000		6.05000	6.12500		6.05000	6.12500	
	Std. Dev	0.17321	0.10000		0.17321	0.15000		0.17321	0.15000	
	Mean - 3 Sigma	5.53038	5.75000		5.53038	5.67500		5.53038	5.67500	
	Mean + 3 Sigma	6.56962	6.35000		6.56962	6.57500		6.56962	6.57500	
	Limit:		<7ns			<7ns			<7ns	

		D13_Tpd_AV1.8_DV1.8		ns	D0_Ts_8nS_AV1.8_DV1.8		ns	D1_Ts_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	6.00000	6.40000		3.40000	3.50000		3.40000	3.50000	
	11	5.90000	6.00000		3.50000	3.40000		3.50000	3.40000	
	12	6.00000	6.00000		3.50000	3.50000		3.50000	3.50000	
	13	6.30000	6.20000		3.50000	3.50000		3.50000	3.30000	
	14	6.10000	6.30000		3.40000	3.50000		3.40000	3.50000	
	Min	5.90000	6.00000		3.40000	3.40000		3.40000	3.30000	
	Max	6.30000	6.30000		3.50000	3.50000		3.50000	3.50000	
	Mean	6.07500	6.12500		3.47500	3.47500		3.47500	3.42500	
	Std. Dev	0.17078	0.15000		0.05000	0.05000		0.05000	0.09574	
	Mean - 3 Sigma	5.56265	5.67500		3.32500	3.32500		3.32500	3.13777	
	Mean + 3 Sigma	6.58735	6.57500		3.62500	3.62500		3.62500	3.71223	
	Limit:		<7ns			>2ns			>2ns	

		D2_Ts_8nS_AV1.8_DV1.8		ns	D3_Ts_8nS_AV1.8_DV1.8		ns	D4_Ts_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.40000	3.50000		3.40000	3.50000		3.40000	3.50000	
	11	3.50000	3.40000		3.50000	3.40000		3.50000	3.40000	
	12	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	13	3.50000	3.50000		3.50000	3.50000		3.30000	3.30000	
	14	3.40000	3.50000		3.40000	3.50000		3.40000	3.50000	
	Min	3.40000	3.40000		3.40000	3.40000		3.30000	3.30000	
	Max	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	Mean	3.47500	3.47500		3.47500	3.47500		3.42500	3.42500	
	Std. Dev	0.05000	0.05000		0.05000	0.05000		0.09574	0.09574	
	Mean - 3 Sigma	3.32500	3.32500		3.32500	3.32500		3.13777	3.13777	
	Mean + 3 Sigma	3.62500	3.62500		3.62500	3.62500		3.71223	3.71223	
	Limit:		>2ns			>2ns			>2ns	

		D5_Ts_8nS_AV1.8_DV1.8		ns	D6_Ts_8nS_AV1.8_DV1.8		ns	D7_Ts_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.40000	3.50000		3.40000	3.50000		3.40000	3.00000	
	11	3.50000	3.60000		3.50000	3.60000		3.50000	3.60000	
	12	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	13	3.50000	3.50000		3.50000	3.50000		3.50000	3.30000	
	14	3.40000	3.50000		3.40000	3.50000		3.50000	3.50000	
	Min	3.40000	3.50000		3.40000	3.50000		3.50000	3.30000	
	Max	3.50000	3.60000		3.50000	3.60000		3.50000	3.60000	
	Mean	3.47500	3.52500		3.47500	3.52500		3.50000	3.47500	
	Std. Dev	0.05000	0.05000		0.05000	0.05000		0.00000	0.12583	
	Mean - 3 Sigma	3.32500	3.37500		3.32500	3.37500		3.50000	3.09751	
	Mean + 3 Sigma	3.62500	3.67500		3.62500	3.67500		3.50000	3.85249	
	Limit:		>2ns			>2ns			>2ns	

		D8_Ts_8nS_AV1.8_DV1.8		ns	D9_Ts_8nS_AV1.8_DV1.8		ns	D10_Ts_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.40000	3.10000		3.40000	3.00000		3.40000	3.00000	
	11	3.50000	3.60000		3.50000	3.40000		3.50000	3.40000	
	12	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	13	3.20000	3.30000		3.20000	3.30000		3.20000	3.30000	
	14	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	Min	3.20000	3.30000		3.20000	3.30000		3.20000	3.30000	
	Max	3.50000	3.60000		3.50000	3.50000		3.50000	3.50000	
	Mean	3.42500	3.47500		3.42500	3.42500		3.42500	3.42500	
	Std. Dev	0.15000	0.12583		0.15000	0.09574		0.15000	0.09574	
	Mean - 3 Sigma	2.97500	3.09751		2.97500	3.13777		2.97500	3.13777	
	Mean + 3 Sigma	3.87500	3.85249		3.87500	3.71223		3.87500	3.71223	
	Limit:	>2ns			>2ns			>2ns		

		D11_Ts_8nS_AV1.8_DV1.8		ns	D12_Ts_8nS_AV1.8_DV1.8		ns	D13_Ts_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.40000	3.00000		3.40000	3.00000		3.40000	3.00000	
	11	3.50000	3.40000		3.50000	3.40000		3.50000	3.40000	
	12	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	13	3.20000	3.30000		3.20000	3.30000		3.20000	3.30000	
	14	3.50000	3.20000		3.50000	3.20000		3.40000	3.20000	
	Min	3.20000	3.20000		3.20000	3.20000		3.20000	3.20000	
	Max	3.50000	3.50000		3.50000	3.50000		3.50000	3.50000	
	Mean	3.42500	3.35000		3.42500	3.35000		3.40000	3.35000	
	Std. Dev	0.15000	0.12910		0.15000	0.12910		0.14142	0.12910	
	Mean - 3 Sigma	2.97500	2.96270		2.97500	2.96270		2.97574	2.96270	
	Mean + 3 Sigma	3.87500	3.73730		3.87500	3.73730		3.82426	3.73730	
	Limit:	>2ns			>2ns			>2ns		

		D0_Th_8nS_AV1.8_DV1.8		ns	D1_Th_8nSAV1.8_DV1.8		ns	D2_Th_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.60000	4.50000		4.60000	4.50000		4.60000	4.50000	
	11	4.50000	4.60000		4.50000	4.60000		4.50000	4.60000	
	12	4.50000	4.50000		4.50000	4.50000		4.50000	4.50000	
	13	4.50000	4.50000		4.50000	4.70000		4.50000	4.50000	
	14	4.60000	4.50000		4.60000	4.50000		4.60000	4.50000	
	Min	4.50000	4.50000		4.50000	4.50000		4.50000	4.50000	
	Max	4.60000	4.60000		4.60000	4.70000		4.60000	4.60000	
	Mean	4.52500	4.52500		4.52500	4.57500		4.52500	4.52500	
	Std. Dev	0.05000	0.05000		0.05000	0.09574		0.05000	0.05000	
	Mean - 3 Sigma	4.37500	4.37500		4.37500	4.28777		4.37500	4.37500	
	Mean + 3 Sigma	4.67500	4.67500		4.67500	4.86223		4.67500	4.67500	
	Limit:	>3ns			>3ns			>3ns		

		D3_Th_8nS_AV1.8_DV1.8		ns	D4_Th_8nS_AV1.8_DV1.8		ns	D5_Th_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.60000	4.50000		4.60000	4.50000		4.60000	4.50000	
	11	4.50000	4.60000		4.50000	4.60000		4.50000	4.40000	
	12	4.50000	4.50000		4.50000	4.50000		4.50000	4.50000	
	13	4.50000	4.50000		4.70000	4.70000		4.50000	4.50000	
	14	4.60000	4.50000		4.60000	4.50000		4.60000	4.50000	
	Min	4.50000	4.50000		4.50000	4.50000		4.50000	4.40000	
	Max	4.60000	4.60000		4.70000	4.70000		4.60000	4.50000	
	Mean	4.52500	4.52500		4.57500	4.57500		4.52500	4.47500	
	Std. Dev	0.05000	0.05000		0.09574	0.09574		0.05000	0.05000	
	Mean - 3 Sigma	4.37500	4.37500		4.28777	4.28777		4.37500	4.32500	
	Mean + 3 Sigma	4.67500	4.67500		4.86223	4.86223		4.67500	4.62500	
	Limit:	>3ns			>3ns			>3ns		

		D6_Th_8nS_AV1.8_DV1.8		ns	D7_Th_8nS_AV1.8_DV1.8		ns	D8_Th_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.60000	4.50000		4.60000	5.00000		4.60000	4.90000	
	11	4.50000	4.40000		4.50000	4.40000		4.50000	4.40000	
	12	4.50000	4.50000		4.50000	4.50000		4.50000	4.50000	
	13	4.50000	4.50000		4.50000	4.70000		4.80000	4.70000	
	14	4.60000	4.50000		4.50000	4.50000		4.50000	4.50000	
	Min	4.50000	4.40000		4.50000	4.40000		4.50000	4.40000	
	Max	4.60000	4.50000		4.50000	4.70000		4.80000	4.70000	
	Mean	4.52500	4.47500		4.50000	4.52500		4.57500	4.52500	
	Std. Dev	0.05000	0.05000		0.00000	0.12583		0.15000	0.12583	
	Mean - 3 Sigma	4.37500	4.32500		4.50000	4.14751		4.12500	4.14751	
	Mean + 3 Sigma	4.67500	4.62500		4.50000	4.90249		5.02500	4.90249	
	Limit:	>3ns			>3ns			>3ns		

		D9_Th_8nS_AV1.8_DV1.8		ns	D10_Th_8nS_AV1.8_DV1.8		ns	D11_Th_8nS_AV1.8_DV1.8		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.60000	5.00000		4.60000	5.00000		4.60000	5.00000	
	11	4.50000	4.60000		4.50000	4.60000		4.50000	4.60000	
	12	4.50000	4.50000		4.50000	4.50000		4.50000	4.50000	
	13	4.80000	4.70000		4.80000	4.70000		4.80000	4.70000	
	14	4.50000	4.50000		4.50000	4.50000		4.50000	4.80000	
	Min	4.50000	4.50000		4.50000	4.50000		4.50000	4.50000	
	Max	4.80000	4.70000		4.80000	4.70000		4.80000	4.80000	
	Mean	4.57500	4.57500		4.57500	4.57500		4.57500	4.65000	
	Std. Dev	0.15000	0.09574		0.15000	0.09574		0.15000	0.12910	
	Mean - 3 Sigma	4.12500	4.28777		4.12500	4.28777		4.12500	4.26270	
	Mean + 3 Sigma	5.02500	4.86223		5.02500	4.86223		5.02500	5.03730	
	Limit:		>3ns			>3ns			>3ns	

		D12_Th_8nS_AV1.8_DV1.8		ns	D13_Th_8nS_AV1.8_DV1.8		ns	SPI_minPeriod_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.60000	5.00000		4.60000	5.00000		40.00000	40.00000	
	11	4.50000	4.60000		4.50000	4.60000		40.00000	40.00000	
	12	4.50000	4.50000		4.50000	4.50000		40.00000	40.00000	
	13	4.80000	4.70000		4.80000	4.70000		40.00000	40.00000	
	14	4.50000	4.80000		4.60000	4.80000		40.00000	40.00000	
	Min	4.50000	4.50000		4.50000	4.50000		40.00000	40.00000	
	Max	4.80000	4.80000		4.80000	4.80000		40.00000	40.00000	
	Mean	4.57500	4.65000		4.60000	4.65000		40.00000	40.00000	
	Std. Dev	0.15000	0.12910		0.14142	0.12910		0.00000	0.00000	
	Mean - 3 Sigma	4.12500	4.26270		4.17574	4.26270		40.00000	40.00000	
	Mean + 3 Sigma	5.02500	5.03730		5.02426	5.03730		40.00000	40.00000	
	Limit:		>3ns			>3ns			>40ns	

		CLK_tHi_AV1.8_DV3.3		ns	CLK_tLo_AV1.8_DV3.3		ns	SDIO_setup_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	2.25000	2.25000		7.75000	8.00000		0.00000	0.00000	
	11	2.25000	2.25000		8.00000	8.00000		0.00000	0.00000	
	12	2.25000	2.25000		7.75000	7.75000		-0.25000	-0.25000	
	13	2.50000	2.50000		7.50000	7.50000		-0.25000	-0.25000	
	14	2.25000	2.25000		7.75000	7.75000		-0.25000	-0.25000	
	Min	2.25000	2.25000		7.50000	7.50000		-0.25000	-0.25000	
	Max	2.50000	2.50000		8.00000	8.00000		0.00000	0.00000	
	Mean	2.31250	2.31250		7.75000	7.75000		-0.18750	-0.18750	
	Std. Dev	0.12500	0.12500		0.20412	0.20412		0.12500	0.12500	
	Mean - 3 Sigma	1.93750	1.93750		7.13763	7.13763		-0.56250	-0.56250	
	Mean + 3 Sigma	2.68750	2.68750		8.36237	8.36237		0.18750	0.18750	
	Limit:		<16ns			<16ns			<5ns	

		SDIO_hold_AV1.8_DV3.3		ns	CSB_setup_AV1.8_DV3.3		ns	CSB_hold_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	1.00000	1.00000		0.00000	0.00000		0.50000	0.50000	
	11	1.00000	1.00000		0.00000	0.00000		0.50000	0.50000	
	12	1.00000	1.00000		0.00000	0.00000		0.50000	0.50000	
	13	1.00000	1.00000		0.00000	0.00000		0.50000	0.50000	
	14	1.00000	1.00000		0.00000	0.00000		0.50000	0.75000	
	Min	1.00000	1.00000		0.00000	0.00000		0.50000	0.50000	
	Max	1.00000	1.00000		0.00000	0.00000		0.50000	0.75000	
	Mean	1.00000	1.00000		0.00000	0.00000		0.50000	0.56250	
	Std. Dev	0.00000	0.00000		0.00000	0.00000		0.00000	0.12500	
	Mean - 3 Sigma	1.00000	1.00000		0.00000	0.00000		0.50000	0.18750	
	Mean + 3 Sigma	1.00000	1.00000		0.00000	0.00000		0.50000	0.93750	
	Limit:		<3ns			<5ns			<3ns	

		DCO_Tpd_AV1.8_DV3.3		ns	D0_Tpd_AV1.8_DV3.3		ns	D1_Tpd_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.60000	4.60000		5.00000	5.00000		4.90000	5.00000	
	11	4.60000	4.60000		5.00000	5.00000		5.00000	5.00000	
	12	4.70000	4.70000		5.00000	5.00000		5.00000	5.00000	
	13	4.70000	4.70000		5.10000	5.10000		5.00000	5.10000	
	14	4.70000	4.70000		5.20000	5.10000		5.00000	5.10000	
	Min	4.60000	4.60000		5.00000	5.00000		5.00000	5.00000	
	Max	4.70000	4.70000		5.20000	5.10000		5.00000	5.10000	
	Mean	4.67500	4.67500		5.07500	5.05000		5.00000	5.05000	
	Std. Dev	0.05000	0.05000		0.09574	0.05774		0.00000	0.05774	
	Mean - 3 Sigma	4.52500	4.52500		4.78777	4.87679		5.00000	4.87679	
	Mean + 3 Sigma	4.82500	4.82500		5.36223	5.22321		5.00000	5.22321	
	Limit:		<6.5ns			<7ns			<7ns	

		D2_Tpd_AV1.8_DV3.3		ns	D3_Tpd_AV1.8_DV3.3		ns	D4_Tpd_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	11	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	12	5.10000	5.10000		5.10000	5.10000		5.10000	5.10000	
	13	5.10000	5.10000		5.10000	5.10000		5.30000	5.10000	
	14	5.20000	5.10000		5.20000	5.10000		5.20000	5.10000	
	Min	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	Max	5.20000	5.10000		5.20000	5.10000		5.30000	5.10000	
	Mean	5.10000	5.07500		5.10000	5.07500		5.15000	5.07500	
	Std. Dev	0.08165	0.05000		0.08165	0.05000		0.12910	0.05000	
	Mean - 3 Sigma	4.85505	4.92500		4.85505	4.92500		4.76270	4.92500	
	Mean + 3 Sigma	5.34495	5.22500		5.34495	5.22500		5.53730	5.22500	
	Limit:	<7ns			<7ns			<7ns		

		D5_Tpd_AV1.8_DV3.3		ns	D6_Tpd_AV1.8_DV3.3		ns	D7_Tpd_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	11	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	12	5.10000	5.00000		5.10000	5.00000		5.00000	5.20000	
	13	5.10000	5.10000		5.30000	5.10000		5.10000	5.30000	
	14	5.20000	5.10000		5.20000	5.10000		5.20000	5.10000	
	Min	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	Max	5.20000	5.10000		5.30000	5.10000		5.20000	5.30000	
	Mean	5.10000	5.05000		5.15000	5.05000		5.07500	5.15000	
	Std. Dev	0.08165	0.05774		0.12910	0.05774		0.09574	0.12910	
	Mean - 3 Sigma	4.85505	4.87679		4.76270	4.87679		4.78777	4.76270	
	Mean + 3 Sigma	5.34495	5.22321		5.53730	5.22321		5.36223	5.53730	
	Limit:	<7ns			<7ns			<7ns		

		D8_Tpd_AV1.8_DV3.3		ns	D9_Tpd_AV1.8_DV3.3		ns	D10_Tpd_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	11	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	12	5.10000	5.20000		5.10000	5.20000		5.10000	5.20000	
	13	5.30000	5.10000		5.30000	5.30000		5.30000	5.30000	
	14	5.20000	5.10000		5.20000	5.30000		5.20000	5.10000	
	Min	5.00000	5.00000		5.00000	5.00000		5.00000	5.00000	
	Max	5.30000	5.20000		5.30000	5.30000		5.30000	5.30000	
	Mean	5.15000	5.10000		5.15000	5.20000		5.15000	5.15000	
	Std. Dev	0.12910	0.08165		0.12910	0.14142		0.12910	0.12910	
	Mean - 3 Sigma	4.76270	4.85505		4.76270	4.77574		4.76270	4.76270	
	Mean + 3 Sigma	5.53730	5.34495		5.53730	5.62426		5.53730	5.53730	
	Limit:	<7ns			<7ns			<7ns		

		D11_Tpd_AV1.8_DV3.3		ns	D12_Tpd_AV1.8_DV3.3		ns	D13_Tpd_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	5.000	5.000		5.000	5.000		5.000	5.000	
	11	5.000	5.000		5.000	5.000		5.000	5.000	
	12	5.100	5.200		5.100	5.200		5.100	5.200	
	13	5.300	5.300		5.300	5.300		5.300	5.300	
	14	5.200	5.100		5.200	5.100		5.200	5.100	
	Min	5.000	5.000		5.000	5.000		5.000	5.000	
	Max	5.300	5.300		5.300	5.300		5.300	5.300	
	Mean	5.150	5.150		5.150	5.150		5.150	5.150	
	Std. Dev	0.129	0.129		0.129	0.129		0.129	0.129	
	Mean - 3 Sigma	4.763	4.763		4.763	4.763		4.763	4.763	
	Mean + 3 Sigma	5.537	5.537		5.537	5.537		5.537	5.537	
	Limit:	<7ns			<7ns			<7ns		

		D0_Ts_8nS_AV1.8_DV3.3		ns	D1_Ts_8nS_AV1.8_DV3.3		ns	D2_Ts_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.600	3.600		3.700	3.600		3.600	3.600	
	11	3.600	3.600		3.600	3.600		3.600	3.600	
	12	3.700	3.700		3.700	3.700		3.600	3.600	
	13	3.600	3.600		3.700	3.600		3.600	3.600	
	14	3.500	3.600		3.700	3.600		3.500	3.600	
	Min	3.500	3.600		3.600	3.600		3.500	3.600	
	Max	3.700	3.700		3.700	3.700		3.600	3.600	
	Mean	3.600	3.625		3.675	3.625		3.575	3.600	
	Std. Dev	0.082	0.050		0.050	0.050		0.050	0.000	
	Mean - 3 Sigma	3.355	3.475		3.525	3.475		3.425	3.600	
	Mean + 3 Sigma	3.845	3.775		3.825	3.775		3.725	3.600	
	Limit:	>2ns			>2ns			>2ns		

		D3_Ts_8nS_AV1.8_DV3.3		ns	D4_Ts_8nS_AV1.8_DV3.3		ns	D5_Ts_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.600	3.600		3.600	3.600		3.600	3.600	
	11	3.600	3.600		3.600	3.600		3.600	3.600	
	12	3.600	3.600		3.600	3.600		3.600	3.700	
	13	3.600	3.600		3.400	3.600		3.600	3.600	
	14	3.500	3.600		3.500	3.600		3.500	3.600	
	Min	3.500	3.600		3.400	3.600		3.500	3.600	
	Max	3.600	3.600		3.600	3.600		3.600	3.700	
	Mean	3.575	3.600		3.525	3.600		3.575	3.625	
	Std. Dev	0.050	0.000		0.096	0.000		0.050	0.050	
	Mean - 3 Sigma	3.425	3.600		3.238	3.600		3.425	3.475	
	Mean + 3 Sigma	3.725	3.600		3.812	3.600		3.725	3.775	
	Limit:	>2ns			>2ns			<0.05V		

		D6_Ts_8nS_AV1.8_DV3.3		ns	D7_Ts_8nS_AV1.8_DV3.3		ns	D8_Ts_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.600	3.600		3.600	3.600		3.600	3.600	
	11	3.600	3.600		3.600	3.600		3.600	3.600	
	12	3.600	3.700		3.700	3.700		3.600	3.500	
	13	3.400	3.600		3.600	3.600		3.400	3.600	
	14	3.500	3.600		3.500	3.600		3.500	3.600	
	Min	3.400	3.600		3.500	3.600		3.400	3.500	
	Max	3.600	3.700		3.700	3.700		3.600	3.600	
	Mean	3.525	3.625		3.600	3.625		3.525	3.575	
	Std. Dev	0.096	0.050		0.082	0.050		0.096	0.050	
	Mean - 3 Sigma	3.238	3.475		3.355	3.475		3.238	3.425	
	Mean + 3 Sigma	3.812	3.775		3.845	3.775		3.812	3.725	
	Limit:	>2ns			>2ns			>2ns		

		D9_Ts_8nS_AV1.8_DV3.3		ns	D10_Ts_8nS_AV1.8_DV3.3		ns	D11_Ts_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.600	3.600		3.600	3.600		3.600	3.600	
	11	3.600	3.600		3.600	3.600		3.600	3.600	
	12	3.600	3.500		3.600	3.500		3.600	3.500	
	13	3.400	3.400		3.400	3.400		3.400	3.400	
	14	3.500	3.400		3.500	3.600		3.500	3.600	
	Min	3.400	3.400		3.400	3.400		3.400	3.400	
	Max	3.600	3.600		3.600	3.600		3.600	3.600	
	Mean	3.525	3.475		3.525	3.525		3.525	3.525	
	Std. Dev	0.096	0.096		0.096	0.096		0.096	0.096	
	Mean - 3 Sigma	3.238	3.188		3.238	3.238		3.238	3.238	
	Mean + 3 Sigma	3.812	3.762		3.812	3.812		3.812	3.812	
	Limit:	>2ns			>2ns			>2ns		

		D12_Ts_8nS_AV1.8_DV3.3		ns	D13_Ts_8nS_AV1.8_DV3.3		ns	D0_Th_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.600	3.600		3.600	3.600		4.400	4.400	
	11	3.600	3.600		3.600	3.600		4.400	4.400	
	12	3.600	3.500		3.600	3.500		4.300	4.300	
	13	3.400	3.400		3.400	3.400		4.400	4.400	
	14	3.500	3.600		3.500	3.600		4.500	4.400	
	Min	3.400	3.400		3.400	3.400		4.300	4.300	
	Max	3.600	3.600		3.600	3.600		4.500	4.400	
	Mean	3.525	3.525		3.525	3.525		4.400	4.375	
	Std. Dev	0.096	0.096		0.096	0.096		0.082	0.050	
	Mean - 3 Sigma	3.238	3.238		3.238	3.238		4.155	4.225	
	Mean + 3 Sigma	3.812	3.812		3.812	3.812		4.645	4.525	
	Limit:	>2ns			>2ns			>3ns		

		D1_Th_8nSAV1.8_DV3.3		ns	D2_Th_8nS_AV1.8_DV3.3		ns	D3_Th_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.300	4.400		4.400	4.400		4.400	4.400	
	11	4.400	4.400		4.400	4.400		4.400	4.400	
	12	4.300	4.300		4.400	4.400		4.400	4.400	
	13	4.300	4.400		4.400	4.400		4.400	4.400	
	14	4.300	4.400		4.500	4.400		4.500	4.400	
	Min	4.300	4.300		4.400	4.400		4.400	4.400	
	Max	4.400	4.400		4.500	4.400		4.500	4.400	
	Mean	4.325	4.375		4.425	4.400		4.425	4.400	
	Std. Dev	0.050	0.050		0.050	0.000		0.050	0.000	
	Mean - 3 Sigma	4.175	4.225		4.275	4.400		4.275	4.400	
	Mean + 3 Sigma	4.475	4.525		4.575	4.400		4.575	4.400	
	Limit:	>3ns			>3ns			>3ns		



		D4_Th_8nS_AV1.8_DV3.3		ns	D5_Th_8nS_AV1.8_DV3.3		ns	D6_Th_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.400	4.400		4.400	4.400		4.400	4.400	
	11	4.400	4.400		4.400	4.400		4.400	4.400	
	12	4.400	4.400		4.400	4.300		4.400	4.300	
	13	4.600	4.400		4.400	4.400		4.600	4.400	
	14	4.500	4.400		4.500	4.400		4.500	4.400	
	Min	4.400	4.400		4.400	4.300		4.400	4.300	
	Max	4.600	4.400		4.500	4.400		4.600	4.400	
	Mean	4.475	4.400		4.425	4.375		4.475	4.375	
	Std. Dev	0.096	0.000		0.050	0.050		0.096	0.050	
	Mean - 3 Sigma	4.188	4.400		4.275	4.225		4.188	4.225	
	Mean + 3 Sigma	4.762	4.400		4.575	4.525		4.762	4.525	
	Limit:	>3ns			>3ns			>3ns		

		D7_Th_8nS_AV1.8_DV3.3		ns	D8_Th_8nS_AV1.8_DV3.3		ns	D9_Th_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.400	4.400		4.400	4.400		4.400	4.400	
	11	4.400	4.400		4.400	4.400		4.400	4.400	
	12	4.300	4.300		4.400	4.500		4.400	4.500	
	13	4.400	4.400		4.600	4.400		4.600	4.600	
	14	4.500	4.400		4.500	4.400		4.500	4.600	
	Min	4.300	4.300		4.400	4.400		4.400	4.400	
	Max	4.500	4.400		4.600	4.500		4.600	4.600	
	Mean	4.400	4.375		4.475	4.425		4.475	4.525	
	Std. Dev	0.082	0.050		0.096	0.050		0.096	0.096	
	Mean - 3 Sigma	4.155	4.225		4.188	4.275		4.188	4.238	
	Mean + 3 Sigma	4.645	4.525		4.762	4.575		4.762	4.812	
	Limit:	>3ns			>3ns			>3ns		

		D10_Th_8nS_AV1.8_DV3.3		ns	D11_Th_8nS_AV1.8_DV3.3		ns	D12_Th_8nS_AV1.8_DV3.3		ns
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.400	4.400		4.400	4.400		4.400	4.400	
	11	4.400	4.400		4.400	4.400		4.400	4.400	
	12	4.400	4.500		4.400	4.500		4.400	4.500	
	13	4.600	4.600		4.600	4.600		4.600	4.600	
	14	4.500	4.400		4.500	4.400		4.500	4.400	
	Min	4.400	4.400		4.400	4.400		4.400	4.400	
	Max	4.600	4.600		4.600	4.600		4.600	4.600	
	Mean	4.475	4.475		4.475	4.475		4.475	4.475	
	Std. Dev	0.096	0.096		0.096	0.096		0.096	0.096	
	Mean - 3 Sigma	4.188	4.188		4.188	4.188		4.188	4.188	
	Mean + 3 Sigma	4.762	4.762		4.762	4.762		4.762	4.762	
	Limit:	>3ns			>3ns			>3ns		

		D13_Th_8nS_AV1.8_DV3.3		ns	Verror_SnsGnd_1.00v_AV1.8_DV2.5		mV	LoadReg_1mA_SnsGnd_1.00v_AV1.8_DV2.5		mV
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	4.400	4.400		-4.016	1.003		-3.556	-3.605	
	11	4.400	4.400		-1.662	1.001		-3.350	-3.389	
	12	4.400	4.500		-12.442	1.013		-3.369	-3.323	
	13	4.600	4.600		-1.491	1.001		-3.500	-4.674	
	14	4.500	4.400		-4.725	1.005		-3.406	-3.364	
	Min	4.400	4.400		-12.442	1.001		-3.500	-4.674	
	Max	4.600	4.600		-1.491	1.013		-3.350	-3.323	
	Mean	4.475	4.475		-5.080	1.005		-3.406	-3.688	
	Std. Dev	0.096	0.096		5.128	0.005		0.067	0.658	
	Mean - 3 Sigma	4.188	4.188		-20.464	0.989		-3.606	-5.662	
	Mean + 3 Sigma	4.762	4.762		10.304	1.021		-3.206	-1.713	
	Limit:	>3ns			+/- 35mV			+/- 13 mV		

		GainError_AV1.8_DV2.5		%FSR	OffsetError_AV1.8_DV2.5		%FSR	GainError_AV1.8_DV1.8		%FSR
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	1.005	0.930		-1.197	-1.194		1.008	0.944	
	11	1.495	1.486		-0.729	-0.747		1.502	1.467	
	12	1.228	1.179		-1.045	-1.056		1.232	1.188	
	13	0.981	0.969		-1.254	-1.282		0.993	0.971	
	14	1.600	1.597		-1.188	-1.192		1.610	1.590	
	Min	0.981	0.969		-1.254	-1.282		0.993	0.971	
	Max	1.600	1.597		-0.729	-0.747		1.610	1.590	
	Mean	1.326	1.307		-1.054	-1.069		1.334	1.304	
	Std. Dev	0.278	0.287		0.234	0.234		0.277	0.279	
	Mean - 3 Sigma	0.491	0.447		-1.755	-1.771		0.502	0.467	
	Mean + 3 Sigma	2.161	2.168		-0.353	-0.366		2.166	2.141	
	Limit:	+/-5 %FSR			+/-2.5 %FSR			+/-5 %FSR		

		OffsetError_AV1.8_DV1.8		%FSR	GainError_AV1.8_DV3.3		%FSR	OffsetError_AV1.8_DV3.3		%FSR	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-1.194	-1.201		0.994	0.935		-1.199	-1.199		
	11	-0.730	-0.745		1.498	1.472		-0.730	-0.750		
	12	-1.041	-1.061		1.228	1.181		-1.039	-1.067		
	13	-1.252	-1.276		0.987	0.979		-1.251	-1.291		
	14	-1.186	-1.197		1.608	1.591		-1.185	-1.195		
	Min	-1.252	-1.276		0.987	0.979		-1.251	-1.291		
	Max	-0.730	-0.745		1.608	1.591		-0.730	-0.750		
	Mean	-1.052	-1.070		1.330	1.306		-1.051	-1.076		
	Std. Dev	0.232	0.234		0.279	0.278		0.232	0.236		
	Mean - 3 Sigma	-1.748	-1.772		0.493	0.472		-1.747	-1.783		
	Mean + 3 Sigma	-0.356	-0.367		2.168	2.139		-0.356	-0.368		
				+/-2.5 %FSR					+/-5 %FSR		

		GainError_AV1.7_DV2.5		%FSR	OffsetError_AV1.7_DV2.5		%FSR	GainError_AV1.7_DV1.8		%FSR	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.994	0.913		-1.134	-1.129		0.980	0.932		
	11	1.466	1.439		-0.654	-0.674		1.483	1.446		
	12	1.237	1.182		-0.978	-0.989		1.227	1.175		
	13	0.983	0.975		-1.192	-1.224		0.966	0.986		
	14	1.621	1.574		-1.122	-1.129		1.609	1.581		
	Min	0.983	0.975		-1.192	-1.224		0.966	0.986		
	Max	1.621	1.574		-0.654	-0.674		1.609	1.581		
	Mean	1.327	1.293		-0.986	-1.004		1.321	1.297		
	Std. Dev	0.278	0.267		0.239	0.241		0.285	0.267		
	Mean - 3 Sigma	0.493	0.493		-1.703	-1.726		0.465	0.495		
	Mean + 3 Sigma	2.160	2.093		-0.270	-0.282		2.177	2.098		
				+/-2.5 %FSR					+/-2.5 %FSR		

		OffsetError_AV1.7_DV1.8		%FSR	GainError_AV1.7_DV3.3		%FSR	OffsetError_AV1.7_DV3.3		%FSR	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-1.129	-1.133		0.982	0.928		-1.136	-1.128		
	11	-0.655	-0.671		1.478	1.458		-0.660	-0.677		
	12	-0.977	-0.991		1.237	1.190		-0.976	-0.987		
	13	-1.192	-1.222		0.979	0.973		-1.196	-1.227		
	14	-1.122	-1.132		1.608	1.580		-1.119	-1.131		
	Min	-1.192	-1.222		0.979	0.973		-1.196	-1.227		
	Max	-0.655	-0.671		1.608	1.580		-0.660	-0.677		
	Mean	-0.987	-1.004		1.326	1.301		-0.988	-1.005		
	Std. Dev	0.238	0.241		0.277	0.272		0.237	0.241		
	Mean - 3 Sigma	-1.701	-1.728		0.494	0.484		-1.698	-1.727		
	Mean + 3 Sigma	-0.272	-0.280		2.157	2.117		-0.278	-0.284		
				+/-2.5 %FSR					+/-5 %FSR		

		GainError_AV1.9_DV2.5		%FSR	OffsetError_AV1.9_DV2.5		%FSR	GainError_AV1.9_DV1.8		%FSR	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.037	0.972		-1.277	-1.277		1.040	0.967		
	11	1.541	1.501		-0.804	-0.824		1.536	1.510		
	12	1.254	1.205		-1.115	-1.133		1.256	1.215		
	13	1.012	1.001		-1.322	-1.354		1.006	1.004		
	14	1.624	1.589		-1.265	-1.277		1.624	1.584		
	Min	1.012	1.001		-1.322	-1.354		1.006	1.004		
	Max	1.624	1.589		-0.804	-0.824		1.624	1.584		
	Mean	1.358	1.324		-1.126	-1.147		1.355	1.328		
	Std. Dev	0.280	0.271		0.232	0.234		0.281	0.269		
	Mean - 3 Sigma	0.519	0.511		-1.822	-1.850		0.513	0.522		
	Mean + 3 Sigma	2.196	2.137		-0.430	-0.444		2.197	2.134		
				+/-5 %FSR					+/-2.5 %FSR		

		OffsetError_AV1.9_DV1.8		%FSR	GainError_AV1.9_DV3.3		%FSR	OffsetError_AV1.9_DV3.3		%FSR	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-1.279	-1.276		1.039	0.983		-1.279	-1.273		
	11	-0.802	-0.829		1.534	1.517		-0.802	-0.825		
	12	-1.118	-1.125		1.252	1.200		-1.120	-1.128		
	13	-1.317	-1.347		1.014	1.014		-1.317	-1.349		
	14	-1.266	-1.279		1.613	1.593		-1.265	-1.272		
	Min	-1.317	-1.347		1.014	1.014		-1.317	-1.349		
	Max	-0.802	-0.829		1.613	1.593		-0.802	-0.825		
	Mean	-1.126	-1.145		1.353	1.331		-1.126	-1.144		
	Std. Dev	0.232	0.230		0.274	0.271		0.231	0.231		
	Mean - 3 Sigma	-1.820	-1.835		0.531	0.518		-1.821	-1.837		
	Mean + 3 Sigma	-0.431	-0.455		2.175	2.144		-0.432	-0.451		
				+/-2.5 %FSR					+/-5 %FSR		

		SNR_AV1.8_DV2.5		dBc	SINAD_AV1.8_DV2.5		dBc	ENOB_AV1.8_DV2.5		Bits
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	68.526	68.911		68.417	68.762		11.073	11.130	
	11	68.030	68.250		67.815	68.014		10.973	11.006	
	12	68.255	68.712		68.108	68.533		11.021	11.092	
	13	68.376	68.822		68.307	68.729		11.054	11.124	
	14	68.465	68.745		68.269	68.553		11.048	11.095	
	Min	68.030	68.250		67.815	68.014		10.973	11.006	
	Max	68.465	68.822		68.307	68.729		11.054	11.124	
	Mean	68.281	68.632		68.125	68.457		11.024	11.079	
	Std. Dev	0.188	0.259		0.224	0.308		0.037	0.051	
	Mean - 3 Sigma	67.717	67.856		67.453	67.533		10.912	10.926	
	Mean + 3 Sigma	68.846	69.409		68.797	69.382		11.136	11.233	
				>66 dBc			>67 dBc			>10.8 Bits

		HD2_AV1.8_DV2.5		dBc	HD3_AV1.8_DV2.5		dBc	SFDR_AV1.8_DV2.5		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-110.648	-101.451		-85.752	-84.625		85.752	84.625	
	11	-83.581	-83.339		-85.761	-85.741		83.581	83.339	
	12	-86.147	-85.797		-87.318	-86.505		86.147	85.797	
	13	-106.566	-99.628		-87.712	-86.708		87.712	86.708	
	14	-84.202	-85.012		-88.117	-87.594		84.202	85.012	
	Min	-106.566	-99.628		-88.117	-87.594		83.581	83.339	
	Max	-83.581	-83.339		-85.761	-85.741		87.712	86.708	
	Mean	-90.124	-88.444		-87.227	-86.637		85.411	85.214	
	Std. Dev	11.016	7.526		1.030	0.762		1.884	1.429	
	Mean - 3 Sigma	-123.171	-111.023		-90.318	-88.922		79.758	80.926	
	Mean + 3 Sigma	-57.077	-65.866		-84.137	-84.352		91.063	89.502	
				<-72 dBc			<-72 dBc			>72 dBc

		woSpur_AV1.8_DV2.5		dBc	SNR_AV1.7_DV1.8		dBc	SINAD_AV1.7_DV1.8		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-90.124	-92.543		68.767	69.388		68.647	69.235	
	11	-85.064	-83.831		68.404	68.753		68.203	68.521	
	12	-87.783	-92.625		68.718	69.174		68.574	69.008	
	13	-92.444	-91.298		68.645	69.255		68.554	69.157	
	14	-88.513	-93.222		68.697	69.235		68.484	69.011	
	Min	-92.444	-93.222		68.404	68.753		68.203	68.521	
	Max	-85.064	-83.831		68.718	69.255		68.574	69.157	
	Mean	-88.451	-90.244		68.616	69.104		68.453	68.924	
	Std. Dev	3.048	4.350		0.145	0.237		0.171	0.278	
	Mean - 3 Sigma	-97.594	-103.295		68.182	68.395		67.939	68.091	
	Mean + 3 Sigma	-79.308	-77.194		69.050	69.814		68.968	69.758	
				<-75 dBc			>66 dBc			>67 dBc

		ENOB_AV1.7_DV1.8		Bits	HD2_AV1.7_DV1.8		dBc	HD3_AV1.7_DV1.8		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	11.111	11.208		-104.503	-97.734		-85.279	-84.823	
	11	11.037	11.090		-83.496	-83.266		-87.748	-87.102	
	12	11.099	11.171		-87.733	-87.409		-87.228	-87.125	
	13	11.095	11.196		-98.366	-96.468		-86.914	-87.366	
	14	11.084	11.171		-84.092	-84.813		-88.039	-87.571	
	Min	11.037	11.090		-98.366	-96.468		-88.039	-87.571	
	Max	11.099	11.196		-83.496	-83.266		-86.914	-87.102	
	Mean	11.079	11.157		-88.422	-87.989		-87.482	-87.291	
	Std. Dev	0.028	0.046		6.889	5.905		0.506	0.221	
	Mean - 3 Sigma	10.993	11.018		-109.089	-105.705		-89.000	-87.956	
	Mean + 3 Sigma	11.164	11.295		-67.754	-70.273		-85.964	-86.627	
				>10.8			<-72 dBc			<-72 dBc

		SFDR_AV1.7_DV1.8		dBc	woSpur_AV1.7_DV1.8		dBc	SNR_AV1.7_DV3.3		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	85.279	84.823		-89.972	-92.601		68.315	68.623	
	11	83.496	83.266		-85.615	-85.374		67.828	67.910	
	12	87.228	87.125		-88.520	-92.023		67.939	68.300	
	13	86.914	87.366		-93.166	-92.441		68.342	68.416	
	14	84.092	84.813		-87.947	-94.255		68.272	68.320	
	Min	83.496	83.266		-93.166	-94.255		67.828	67.910	
	Max	87.228	87.366		-85.615	-85.374		68.342	68.416	
	Mean	85.432	85.643		-88.812	-91.023		68.095	68.237	
	Std. Dev	1.912	1.958		3.163	3.889		0.250	0.224	
	Mean - 3 Sigma	79.696	79.768		-98.301	-102.690		67.344	67.566	
	Mean + 3 Sigma	91.169	91.517		-79.323	-79.356		68.846	68.908	
				>72 dBc			<-75 dBc			>66 dBc

		SINAD_AV1.7_DV3.3		dBc	ENOB_AV1.7_DV3.3		Bits	HD2_AV1.7_DV3.3		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	68.199	68.472		11.036	11.082		-104.066	-98.452	
	11	67.614	67.678		10.939	10.950		-82.764	-82.414	
	12	67.800	68.136		10.970	11.026		-87.237	-86.568	
	13	68.246	68.320		11.044	11.056		-100.228	-97.800	
	14	68.077	68.127		11.016	11.024		-84.382	-84.910	
	Min	67.614	67.678		10.939	10.950		-100.228	-97.800	
	Max	68.246	68.320		11.044	11.056		-82.764	-82.414	
	Mean	67.934	68.065		10.992	11.014		-88.653	-87.923	
	Std. Dev	0.282	0.273		0.047	0.045		7.935	6.803	
	Mean - 3 Sigma	67.089	67.247		10.852	10.878		-112.459	-108.331	
	Mean + 3 Sigma	68.780	68.884		11.133	11.150		-64.846	-67.515	
				>67 dBc			>10.8			<-72 dBc

		HD3_AV1.7_DV3.3		dBc	SFDR_AV1.7_DV3.3		dBc	woSpur_AV1.7_DV3.3		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-85.176	-84.209		85.176	84.209		-90.028	-91.914	
	11	-86.876	-86.630		82.764	82.414		-83.861	-83.241	
	12	-86.579	-86.302		86.579	86.302		-88.375	-91.269	
	13	-86.367	-86.341		86.367	86.341		-93.263	-92.793	
	14	-87.485	-86.762		84.382	84.910		-88.260	-92.633	
	Min	-87.485	-86.762		82.764	82.414		-93.263	-92.793	
	Max	-86.367	-86.302		86.579	86.341		-83.861	-83.241	
	Mean	-86.826	-86.509		85.023	84.992		-88.440	-89.984	
	Std. Dev	0.486	0.223		1.802	1.843		3.841	4.547	
	Mean - 3 Sigma	-88.284	-87.179		79.617	79.462		-99.964	-103.625	
	Mean + 3 Sigma	-85.368	-85.839		90.428	90.521		-76.917	-76.343	
				<-72 dBc			>72 dBc			<-75 dBc

		SNR_AV1.9_DV1.8		dBc	SINAD_AV1.9_DV1.8		dBc	ENOB_AV1.9_DV1.8		Bits
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	68.669	69.129		68.458	68.876		11.079	11.149	
	11	68.284	68.476		68.011	68.166		11.005	11.031	
	12	68.575	68.994		68.386	68.758		11.068	11.129	
	13	68.575	69.099		68.437	68.928		11.076	11.158	
	14	68.783	69.062		68.474	68.740		11.082	11.126	
	Min	68.284	68.476		68.011	68.166		11.005	11.031	
	Max	68.783	69.099		68.474	68.928		11.082	11.158	
	Mean	68.554	68.908		68.327	68.648		11.058	11.111	
	Std. Dev	0.205	0.291		0.214	0.332		0.035	0.055	
	Mean - 3 Sigma	67.940	68.035		67.686	67.651		10.951	10.945	
	Mean + 3 Sigma	69.169	69.781		68.968	69.645		11.164	11.277	
				>66 dBc			>67 dBc			>10.8

		HD2_AV1.9_DV1.8		dBc	HD3_AV1.9_DV1.8		dBc	SFDR_AV1.9_DV1.8		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-99.817	-95.820		-82.798	-82.336		82.798	82.336	
	11	-84.275	-83.639		-83.163	-83.019		83.163	83.019	
	12	-86.018	-85.005		-85.172	-84.815		85.172	84.815	
	13	-106.392	-101.882		-84.169	-83.717		84.169	83.717	
	14	-82.964	-83.593		-84.503	-84.073		82.964	83.593	
	Min	-106.392	-101.882		-85.172	-84.815		82.964	83.019	
	Max	-82.964	-83.593		-83.163	-83.019		85.172	84.815	
	Mean	-89.912	-88.530		-84.252	-83.906		83.867	83.786	
	Std. Dev	11.057	8.926		0.837	0.748		1.018	0.750	
	Mean - 3 Sigma	-123.084	-115.307		-86.764	-86.148		80.814	81.535	
	Mean + 3 Sigma	-56.740	-61.753		-81.740	-81.663		86.920	86.037	
				<-72 dBc			<-72 dBc			>72 dBc

		woSpur_AV1.9_DV1.8		dBc	SNR_AV1.9_DV3.3		dBc	SINAD_AV1.9_DV3.3		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-90.526	-93.053		68.055	68.416		67.874	68.203	
	11	-83.520	-83.226		67.633	67.642		67.389	67.375	
	12	-87.756	-93.138		67.767	68.065		67.594	67.857	
	13	-93.488	-92.743		68.225	68.229		68.089	68.086	
	14	-89.029	-94.032		67.995	68.129		67.741	67.876	
	Min	-93.488	-94.032		67.633	67.642		67.389	67.375	
	Max	-83.520	-83.226		68.225	68.229		68.089	68.086	
	Mean	-88.448	-90.785		67.905	68.016		67.703	67.799	
	Std. Dev	4.103	5.068		0.261	0.258		0.295	0.301	
	Mean - 3 Sigma	-100.757	-105.988		67.123	67.242		66.819	66.896	
	Mean + 3 Sigma	-76.139	-75.581		68.687	68.791		68.587	68.701	
				<-75 dBc			>66 dBc			>67 dBc

		ENOB_AV1.9_DV3.3		Bits	HD2_AV1.9_DV3.3		dBc	HD3_AV1.9_DV3.3		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	10.982	11.037		-100.587	-97.219		-82.949	-82.367	
	11	10.902	10.900		-84.025	-83.439		-83.268	-82.978	
	12	10.936	10.980		-85.052	-84.351		-85.302	-84.762	
	13	11.018	11.018		-124.230	-102.877		-83.903	-83.721	
	14	10.960	10.983		-83.341	-83.969		-84.402	-84.114	
	Min	10.902	10.900		-124.230	-102.877		-85.302	-84.762	
	Max	11.018	11.018		-83.341	-83.439		-83.268	-82.978	
	Mean	10.954	10.970		-94.162	-88.659		-84.219	-83.894	
	Std. Dev	0.049	0.050		20.058	9.486		0.858	0.747	
	Mean - 3 Sigma	10.807	10.820		-154.335	-117.117		-86.794	-86.134	
	Mean + 3 Sigma	11.101	11.120		-33.989	-60.201		-81.644	-81.654	
		>10.8			<-72 dBc			<-72 dBc		

		SFDR_AV1.9_DV3.3		dBc	woSpur_AV1.9_DV3.3		dBc	SNR_AV1.7_DV1.8 (70MHz)		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	82.949	82.367		-90.131	-92.742		68.614	68.388	
	11	83.268	82.978		-84.023	-82.768		68.420	67.836	
	12	85.052	84.351		-87.832	-92.210		68.270	67.891	
	13	83.903	83.721		-92.411	-91.749		68.819	68.088	
	14	83.341	83.969		-89.025	-93.017		68.625	67.951	
	Min	83.268	82.978		-92.411	-93.017		68.270	67.836	
	Max	85.052	84.351		-84.023	-82.768		68.819	68.088	
	Mean	83.891	83.755		-88.323	-89.936		68.533	67.942	
	Std. Dev	0.824	0.579		3.461	4.807		0.240	0.109	
	Mean - 3 Sigma	81.419	82.017		-98.706	-104.357		67.814	67.615	
	Mean + 3 Sigma	86.363	85.492		-77.940	-75.515		69.253	68.268	
		>72 dBc			<-75 dBc			>65 dBc		

		SINAD_AV1.7_DV1.8 (70MHz)		dBc	ENOB_AV1.7_DV1.8 (70MHz)		Bits	HD2_AV1.7_DV1.8 (70MHz)		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	66.858	66.678		10.814	10.784		-73.430	-73.354	
	11	66.379	65.865		10.734	10.649		-72.046	-71.530	
	12	66.426	65.995		10.742	10.670		-72.290	-71.741	
	13	66.928	66.378		10.825	10.734		-73.424	-73.195	
	14	67.058	66.268		10.847	10.716		-75.917	-74.102	
	Min	66.379	65.865		10.734	10.649		-75.917	-74.102	
	Max	67.058	66.378		10.847	10.734		-72.046	-71.530	
	Mean	66.698	66.126		10.787	10.692		-73.419	-72.642	
	Std. Dev	0.345	0.237		0.057	0.039		1.771	1.223	
	Mean - 3 Sigma	65.662	65.414		10.615	10.574		-78.731	-76.311	
	Mean + 3 Sigma	67.734	66.838		10.959	10.810		-68.108	-68.973	
		>64 dBc			>10 Bits			<-65 dBc		

		HD3_AV1.7_DV1.8 (70MHz)		dBc	SFDR_AV1.7_DV1.8 (70MHz)		dBc	woSpur_AV1.7_DV1.8 (70MHz)		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-78.972	-78.865		73.430	73.354		-85.020	-85.815	
	11	-77.506	-77.734		72.046	71.530		-83.996	-84.160	
	12	-79.573	-79.795		72.290	71.741		-85.340	-84.214	
	13	-77.644	-77.569		73.424	73.195		-85.030	-84.383	
	14	-76.764	-76.760		75.917	74.102		-84.511	-83.651	
	Min	-79.573	-79.795		72.046	71.530		-85.340	-84.383	
	Max	-76.764	-76.760		75.917	74.102		-83.996	-83.651	
	Mean	-77.871	-77.964		73.419	72.642		-84.719	-84.102	
	Std. Dev	1.198	1.293		1.771	1.223		0.591	0.315	
	Mean - 3 Sigma	-81.466	-81.842		68.108	68.973		-86.493	-85.048	
	Mean + 3 Sigma	-74.277	-74.087		78.731	76.311		-82.945	-83.156	
		<-65 dBc			>65 dBc			<-70 dBc		

		SNR_AV1.9_DV1.8 (70MHz)		dBc	SINAD_AV1.9_DV1.8 (70MHz)		dBc	ENOB_AV1.9_DV1.8 (70MHz)		Bits
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	68.727	68.414		67.384	67.141		10.901	10.861	
	11	68.464	68.107		66.834	66.498		10.810	10.754	
	12	68.166	68.100		66.592	66.532		10.769	10.760	
	13	68.658	68.058		66.959	66.610		10.830	10.772	
	14	68.516	68.276		67.257	67.027		10.880	10.842	
	Min	68.166	68.058		66.592	66.498		10.769	10.754	
	Max	68.658	68.276		67.257	67.027		10.880	10.842	
	Mean	68.451	68.136		66.911	66.667		10.822	10.782	
	Std. Dev	0.207	0.096		0.277	0.245		0.046	0.041	
	Mean - 3 Sigma	67.830	67.847		66.080	65.933		10.684	10.660	
	Mean + 3 Sigma	69.072	68.424		67.742	67.400		10.960	10.904	
		>65 dBc			>64 dBc			>10 Bits		

		HD2_AV1.9_DV1.8 (70MHz)		dBc	HD3_AV1.9_DV1.8 (70MHz)		dBc	SFDR_AV1.9_DV1.8 (70MHz)		dBc
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-74.707	-74.946		-79.544	-79.174		74.707	74.946	
	11	-73.280	-72.924		-78.204	-78.110		73.280	72.924	
	12	-73.159	-73.050		-78.238	-78.484		73.159	73.050	
	13	-74.125	-74.441		-76.544	-76.660		74.125	74.441	
	14	-77.562	-76.847		-76.231	-76.514		76.231	76.514	
	Min	-77.562	-76.847		-78.238	-78.484		73.159	72.924	
	Max	-73.159	-72.924		-76.231	-76.514		76.231	76.514	
	Mean	-74.531	-74.315		-77.304	-77.442		74.199	74.232	
	Std. Dev	2.066	1.822		1.066	1.001		1.421	1.669	
	Mean - 3 Sigma	-80.728	-79.782		-80.503	-80.445		69.935	69.224	
	Mean + 3 Sigma	-68.335	-68.849		-74.105	-74.440		78.462	79.241	
				<-65 dBc			<-65 dBc			>65 dBc

		woSpur_AV1.9_DV1.8 (70MHz)		dBc	DNL_Max_AV1.8_DV2.5		LSB	DNL_Min_AV1.8_DV2.5		LSB
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-84.985	-83.926		0.391	0.394		-0.431	-0.425	
	11	-83.178	-83.080		0.303	0.388		-0.335	-0.321	
	12	-82.828	-83.695		0.729	0.701		-0.347	-0.379	
	13	-85.681	-85.485		0.656	0.587		-0.334	-0.320	
	14	-85.783	-84.409		0.392	0.418		-0.368	-0.387	
	Min	-85.783	-85.485		0.303	0.388		-0.368	-0.387	
	Max	-82.828	-83.080		0.729	0.701		-0.334	-0.320	
	Mean	-84.367	-84.167		0.520	0.523		-0.346	-0.352	
	Std. Dev	1.583	1.033		0.204	0.147		0.016	0.036	
	Mean - 3 Sigma	-89.116	-87.265		-0.093	0.082		-0.394	-0.460	
	Mean + 3 Sigma	-79.619	-81.069		1.133	0.965		-0.298	-0.243	
				<-70 dBc			<-1.8 LSB			>-0.8 LSB

		INL_Max_AV1.8_DV2.5		LSB	INL_Min_AV1.8_DV2.5		LSB	DNL_Max_AV1.7_DV1.8		LSB
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	1.781	2.075		-1.210	-1.352		0.406	0.449	
	11	1.817	2.039		-1.675	-1.697		0.298	0.337	
	12	1.958	1.982		-1.950	-1.920		0.749	0.638	
	13	1.551	1.679		-1.531	-1.426		0.570	0.612	
	14	1.288	1.525		-1.727	-1.671		0.435	0.391	
	Min	1.288	1.525		-1.950	-1.920		0.298	0.337	
	Max	1.958	2.039		-1.531	-1.426		0.749	0.638	
	Mean	1.653	1.806		-1.720	-1.678		0.513	0.495	
	Std. Dev	0.296	0.245		0.174	0.202		0.193	0.153	
	Mean - 3 Sigma	0.764	1.070		-2.242	-2.285		-0.065	0.037	
	Mean + 3 Sigma	2.543	2.542		-1.199	-1.072		1.090	0.952	
				<-6 LSB			>-6 LSB			<-1.8 LSB

		DNL_Min_AV1.7_DV1.8		LSB	INL_Max_AV1.7_DV1.8		LSB	INL_Min_AV1.7_DV1.8		LSB
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-0.405	-0.410		1.560	1.675		-1.239	-1.336	
	11	-0.263	-0.287		1.746	1.649		-1.567	-1.530	
	12	-0.381	-0.376		1.742	1.699		-1.804	-1.995	
	13	-0.332	-0.343		1.542	1.493		-1.460	-1.344	
	14	-0.363	-0.345		1.455	1.400		-1.538	-1.474	
	Min	-0.381	-0.376		1.455	1.400		-1.804	-1.995	
	Max	-0.263	-0.287		1.746	1.699		-1.460	-1.344	
	Mean	-0.335	-0.338		1.622	1.560		-1.592	-1.586	
	Std. Dev	0.052	0.037		0.146	0.138		0.148	0.284	
	Mean - 3 Sigma	-0.490	-0.450		1.183	1.146		-2.036	-2.437	
	Mean + 3 Sigma	-0.180	-0.226		2.060	1.975		-1.147	-0.735	
				>-0.8 LSB			<-6 LSB			>-6 LSB

		DNL_Max_AV1.7_DV3.3		LSB	DNL_Min_AV1.7_DV3.3		LSB	INL_Max_AV1.7_DV3.3		LSB
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.482	0.455		-0.424	-0.418		1.798	1.900	
	11	0.343	0.321		-0.344	-0.368		2.008	2.203	
	12	0.761	0.776		-0.359	-0.347		2.488	2.274	
	13	0.590	0.618		-0.335	-0.343		1.957	1.958	
	14	0.480	0.492		-0.410	-0.364		1.683	1.846	
	Min	0.343	0.321		-0.410	-0.368		1.683	1.846	
	Max	0.761	0.776		-0.335	-0.343		2.488	2.274	
	Mean	0.543	0.552		-0.362	-0.355		2.034	2.070	
	Std. Dev	0.177	0.193		0.034	0.012		0.334	0.202	
	Mean - 3 Sigma	0.014	-0.026		-0.463	-0.393		1.031	1.465	
	Mean + 3 Sigma	1.073	1.130		-0.262	-0.318		3.037	2.675	
				<-1.8 LSB			>-0.8 LSB			<-6 LSB

		INL_Min_AV1.7_DV3.3		LSB	DNL_Max_AV1.9_DV1.8		LSB	DNL_Min_AV1.9_DV1.8		LSB	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-1.471	-1.674		0.411	0.349		-0.416	-0.414		
	11	-2.123	-1.908		0.307	0.312		-0.271	-0.341		
	12	-2.004	-2.120		0.610	0.633		-0.407	-0.408		
	13	-1.866	-1.776		0.566	0.488		-0.312	-0.334		
	14	-1.946	-2.328		0.374	0.407		-0.414	-0.374		
	Min	-2.123	-2.328		0.307	0.312		-0.414	-0.408		
	Max	-1.866	-1.776		0.610	0.633		-0.271	-0.334		
	Mean	-1.985	-2.033		0.464	0.460		-0.351	-0.364		
	Std. Dev	0.108	0.242		0.146	0.136		0.071	0.034		
	Mean - 3 Sigma	-2.309	-2.760		0.025	0.052		-0.563	-0.466		
	Mean + 3 Sigma	-1.661	-1.306		0.903	0.868		-0.139	-0.262		
				>6 LSB				<1.8 LSB	>-0.8 LSB		

		INL_Max_AV1.9_DV1.8		LSB	INL_Min_AV1.9_DV1.8		LSB	DNL_Max_AV1.9_DV3.3		LSB	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.862	1.892		-1.257	-1.285		0.398	0.392		
	11	1.975	2.038		-1.777	-1.815		0.348	0.349		
	12	1.852	1.861		-1.803	-1.884		0.636	0.582		
	13	1.407	1.703		-1.384	-1.435		0.471	0.538		
	14	1.299	1.447		-1.538	-1.674		0.400	0.381		
	Min	1.299	1.447		-1.803	-1.884		0.348	0.349		
	Max	1.975	2.038		-1.384	-1.435		0.636	0.582		
	Mean	1.633	1.762		-1.625	-1.702		0.464	0.463		
	Std. Dev	0.331	0.251		0.200	0.198		0.125	0.115		
	Mean - 3 Sigma	0.641	1.010		-2.227	-2.297		0.088	0.118		
	Mean + 3 Sigma	2.625	2.514		-1.024	-1.107		0.840	0.807		
				<6 LSB				>6 LSB	<1.8 LSB		

		DNL_Min_AV1.9_DV3.3		LSB	INL_Max_AV1.9_DV3.3		LSB	INL_Min_AV1.9_DV3.3		LSB	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.395	-0.416		2.205	2.149		-1.658	-1.484		
	11	-0.292	-0.300		2.028	2.094		-1.981	-1.930		
	12	-0.352	-0.393		2.303	2.048		-2.033	-1.885		
	13	-0.376	-0.334		2.023	2.077		-1.726	-1.918		
	14	-0.351	-0.370		1.902	1.883		-2.059	-2.264		
	Min	-0.376	-0.393		1.902	1.883		-2.059	-2.264		
	Max	-0.292	-0.300		2.303	2.094		-1.726	-1.885		
	Mean	-0.343	-0.349		2.064	2.026		-1.950	-1.999		
	Std. Dev	0.036	0.041		0.170	0.097		0.153	0.177		
	Mean - 3 Sigma	-0.450	-0.472		1.555	1.735		-2.408	-2.531		
	Mean + 3 Sigma	-0.235	-0.227		2.573	2.316		-1.492	-1.467		
				>0.8 LSB				<6 LSB	>6 LSB		

		CLK_Rin_clk_n_dig_		Ohms	CLK_Rin_clk_p_dig_		Ohms	CLK_MinVinDiff_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	12.019	11.970		11.948	11.852		0.008	0.004		
	11	11.927	11.970		11.841	11.868		0.008	0.004		
	12	11.836	11.803		11.902	11.837		0.004	0.002		
	13	11.790	11.787		11.703	11.669		0.006	0.002		
	14	11.668	11.742		11.749	11.730		0.008	0.004		
	Min	11.668	11.742		11.703	11.669		0.004	0.002		
	Max	11.927	11.970		11.902	11.868		0.008	0.004		
	Mean	11.805	11.825		11.799	11.776		0.007	0.003		
	Std. Dev	0.108	0.100		0.089	0.092		0.002	0.001		
	Mean - 3 Sigma	11.482	11.526		11.531	11.498		0.001	0.000		
	Mean + 3 Sigma	12.129	12.125		12.067	12.053		0.012	0.006		
				>8k, <15k Ohms				>8k, <15k Ohms	<0.2 V		

		CLK_MaxVinDiff_AV1.8_DV2.5		V	CLK_MaxVinComm_AV1.8_DV2.5		V	CLK_MinVinComm_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.600	3.600		1.790	1.790		0.000	0.000		
	11	3.600	3.600		1.790	1.790		0.000	0.000		
	12	3.600	3.600		1.800	1.791		0.000	0.000		
	13	3.600	3.600		1.790	1.791		0.000	0.000		
	14	3.600	3.600		1.791	1.790		0.000	0.000		
	Min	3.600	3.600		1.790	1.790		0.000	0.000		
	Max	3.600	3.600		1.800	1.791		0.000	0.000		
	Mean	3.600	3.600		1.793	1.791		0.000	0.000		
	Std. Dev	0.000	0.000		0.005	0.001		0.000	0.000		
	Mean - 3 Sigma	3.600	3.600		1.778	1.789		0.000	0.000		
	Mean + 3 Sigma	3.600	3.600		1.807	1.792		0.000	0.000		
				>3.4V				>1.6V	<0.2V		

		CLK_Rin_clk_n_dig_		Ohms	CLK_Rin_clk_p_dig_		Ohms	CLK_MinVinDiff_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	12.019	11.909		11.993	11.929		0.008	0.004		
	11	11.943	11.894		11.871	11.868		0.002	0.004		
	12	11.699	11.787		11.826	11.852		0.004	0.002		
	13	11.775	11.787		11.627	11.730		0.004	0.004		
	14	11.760	11.711		11.810	11.745		0.006	0.004		
	Min	11.699	11.711		11.627	11.730		0.002	0.002		
	Max	11.943	11.894		11.871	11.868		0.006	0.004		
	Mean	11.794	11.795		11.784	11.799		0.004	0.004		
	Std. Dev	0.104	0.075		0.107	0.071		0.002	0.001		
	Mean - 3 Sigma	11.480	11.570		11.461	11.585		-0.001	0.001		
	Mean + 3 Sigma	12.107	12.020		12.106	12.012		0.009	0.007		
				>8k, <15k Ohms				>8k, <15k Ohms			<0.2 V

		CLK_MaxVinDiff_AV1.8_DV1.8		V	CLK_MaxVinComm_AV1.8_DV1.8		V	CLK_MinVinComm_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.600	3.600		1.790	1.790		0.000	0.000		
	11	3.600	3.600		1.790	1.790		0.000	0.000		
	12	3.600	3.600		1.800	1.800		0.000	0.000		
	13	3.600	3.600		1.790	1.790		0.000	0.000		
	14	3.600	3.600		1.800	1.790		0.000	0.000		
	Min	3.600	3.600		1.790	1.790		0.000	0.000		
	Max	3.600	3.600		1.800	1.800		0.000	0.000		
	Mean	3.600	3.600		1.795	1.793		0.000	0.000		
	Std. Dev	0.000	0.000		0.006	0.005		0.000	0.000		
	Mean - 3 Sigma	3.600	3.600		1.778	1.778		0.000	0.000		
	Mean + 3 Sigma	3.600	3.600		1.812	1.808		0.000	0.000		
				>3.4V				>1.6V			<0.2V

		CLK_Rin_clk_n_dig_		Ohms	CLK_Rin_clk_p_dig_		Ohms	CLK_MinVinDiff_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.988	11.924		11.993	11.837		0.008	0.004		
	11	11.927	11.985		11.841	11.868		0.010	0.004		
	12	11.866	11.818		11.856	11.868		0.006	0.002		
	13	11.836	11.787		11.719	11.669		0.006	0.004		
	14	11.744	11.650		11.719	11.730		0.008	0.004		
	Min	11.744	11.650		11.719	11.669		0.006	0.002		
	Max	11.927	11.985		11.856	11.868		0.010	0.004		
	Mean	11.843	11.810		11.784	11.783		0.008	0.004		
	Std. Dev	0.076	0.138		0.075	0.100		0.002	0.001		
	Mean - 3 Sigma	11.615	11.397		11.558	11.483		0.002	0.001		
	Mean + 3 Sigma	12.072	12.223		12.009	12.084		0.013	0.007		
				>8k, <15k Ohms				>8k, <15k Ohms			<0.2 V

		CLK_MaxVinDiff_AV1.8_DV3.3		V	CLK_MaxVinComm_AV1.8_DV3.3		V	CLK_MinVinComm_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.600	3.600		1.790	1.790		0.000	0.000		
	11	3.600	3.600		1.790	1.790		0.000	0.000		
	12	3.600	3.600		1.800	1.799		0.000	0.000		
	13	3.600	3.600		1.790	1.790		0.000	0.000		
	14	3.600	3.600		1.800	1.791		0.000	0.000		
	Min	3.600	3.600		1.790	1.790		0.000	0.000		
	Max	3.600	3.600		1.800	1.799		0.000	0.000		
	Mean	3.600	3.600		1.795	1.793		0.000	0.000		
	Std. Dev	0.000	0.000		0.006	0.004		0.000	0.000		
	Mean - 3 Sigma	3.600	3.600		1.778	1.779		0.000	0.000		
	Mean + 3 Sigma	3.600	3.600		1.812	1.806		0.000	0.000		
				>3.4V				>1.6V			<0.2V

		CLK_Rin_clk_n_dig_		Ohms	CLK_Rin_clk_p_dig_		Ohms	CLK_MinVinDiff_AV1.7_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.958	11.924		11.917	11.868		0.002	0.002		
	11	11.866	11.909		11.871	11.852		0.010	0.004		
	12	11.790	11.742		11.841	11.791		0.006	0.004		
	13	11.805	11.787		11.719	11.684		0.006	0.004		
	14	11.729	11.696		11.841	11.761		0.008	0.002		
	Min	11.729	11.696		11.719	11.684		0.006	0.002		
	Max	11.866	11.909		11.871	11.852		0.010	0.004		
	Mean	11.798	11.784		11.818	11.772		0.008	0.004		
	Std. Dev	0.056	0.092		0.068	0.070		0.002	0.001		
	Mean - 3 Sigma	11.629	11.508		11.615	11.562		0.002	0.001		
	Mean + 3 Sigma	11.967	12.059		12.021	11.982		0.013	0.007		
				>8k, <15k Ohms				>8k, <15k Ohms			<0.2 V



		CLK_MaxVinDiff_AV1.7_DV2.5		V	CLK_MaxVinComm_AV1.7_DV2.5		V	CLK_MinVinComm_AV1.7_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.400	3.400		1.700	1.690		0.000	0.000		
	11	3.400	3.400		1.700	1.700		0.000	0.000		
	12	3.400	3.400		1.700	1.700		0.000	0.000		
	13	3.400	3.400		1.700	1.691		0.000	0.000		
	14	3.400	3.400		1.700	1.700		0.000	0.000		
	Min	3.400	3.400		1.700	1.691		0.000	0.000		
	Max	3.400	3.400		1.700	1.700		0.000	0.000		
	Mean	3.400	3.400		1.700	1.698		0.000	0.000		
	Std. Dev	0.000	0.000		0.000	0.004		0.000	0.000		
	Mean - 3 Sigma	3.400	3.400		1.700	1.684		0.000	0.000		
	Mean + 3 Sigma	3.400	3.400		1.700	1.711		0.000	0.000		
				>3.2V					>1.5V	<0.2V	

		CLK_Rin_clk_n_dig_		k Ohms	CLK_Rin_clk_p_dig_		k Ohms	CLK_MinVinDiff_AV1.7_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.912	11.940		11.887	11.837		0.008	0.004		
	11	11.912	11.879		11.780	11.852		0.008	0.004		
	12	11.699	11.818		11.749	11.868		0.006	0.004		
	13	11.851	11.787		11.703	11.684		0.006	0.004		
	14	11.729	11.757		11.780	11.699		0.008	0.004		
	Min	11.699	11.757		11.703	11.684		0.006	0.004		
	Max	11.912	11.879		11.780	11.868		0.008	0.004		
	Mean	11.798	11.810		11.753	11.776		0.007	0.004		
	Std. Dev	0.101	0.052		0.036	0.097		0.001	0.000		
	Mean - 3 Sigma	11.495	11.654		11.645	11.484		0.004	0.004		
	Mean + 3 Sigma	12.100	11.966		11.861	12.068		0.010	0.004		
				>8k, <15k Ohms					>8k, <15k Ohms	<0.2 V	

		CLK_MaxVinDiff_AV1.7_DV1.8		V	CLK_MaxVinComm_AV1.7_DV1.8		V	CLK_MinVinComm_AV1.7_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.400	3.400		1.700	1.692		0.000	0.000		
	11	3.400	3.400		1.700	1.700		0.000	0.000		
	12	3.400	3.400		1.700	1.700		0.000	0.000		
	13	3.400	3.400		1.700	1.691		0.000	0.000		
	14	3.400	3.400		1.700	1.700		0.000	0.000		
	Min	3.400	3.400		1.700	1.691		0.000	0.000		
	Max	3.400	3.400		1.700	1.700		0.000	0.000		
	Mean	3.400	3.400		1.700	1.698		0.000	0.000		
	Std. Dev	0.000	0.000		0.000	0.004		0.000	0.000		
	Mean - 3 Sigma	3.400	3.400		1.700	1.684		0.000	0.000		
	Mean + 3 Sigma	3.400	3.400		1.700	1.711		0.000	0.000		
				>3.2V					>1.5V	<0.2V	

		CLK_Rin_clk_n_dig_		k Ohms	CLK_Rin_clk_p_dig_		k Ohms	CLK_MinVinDiff_AV1.7_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.866	11.909		11.871	11.791		0.010	0.008		
	11	11.912	11.833		11.826	11.776		0.010	0.006		
	12	11.836	11.757		11.871	11.883		0.006	0.004		
	13	11.821	11.726		11.688	11.684		0.002	0.004		
	14	11.744	11.742		11.749	11.776		0.010	0.004		
	Min	11.744	11.726		11.688	11.684		0.002	0.004		
	Max	11.912	11.833		11.871	11.883		0.010	0.006		
	Mean	11.828	11.765		11.784	11.780		0.007	0.005		
	Std. Dev	0.069	0.047		0.081	0.081		0.004	0.001		
	Mean - 3 Sigma	11.622	11.622		11.540	11.536		-0.004	0.002		
	Mean + 3 Sigma	12.034	11.907		12.027	12.023		0.018	0.008		
				>8k, <15k Ohms					>8k, <15k Ohms	<0.2 V	

		CLK_MaxVinDiff_AV1.7_DV3.3		V	CLK_MaxVinComm_AV1.7_DV3.3		V	CLK_MinVinComm_AV1.7_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.400	3.400		1.700	1.692		0.000	0.000		
	11	3.400	3.400		1.700	1.700		0.000	0.000		
	12	3.400	3.400		1.679	1.700		0.000	0.000		
	13	3.400	3.400		1.700	1.691		0.000	0.000		
	14	3.400	3.400		1.700	1.700		0.000	0.000		
	Min	3.400	3.400		1.679	1.691		0.000	0.000		
	Max	3.400	3.400		1.700	1.700		0.000	0.000		
	Mean	3.400	3.400		1.695	1.698		0.000	0.000		
	Std. Dev	0.000	0.000		0.011	0.004		0.000	0.000		
	Mean - 3 Sigma	3.400	3.400		1.663	1.684		0.000	0.000		
	Mean + 3 Sigma	3.400	3.400		1.726	1.711		0.000	0.000		
				>3.2V					>1.5V	<0.2V	

		CLK_Rin_clk_n_dig_		k Ohms	CLK_Rin_clk_p_dig_		k Ohms	CLK_MinVinDiff_AV1.9_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.973	11.940		11.932	11.883		0.006	0.002		
	11	11.943	12.001		11.887	11.929		0.006	0.002		
	12	11.882	11.787		11.932	11.852		0.006	0.004		
	13	11.882	11.787		11.764	11.684		0.006	0.004		
	14	11.790	11.803		11.810	11.806		0.006	0.004		
	Min	11.790	11.787		11.764	11.684		0.006	0.002		
	Max	11.943	12.001		11.932	11.929		0.006	0.004		
	Mean	11.874	11.844		11.848	11.818		0.006	0.004		
	Std. Dev	0.063	0.104		0.075	0.102		0.000	0.001		
	Mean - 3 Sigma	11.685	11.532		11.623	11.511		0.006	0.001		
	Mean + 3 Sigma	12.063	12.157		12.074	12.125		0.006	0.007		
				>8k, <15k Ohms				>8k, <15k Ohms			<0.2 V

		CLK_MaxVinDiff_AV1.9_DV2.5		V	CLK_MaxVinComm_AV1.9_DV2.5		V	CLK_MinVinComm_AV1.9_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.800	3.800		1.890	1.890		0.000	0.000		
	11	3.800	3.800		1.890	1.890		0.000	0.000		
	12	3.800	3.800		1.891	1.890		0.000	0.000		
	13	3.800	3.800		1.890	1.890		0.000	0.000		
	14	3.800	3.800		1.890	1.891		0.000	0.000		
	Min	3.800	3.800		1.890	1.890		0.000	0.000		
	Max	3.800	3.800		1.891	1.891		0.000	0.000		
	Mean	3.800	3.800		1.890	1.890		0.000	0.000		
	Std. Dev	0.000	0.000		0.001	0.001		0.000	0.000		
	Mean - 3 Sigma	3.800	3.800		1.889	1.889		0.000	0.000		
	Mean + 3 Sigma	3.800	3.800		1.892	1.892		0.000	0.000		
				>3.6V				>1.7V			<0.2V

		CLK_Rin_clk_n_dig_		k Ohms	CLK_Rin_clk_p_dig_		k Ohms	CLK_MinVinDiff_AV1.9_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.897	11.940		11.902	11.929		0.006	0.002		
	11	11.927	11.864		11.902	11.852		0.006	0.004		
	12	11.744	11.879		11.810	11.898		0.008	0.004		
	13	11.790	11.757		11.719	11.669		0.008	0.004		
	14	11.744	11.757		11.764	11.806		0.002	0.004		
	Min	11.744	11.757		11.719	11.669		0.002	0.004		
	Max	11.927	11.879		11.902	11.898		0.008	0.004		
	Mean	11.801	11.814		11.799	11.806		0.006	0.004		
	Std. Dev	0.087	0.066		0.078	0.099		0.003	0.000		
	Mean - 3 Sigma	11.542	11.615		11.564	11.509		-0.002	0.004		
	Mean + 3 Sigma	12.061	12.013		12.033	12.103		0.014	0.004		
				>8k, <15k Ohms				>8k, <15k Ohms			<0.2 V

		CLK_MaxVinDiff_AV1.9_DV1.8		V	CLK_MaxVinComm_AV1.9_DV1.8		V	CLK_MinVinComm_AV1.9_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.800	3.800		1.890	1.893		0.000	0.000		
	11	3.800	3.800		1.890	1.890		0.000	0.000		
	12	3.800	3.800		1.890	1.890		0.000	0.000		
	13	3.800	3.800		1.890	1.898		0.000	0.000		
	14	3.800	3.800		1.891	1.891		0.000	0.000		
	Min	3.800	3.800		1.890	1.890		0.000	0.000		
	Max	3.800	3.800		1.891	1.898		0.000	0.000		
	Mean	3.800	3.800		1.890	1.892		0.000	0.000		
	Std. Dev	0.000	0.000		0.001	0.004		0.000	0.000		
	Mean - 3 Sigma	3.800	3.800		1.889	1.881		0.000	0.000		
	Mean + 3 Sigma	3.800	3.800		1.892	1.904		0.000	0.000		
				>3.6V				>1.7V			<0.2V

		CLK_Rin_clk_n_dig_		k Ohms	CLK_Rin_clk_p_dig_		k Ohms	CLK_MinVinDiff_AV1.9_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	11.988	11.924		11.932	11.929		0.006	0.004		
	11	11.851	11.879		11.856	11.837		0.008	0.004		
	12	11.851	11.803		11.856	11.883		0.006	0.004		
	13	11.805	11.787		11.688	11.699		0.006	0.004		
	14	11.836	11.726		11.795	11.776		0.006	0.004		
	Min	11.805	11.726		11.688	11.699		0.006	0.004		
	Max	11.851	11.879		11.856	11.883		0.008	0.004		
	Mean	11.836	11.799		11.799	11.799		0.007	0.004		
	Std. Dev	0.022	0.063		0.079	0.079		0.001	0.000		
	Mean - 3 Sigma	11.771	11.611		11.561	11.561		0.004	0.004		
	Mean + 3 Sigma	11.900	11.987		12.036	12.037		0.010	0.004		
				>8k, <15k Ohms				>8k, <15k Ohms			<0.2 V

		CLK_MaxVinDiff_AV1.9_DV3.3		V	CLK_MaxVinComm_AV1.9_DV3.3		V	CLK_MinVinComm_AV1.9_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.800	3.800		1.890	1.891		0.000	0.000		
	11	3.800	3.800		1.891	1.891		0.000	0.000		
	12	3.800	3.800		1.890	1.890		0.000	0.000		
	13	3.800	3.800		1.890	1.897		0.000	0.000		
	14	3.800	3.800		1.890	1.890		0.000	0.000		
	Min	3.800	3.800		1.890	1.890		0.000	0.000		
	Max	3.800	3.800		1.891	1.897		0.000	0.000		
	Mean	3.800	3.800		1.890	1.892		0.000	0.000		
	Std. Dev	0.000	0.000		0.001	0.003		0.000	0.000		
	Mean - 3 Sigma	3.800	3.800		1.889	1.882		0.000	0.000		
	Mean + 3 Sigma	3.800	3.800		1.892	1.902		0.000	0.000		
				>3.6V					>1.7V	<0.2V	

		SCLK_Vih_AV1.8_DV2.5		V	SCLK_Vih_AV1.8_DV2.5		V	CSB_Vih_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.784	0.784		0.941	0.942		0.835	0.837		
	11	0.787	0.788		0.944	0.944		0.837	0.838		
	12	0.790	0.788		0.949	0.949		0.836	0.840		
	13	0.785	0.784		0.944	0.944		0.836	0.837		
	14	0.788	0.786		0.949	0.948		0.835	0.838		
	Min	0.785	0.784		0.944	0.944		0.835	0.837		
	Max	0.790	0.788		0.949	0.949		0.837	0.840		
	Mean	0.788	0.787		0.947	0.946		0.836	0.838		
	Std. Dev	0.002	0.002		0.003	0.003		0.001	0.001		
	Mean - 3 Sigma	0.781	0.781		0.938	0.938		0.834	0.834		
	Mean + 3 Sigma	0.794	0.792		0.955	0.954		0.838	0.842		
				>0.7V					<1.1V	>0.7V	

		CSB_Vih_AV1.8_DV2.5		V	PWDN_Vih_AV1.8_DV2.5		V	PWDN_Vih_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.832	0.834		0.938	0.940		0.780	0.778		
	11	0.833	0.836		0.927	0.928		0.776	0.776		
	12	0.833	0.835		0.935	0.935		0.773	0.773		
	13	0.832	0.832		0.928	0.926		0.773	0.772		
	14	0.832	0.837		0.926	0.927		0.773	0.773		
	Min	0.832	0.832		0.926	0.926		0.773	0.772		
	Max	0.833	0.837		0.935	0.935		0.776	0.776		
	Mean	0.833	0.835		0.929	0.929		0.774	0.774		
	Std. Dev	0.001	0.002		0.004	0.004		0.002	0.002		
	Mean - 3 Sigma	0.831	0.829		0.917	0.917		0.769	0.768		
	Mean + 3 Sigma	0.834	0.841		0.941	0.941		0.778	0.779		
				<1.1V					>0.7V	<1.1V	

		OEB_Vih_AV1.8_DV2.5		V	OEB_Vih_AV1.8_DV2.5		V	SDIO_Vih_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.843	0.877		0.772	0.772		0.831	0.833		
	11	0.879	0.892		0.775	0.774		0.832	0.831		
	12	0.873	0.869		0.773	0.773		0.832	0.833		
	13	0.871	0.851		0.774	0.773		0.831	0.831		
	14	0.868	0.886		0.779	0.778		0.828	0.830		
	Min	0.868	0.851		0.773	0.773		0.828	0.830		
	Max	0.879	0.892		0.779	0.778		0.832	0.833		
	Mean	0.873	0.875		0.775	0.775		0.831	0.831		
	Std. Dev	0.005	0.018		0.003	0.002		0.002	0.001		
	Mean - 3 Sigma	0.859	0.819		0.767	0.767		0.825	0.827		
	Mean + 3 Sigma	0.887	0.930		0.783	0.782		0.836	0.835		
				>0.7V					<1.1V	<1.1V	

		SDIO_Vih_AV1.8_DV2.5		V	SDIO_Voh_50u_AV1.8_DV2.5		V	DCO_Voh_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.958	0.958		2.425	2.425		2.495	2.495		
	11	0.958	0.958		2.425	2.426		2.495	2.495		
	12	0.958	0.958		2.425	2.425		2.495	2.495		
	13	0.958	0.958		2.425	2.426		2.496	2.495		
	14	0.958	0.958		2.424	2.425		2.495	2.495		
	Min	0.958	0.958		2.424	2.425		2.495	2.495		
	Max	0.958	0.958		2.425	2.426		2.496	2.495		
	Mean	0.958	0.958		2.425	2.426		2.495	2.495		
	Std. Dev	0.000	0.000		0.000	0.001		0.000	0.000		
	Mean - 3 Sigma	0.958	0.958		2.423	2.424		2.494	2.495		
	Mean + 3 Sigma	0.958	0.958		2.426	2.427		2.497	2.495		
				>2.4V					>2.4V	>2.4V	

		OR_Voh_50u_AV1.8_DV2.5		V	D0_Voh_50u_AV1.8_DV2.5		V	D1_Voh_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.495	2.494		2.496	2.495		2.496	2.497		
	11	2.494	2.496		2.495	2.496		2.495	2.496		
	12	2.495	2.495		2.496	2.496		2.495	2.496		
	13	2.494	2.495		2.496	2.495		2.496	2.495		
	14	2.494	2.495		2.495	2.496		2.496	2.496		
	Min	2.494	2.495		2.495	2.495		2.495	2.495		
	Max	2.495	2.496		2.496	2.496		2.496	2.496		
	Mean	2.494	2.495		2.496	2.496		2.496	2.496		
	Std. Dev	0.000	0.000		0.001	0.000		0.001	0.000		
	Mean - 3 Sigma	2.493	2.494		2.494	2.494		2.494	2.494		
	Mean + 3 Sigma	2.496	2.497		2.497	2.497		2.497	2.497		
				>2.4V					>2.4V		

		D2_Voh_50u_AV1.8_DV2.5		V	D3_Voh_50u_AV1.8_DV2.5		V	D4_Voh_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.495	2.496		2.495	2.496		2.495	2.495		
	11	2.496	2.496		2.496	2.495		2.495	2.496		
	12	2.495	2.495		2.496	2.496		2.496	2.495		
	13	2.496	2.495		2.496	2.496		2.496	2.496		
	14	2.495	2.496		2.496	2.496		2.495	2.495		
	Min	2.495	2.495		2.496	2.495		2.495	2.495		
	Max	2.496	2.496		2.496	2.496		2.496	2.496		
	Mean	2.496	2.496		2.496	2.496		2.496	2.496		
	Std. Dev	0.001	0.001		0.000	0.000		0.001	0.001		
	Mean - 3 Sigma	2.494	2.494		2.496	2.494		2.494	2.494		
	Mean + 3 Sigma	2.497	2.497		2.496	2.497		2.497	2.497		
				>2.4V					>2.4V		

		D5_Voh_50u_AV1.8_DV2.5		V	D6_Voh_50u_AV1.8_DV2.5		V	D7_Voh_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.496	2.496		2.495	2.495		2.495	2.495		
	11	2.495	2.496		2.496	2.496		2.496	2.496		
	12	2.495	2.496		2.495	2.495		2.495	2.495		
	13	2.496	2.497		2.495	2.496		2.496	2.495		
	14	2.495	2.496		2.495	2.495		2.495	2.496		
	Min	2.495	2.496		2.495	2.495		2.495	2.495		
	Max	2.496	2.497		2.496	2.496		2.496	2.496		
	Mean	2.495	2.496		2.495	2.496		2.496	2.496		
	Std. Dev	0.000	0.000		0.000	0.001		0.001	0.001		
	Mean - 3 Sigma	2.494	2.495		2.494	2.494		2.494	2.494		
	Mean + 3 Sigma	2.497	2.498		2.497	2.497		2.497	2.497		
				>2.4V					>2.4V		

		D8_Voh_50u_AV1.8_DV2.5		V	D9_Voh_50u_AV1.8_DV2.5		V	D10_Voh_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.496	2.497		2.495	2.495		2.495	2.496		
	11	2.495	2.496		2.496	2.495		2.495	2.495		
	12	2.495	2.496		2.496	2.496		2.495	2.494		
	13	2.496	2.495		2.496	2.495		2.496	2.496		
	14	2.496	2.496		2.496	2.496		2.496	2.496		
	Min	2.495	2.495		2.496	2.495		2.495	2.494		
	Max	2.496	2.496		2.496	2.496		2.496	2.496		
	Mean	2.496	2.496		2.496	2.496		2.496	2.495		
	Std. Dev	0.001	0.000		0.000	0.001		0.001	0.001		
	Mean - 3 Sigma	2.494	2.494		2.496	2.494		2.494	2.492		
	Mean + 3 Sigma	2.497	2.497		2.496	2.497		2.497	2.498		
				>2.4V					>2.4V		

		D11_Voh_50u_AV1.8_DV2.5		V	D12_Voh_50u_AV1.8_DV2.5		V	D13_Voh_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.495	2.497		2.495	2.495		2.495	2.496		
	11	2.494	2.495		2.495	2.495		2.496	2.495		
	12	2.495	2.495		2.495	2.495		2.495	2.494		
	13	2.495	2.496		2.496	2.495		2.495	2.494		
	14	2.496	2.496		2.495	2.495		2.494	2.495		
	Min	2.494	2.495		2.495	2.495		2.494	2.494		
	Max	2.496	2.496		2.496	2.495		2.496	2.495		
	Mean	2.495	2.496		2.495	2.495		2.495	2.495		
	Std. Dev	0.001	0.001		0.000	0.000		0.001	0.001		
	Mean - 3 Sigma	2.493	2.494		2.494	2.495		2.493	2.493		
	Mean + 3 Sigma	2.497	2.497		2.497	2.495		2.497	2.496		
				>2.4V					>2.4V		

		SDIO_Vol_50u_AV1.8_DV2.5		V	DCO_Vol_50u_AV1.8_DV2.5		V	OR_Vol_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.004		0.004	0.005		0.003	0.003		
	11	0.006	0.006		0.004	0.005		0.004	0.003		
	12	0.004	0.004		0.004	0.004		0.002	0.004		
	13	0.003	0.003		0.004	0.005		0.003	0.002		
	14	0.002	0.004		0.003	0.004		0.003	0.003		
	Min	0.002	0.003		0.003	0.004		0.002	0.002		
	Max	0.006	0.006		0.004	0.005		0.004	0.004		
	Mean	0.004	0.004		0.004	0.005		0.003	0.003		
	Std. Dev	0.002	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	-0.001	0.000		0.002	0.003		0.001	0.001		
	Mean + 3 Sigma	0.009	0.008		0.005	0.006		0.005	0.005		
				<0.05V					<0.05V		

		D0_Vol_50u_AV1.8_DV2.5		V	D1_Vol_50u_AV1.8_DV2.5		V	D2_Vol_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.004		0.005	0.004		0.005	0.004		
	11	0.005	0.004		0.004	0.004		0.004	0.004		
	12	0.004	0.003		0.004	0.004		0.005	0.004		
	13	0.004	0.004		0.005	0.004		0.003	0.004		
	14	0.004	0.004		0.004	0.005		0.004	0.004		
	Min	0.004	0.003		0.004	0.004		0.003	0.004		
	Max	0.005	0.004		0.005	0.005		0.005	0.004		
	Mean	0.004	0.004		0.004	0.004		0.004	0.004		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.000		
	Mean - 3 Sigma	0.003	0.002		0.003	0.003		0.002	0.004		
	Mean + 3 Sigma	0.006	0.005		0.006	0.006		0.006	0.004		
				<0.05V					<0.05V		

		D3_Vol_50u_AV1.8_DV2.5		V	D4_Vol_50u_AV1.8_DV2.5		V	D5_Vol_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.005	0.004		0.005	0.004		0.004	0.004		
	11	0.004	0.004		0.004	0.003		0.005	0.004		
	12	0.004	0.004		0.003	0.004		0.004	0.004		
	13	0.003	0.004		0.004	0.003		0.004	0.004		
	14	0.004	0.004		0.003	0.004		0.004	0.003		
	Min	0.003	0.004		0.003	0.003		0.004	0.003		
	Max	0.004	0.004		0.004	0.004		0.005	0.004		
	Mean	0.004	0.004		0.004	0.004		0.004	0.004		
	Std. Dev	0.001	0.000		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.002	0.004		0.002	0.002		0.003	0.002		
	Mean + 3 Sigma	0.005	0.004		0.005	0.005		0.006	0.005		
				<0.05V					<0.05V		

		D6_Vol_50u_AV1.8_DV2.5		V	D7_Vol_50u_AV1.8_DV2.5		V	D8_Vol_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.003		0.004	0.004		0.004	0.004		
	11	0.005	0.003		0.003	0.003		0.004	0.003		
	12	0.003	0.004		0.004	0.004		0.003	0.003		
	13	0.005	0.004		0.003	0.004		0.004	0.003		
	14	0.004	0.003		0.003	0.004		0.003	0.005		
	Min	0.003	0.003		0.003	0.003		0.003	0.003		
	Max	0.005	0.004		0.004	0.004		0.004	0.005		
	Mean	0.004	0.004		0.003	0.004		0.004	0.004		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.001	0.002		0.002	0.002		0.002	0.001		
	Mean + 3 Sigma	0.007	0.005		0.005	0.005		0.005	0.007		
				<0.05V					<0.05V		

		D9_Vol_50u_AV1.8_DV2.5		V	D10_Vol_50u_AV1.8_DV2.5		V	D11_Vol_50u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.004		0.004	0.005		0.004	0.004		
	11	0.004	0.003		0.003	0.003		0.003	0.004		
	12	0.004	0.003		0.005	0.003		0.004	0.004		
	13	0.004	0.004		0.005	0.003		0.003	0.004		
	14	0.003	0.004		0.004	0.003		0.004	0.003		
	Min	0.003	0.003		0.003	0.003		0.003	0.003		
	Max	0.004	0.004		0.005	0.003		0.004	0.004		
	Mean	0.004	0.004		0.004	0.003		0.004	0.004		
	Std. Dev	0.001	0.001		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	0.002	0.002		0.001	0.003		0.002	0.002		
	Mean + 3 Sigma	0.005	0.005		0.007	0.003		0.005	0.005		
				<0.05V					<0.05V		

		D12_Vol_50u_AV1.8_DV2.5		V	D13_Vol_50u_AV1.8_DV2.5		V	SDIO_Voh_1mA_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.003	0.003		0.004	0.003		2.376	2.378		
	11	0.003	0.003		0.003	0.003		2.377	2.378		
	12	0.003	0.003		0.004	0.003		2.373	2.376		
	13	0.003	0.004		0.003	0.004		2.373	2.375		
	14	0.003	0.003		0.003	0.003		2.372	2.378		
	Min	0.003	0.003		0.003	0.003		2.372	2.375		
	Max	0.003	0.004		0.004	0.004		2.377	2.378		
	Mean	0.003	0.003		0.003	0.003		2.374	2.377		
	Std. Dev	0.000	0.001		0.001	0.001		0.002	0.002		
	Mean - 3 Sigma	0.003	0.002		0.002	0.002		2.367	2.372		
	Mean + 3 Sigma	0.003	0.005		0.005	0.005		2.380	2.381		
				<0.05V					<0.05V		

		DCO_Voh_1mA_AV1.8_DV2.5		V	OR_Voh_1mA_AV1.8_DV2.5		V	D0_Voh_1mA_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.438	2.437		2.436	2.435		2.437	2.436		
	11	2.437	2.437		2.435	2.434		2.436	2.436		
	12	2.439	2.438		2.436	2.436		2.437	2.436		
	13	2.438	2.438		2.436	2.436		2.437	2.437		
	14	2.439	2.438		2.436	2.436		2.437	2.437		
	Min	2.437	2.437		2.435	2.434		2.436	2.436		
	Max	2.439	2.438		2.436	2.436		2.437	2.437		
	Mean	2.438	2.438		2.436	2.436		2.437	2.437		
	Std. Dev	0.001	0.001		0.000	0.001		0.000	0.001		
	Mean - 3 Sigma	2.435	2.436		2.434	2.433		2.435	2.435		
	Mean + 3 Sigma	2.441	2.439		2.437	2.439		2.438	2.438		
				>2.3V					>2.3V		

		D1_Voh_1mA_AV1.8_DV2.5		V	D2_Voh_1mA_AV1.8_DV2.5		V	D3_Voh_1mA_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.436	2.436		2.437	2.437		2.437	2.437		
	11	2.435	2.435		2.437	2.436		2.435	2.435		
	12	2.436	2.437		2.437	2.436		2.437	2.437		
	13	2.437	2.438		2.437	2.437		2.438	2.437		
	14	2.436	2.437		2.436	2.437		2.437	2.437		
	Min	2.435	2.435		2.436	2.436		2.435	2.435		
	Max	2.437	2.438		2.437	2.437		2.438	2.437		
	Mean	2.436	2.437		2.437	2.437		2.437	2.437		
	Std. Dev	0.001	0.001		0.000	0.001		0.001	0.001		
	Mean - 3 Sigma	2.434	2.433		2.435	2.435		2.433	2.434		
	Mean + 3 Sigma	2.438	2.441		2.438	2.438		2.441	2.440		
				>2.3V					>2.3V		

		D4_Voh_1mA_AV1.8_DV2.5		V	D5_Voh_1mA_AV1.8_DV2.5		V	D6_Voh_1mA_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.437	2.436		2.438	2.437		2.437	2.437		
	11	2.436	2.434		2.436	2.436		2.436	2.434		
	12	2.437	2.437		2.437	2.436		2.436	2.436		
	13	2.437	2.436		2.437	2.437		2.436	2.436		
	14	2.436	2.435		2.437	2.437		2.436	2.437		
	Min	2.436	2.434		2.436	2.436		2.436	2.434		
	Max	2.437	2.437		2.437	2.437		2.436	2.437		
	Mean	2.437	2.436		2.437	2.437		2.436	2.436		
	Std. Dev	0.001	0.001		0.000	0.001		0.000	0.001		
	Mean - 3 Sigma	2.435	2.432		2.435	2.435		2.436	2.432		
	Mean + 3 Sigma	2.438	2.439		2.438	2.438		2.436	2.440		
				>2.3V					>2.3V		

		D7_Voh_1mA_AV1.8_DV2.5		V	D8_Voh_1mA_AV1.8_DV2.5		V	D9_Voh_1mA_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.437	2.436		2.438	2.436		2.436	2.436		
	11	2.436	2.435		2.436	2.435		2.436	2.436		
	12	2.437	2.435		2.437	2.437		2.437	2.437		
	13	2.437	2.436		2.438	2.438		2.437	2.438		
	14	2.437	2.436		2.437	2.438		2.437	2.438		
	Min	2.436	2.435		2.436	2.435		2.436	2.436		
	Max	2.437	2.436		2.438	2.438		2.437	2.438		
	Mean	2.437	2.436		2.437	2.437		2.437	2.437		
	Std. Dev	0.000	0.001		0.001	0.001		0.000	0.001		
	Mean - 3 Sigma	2.435	2.434		2.435	2.433		2.435	2.434		
	Mean + 3 Sigma	2.438	2.437		2.439	2.441		2.438	2.440		
				>2.3V					>2.3V		

		D10_Voh_1mA_AV1.8_DV2.5		V	D11_Voh_1mA_AV1.8_DV2.5		V	D12_Voh_1mA_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.436	2.437		2.436	2.437		2.435	2.436		
	11	2.436	2.436		2.436	2.436		2.434	2.433		
	12	2.437	2.438		2.437	2.435		2.436	2.435		
	13	2.437	2.439		2.436	2.436		2.436	2.437		
	14	2.437	2.438		2.435	2.437		2.435	2.436		
	Min	2.436	2.436		2.435	2.435		2.434	2.433		
	Max	2.437	2.439		2.437	2.437		2.436	2.437		
	Mean	2.437	2.438		2.436	2.436		2.435	2.435		
	Std. Dev	0.000	0.001		0.001	0.001		0.001	0.002		
	Mean - 3 Sigma	2.435	2.434		2.434	2.434		2.432	2.430		
	Mean + 3 Sigma	2.438	2.442		2.438	2.438		2.438	2.440		
				>2.3V					>2.3V		

		D13_Voh_1mA_AV1.8_DV2.5		V	SDIO_Vol_500u_AV1.8_DV2.5		V	DCO_Vol_500u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	2.435	2.435		0.033	0.033		0.033	0.034		
	11	2.436	2.435		0.035	0.035		0.032	0.033		
	12	2.436	2.435		0.033	0.033		0.032	0.033		
	13	2.436	2.436		0.031	0.032		0.031	0.033		
	14	2.435	2.435		0.032	0.033		0.032	0.033		
	Min	2.435	2.435		0.031	0.032		0.031	0.033		
	Max	2.436	2.436		0.035	0.035		0.032	0.033		
	Mean	2.436	2.435		0.033	0.033		0.032	0.033		
	Std. Dev	0.000	0.000		0.002	0.001		0.001	0.000		
	Mean - 3 Sigma	2.434	2.434		0.028	0.029		0.030	0.033		
	Mean + 3 Sigma	2.437	2.437		0.038	0.037		0.033	0.033		
				>2.3V					<-0.2V		

		OR_Vol_500u_AV1.8_DV2.5		V	D0_Vol_500u_AV1.8_DV2.5		V	D1_Vol_500u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.033	0.031		0.033	0.033		0.034	0.034		
	11	0.034	0.032		0.034	0.033		0.033	0.034		
	12	0.033	0.031		0.034	0.033		0.033	0.033		
	13	0.033	0.031		0.033	0.032		0.033	0.033		
	14	0.033	0.032		0.034	0.033		0.034	0.034		
	Min	0.033	0.031		0.033	0.032		0.033	0.033		
	Max	0.034	0.032		0.034	0.033		0.034	0.034		
	Mean	0.033	0.032		0.034	0.033		0.033	0.034		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.032	0.030		0.032	0.031		0.032	0.032		
	Mean + 3 Sigma	0.035	0.033		0.035	0.034		0.035	0.035		
				<-0.2V					<-0.2V		

		D2_Vol_500u_AV1.8_DV2.5		V	D3_Vol_500u_AV1.8_DV2.5		V	D4_Vol_500u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.034	0.033		0.033	0.034		0.033	0.034		
	11	0.033	0.033		0.034	0.033		0.034	0.034		
	12	0.034	0.034		0.033	0.034		0.034	0.034		
	13	0.033	0.034		0.033	0.035		0.034	0.033		
	14	0.033	0.034		0.033	0.034		0.034	0.033		
	Min	0.033	0.033		0.033	0.033		0.034	0.033		
	Max	0.034	0.034		0.034	0.035		0.034	0.034		
	Mean	0.033	0.034		0.033	0.034		0.034	0.034		
	Std. Dev	0.001	0.001		0.001	0.001		0.000	0.001		
	Mean - 3 Sigma	0.032	0.032		0.032	0.032		0.034	0.032		
	Mean + 3 Sigma	0.035	0.035		0.035	0.036		0.034	0.035		
				<-0.2V					<-0.2V		

		D5_Vol_500u_AV1.8_DV2.5		V	D6_Vol_500u_AV1.8_DV2.5		V	D7_Vol_500u_AV1.8_DV2.5		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.034	0.033		0.034	0.034		0.033	0.034		
	11	0.034	0.033		0.033	0.034		0.033	0.033		
	12	0.034	0.033		0.033	0.035		0.033	0.034		
	13	0.033	0.034		0.033	0.034		0.033	0.034		
	14	0.033	0.033		0.034	0.034		0.034	0.033		
	Min	0.033	0.033		0.033	0.034		0.033	0.033		
	Max	0.034	0.034		0.034	0.035		0.034	0.034		
	Mean	0.034	0.033		0.033	0.034		0.033	0.034		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.032	0.032		0.032	0.033		0.032	0.032		
	Mean + 3 Sigma	0.035	0.035		0.035	0.036		0.035	0.035		
				<-0.2V					<-0.2V		

		D8_Vol_500u_AV1.8_DV2.5		V	D9_Vol_500u_AV1.8_DV2.5		V	D10_Vol_500u_AV1.8_DV2.5		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.034	0.034		0.034	0.034		0.034	0.034	
	11	0.034	0.034		0.034	0.033		0.034	0.035	
	12	0.034	0.034		0.034	0.033		0.033	0.034	
	13	0.033	0.034		0.034	0.033		0.032	0.033	
	14	0.034	0.034		0.033	0.033		0.033	0.034	
	Min	0.033	0.034		0.033	0.033		0.032	0.033	
	Max	0.034	0.034		0.034	0.033		0.034	0.035	
	Mean	0.034	0.034		0.034	0.033		0.033	0.034	
	Std. Dev	0.001	0.000		0.001	0.000		0.001	0.001	
	Mean - 3 Sigma	0.032	0.034		0.032	0.033		0.031	0.032	
	Mean + 3 Sigma	0.035	0.034		0.035	0.033		0.035	0.036	
		<0.2V			<0.2V			<0.2V		

		D11_Vol_500u_AV1.8_DV2.5		V	D12_Vol_500u_AV1.8_DV2.5		V	D13_Vol_500u_AV1.8_DV2.5		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.033	0.033		0.034	0.033		0.033	0.033	
	11	0.033	0.033		0.034	0.033		0.033	0.033	
	12	0.033	0.033		0.032	0.033		0.033	0.034	
	13	0.032	0.033		0.032	0.033		0.034	0.033	
	14	0.033	0.034		0.033	0.034		0.033	0.033	
	Min	0.032	0.033		0.032	0.033		0.033	0.033	
	Max	0.033	0.034		0.034	0.034		0.034	0.034	
	Mean	0.033	0.033		0.033	0.033		0.033	0.033	
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001	
	Mean - 3 Sigma	0.031	0.032		0.030	0.032		0.032	0.032	
	Mean + 3 Sigma	0.034	0.035		0.036	0.035		0.035	0.035	
		<0.2V			<0.2V			<0.2V		

		SCLK_ViI_AV1.8_DV1.8		V	SCLK_ViH_AV1.8_DV1.8		V	CSB_ViI_AV1.8_DV1.8		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.777	0.776		0.941	0.941		0.809	0.812	
	11	0.779	0.778		0.944	0.944		0.810	0.813	
	12	0.782	0.780		0.949	0.949		0.811	0.813	
	13	0.777	0.776		0.944	0.944		0.811	0.813	
	14	0.779	0.778		0.948	0.948		0.810	0.813	
	Min	0.777	0.776		0.944	0.944		0.810	0.813	
	Max	0.782	0.780		0.949	0.949		0.811	0.813	
	Mean	0.779	0.778		0.946	0.946		0.811	0.813	
	Std. Dev	0.002	0.002		0.003	0.003		0.001	0.000	
	Mean - 3 Sigma	0.773	0.773		0.938	0.938		0.809	0.813	
	Mean + 3 Sigma	0.785	0.783		0.954	0.954		0.812	0.813	
		>0.7V			<1.1V			>0.7V		

		CSB_ViH_AV1.8_DV1.8		V	PWDN_ViI_AV1.8_DV1.8		V	PWDN_ViH_AV1.8_DV1.8		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.805	0.807		0.939	0.939		0.780	0.778	
	11	0.807	0.809		0.932	0.931		0.776	0.776	
	12	0.808	0.809		0.937	0.934		0.772	0.772	
	13	0.808	0.808		0.930	0.930		0.773	0.772	
	14	0.810	0.810		0.931	0.929		0.774	0.773	
	Min	0.807	0.808		0.930	0.929		0.772	0.772	
	Max	0.810	0.810		0.937	0.934		0.776	0.776	
	Mean	0.808	0.809		0.933	0.931		0.774	0.773	
	Std. Dev	0.001	0.001		0.003	0.002		0.002	0.002	
	Mean - 3 Sigma	0.804	0.807		0.923	0.925		0.769	0.768	
	Mean + 3 Sigma	0.812	0.811		0.942	0.937		0.779	0.779	
		<1.1V			>0.7V			<1.1V		

		OEB_ViI_AV1.8_DV1.8		V	OEB_ViH_AV1.8_DV1.8		V	SDIO_ViI_AV1.8_DV1.8		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.916	0.908		0.772	0.771		0.803	0.804	
	11	0.923	0.920		0.775	0.774		0.804	0.805	
	12	0.912	0.901		0.773	0.772		0.803	0.805	
	13	0.908	0.907		0.773	0.773		0.804	0.804	
	14	0.913	0.916		0.779	0.778		0.803	0.802	
	Min	0.908	0.901		0.773	0.772		0.803	0.802	
	Max	0.923	0.920		0.779	0.778		0.804	0.805	
	Mean	0.914	0.911		0.775	0.774		0.804	0.804	
	Std. Dev	0.006	0.009		0.003	0.003		0.001	0.001	
	Mean - 3 Sigma	0.895	0.885		0.767	0.766		0.802	0.800	
	Mean + 3 Sigma	0.933	0.937		0.783	0.782		0.805	0.808	
		>0.7V			<1.1V			>0.7V		



		SDIO_Vih_AV1.8_DV1.8		V	SDIO_Voh_50u_AV1.8_DV1.8		V	DCO_Voh_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.983	0.983		1.747	1.747		1.795	1.794		
	11	0.983	0.983		1.748	1.747		1.794	1.793		
	12	0.983	0.983		1.746	1.747		1.795	1.794		
	13	0.983	0.983		1.746	1.747		1.794	1.795		
	14	0.983	0.983		1.746	1.747		1.794	1.795		
	Min	0.983	0.983		1.746	1.747		1.794	1.793		
	Max	0.983	0.983		1.748	1.747		1.795	1.795		
	Mean	0.983	0.983		1.747	1.747		1.794	1.794		
	Std. Dev	0.000	0.000		0.001	0.000		0.000	0.001		
	Mean - 3 Sigma	0.983	0.983		1.744	1.747		1.793	1.791		
	Mean + 3 Sigma	0.983	0.983		1.750	1.747		1.796	1.797		
				<1.1V					>1.7V		

		OR_Voh_50u_AV1.8_DV1.8		V	D0_Voh_50u_AV1.8_DV1.8		V	D1_Voh_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.795	1.795		1.795	1.795		1.796	1.796		
	11	1.794	1.795		1.796	1.795		1.795	1.796		
	12	1.795	1.795		1.794	1.796		1.796	1.796		
	13	1.795	1.796		1.795	1.795		1.795	1.795		
	14	1.795	1.796		1.795	1.795		1.794	1.795		
	Min	1.794	1.795		1.794	1.795		1.794	1.795		
	Max	1.795	1.796		1.796	1.796		1.796	1.796		
	Mean	1.795	1.796		1.795	1.795		1.795	1.796		
	Std. Dev	0.000	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	1.793	1.794		1.793	1.794		1.793	1.794		
	Mean + 3 Sigma	1.796	1.797		1.797	1.797		1.797	1.797		
				>1.7V					>1.7V		

		D2_Voh_50u_AV1.8_DV1.8		V	D3_Voh_50u_AV1.8_DV1.8		V	D4_Voh_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.795	1.796		1.796	1.795		1.796	1.796		
	11	1.795	1.796		1.796	1.795		1.796	1.795		
	12	1.795	1.796		1.795	1.796		1.795	1.795		
	13	1.796	1.797		1.795	1.797		1.796	1.795		
	14	1.796	1.795		1.795	1.796		1.796	1.796		
	Min	1.795	1.795		1.795	1.795		1.795	1.795		
	Max	1.796	1.797		1.796	1.797		1.796	1.796		
	Mean	1.796	1.796		1.795	1.796		1.796	1.795		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	1.794	1.794		1.794	1.794		1.794	1.794		
	Mean + 3 Sigma	1.797	1.798		1.797	1.798		1.797	1.797		
				>1.7V					>1.7V		

		D5_Voh_50u_AV1.8_DV1.8		V	D6_Voh_50u_AV1.8_DV1.8		V	D7_Voh_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.796	1.795		1.795	1.792		1.796	1.794		
	11	1.796	1.796		1.796	1.792		1.795	1.794		
	12	1.796	1.796		1.796	1.792		1.796	1.795		
	13	1.796	1.796		1.795	1.792		1.796	1.794		
	14	1.795	1.796		1.796	1.792		1.795	1.794		
	Min	1.795	1.796		1.795	1.792		1.795	1.794		
	Max	1.796	1.796		1.796	1.792		1.796	1.795		
	Mean	1.796	1.796		1.796	1.792		1.796	1.794		
	Std. Dev	0.001	0.000		0.001	0.000		0.001	0.000		
	Mean - 3 Sigma	1.794	1.796		1.794	1.792		1.794	1.793		
	Mean + 3 Sigma	1.797	1.796		1.797	1.792		1.797	1.796		
				>1.7V					>1.7V		

		D8_Voh_50u_AV1.8_DV1.8		V	D9_Voh_50u_AV1.8_DV1.8		V	D10_Voh_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.796	1.795		1.796	1.795		1.795	1.796		
	11	1.796	1.796		1.795	1.796		1.796	1.796		
	12	1.797	1.795		1.796	1.796		1.795	1.796		
	13	1.796	1.795		1.796	1.796		1.795	1.796		
	14	1.796	1.795		1.795	1.797		1.796	1.796		
	Min	1.796	1.795		1.795	1.796		1.795	1.796		
	Max	1.797	1.796		1.796	1.797		1.796	1.796		
	Mean	1.796	1.795		1.796	1.796		1.796	1.796		
	Std. Dev	0.000	0.001		0.001	0.000		0.001	0.000		
	Mean - 3 Sigma	1.795	1.794		1.794	1.795		1.794	1.796		
	Mean + 3 Sigma	1.798	1.797		1.797	1.798		1.797	1.796		
				>1.7V					>1.7V		

		D11_Voh_50u_AV1.8_DV1.8		V	D12_Voh_50u_AV1.8_DV1.8		V	D13_Voh_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.795	1.796		1.795	1.796		1.795	1.795		
	11	1.795	1.796		1.794	1.795		1.794	1.796		
	12	1.795	1.795		1.795	1.795		1.794	1.795		
	13	1.795	1.796		1.796	1.795		1.795	1.796		
	14	1.795	1.795		1.795	1.795		1.795	1.794		
	Min	1.795	1.795		1.794	1.795		1.794	1.794		
	Max	1.795	1.796		1.796	1.795		1.795	1.796		
	Mean	1.795	1.795		1.795	1.795		1.795	1.795		
	Std. Dev	0.000	0.001		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	1.795	1.794		1.793	1.795		1.793	1.792		
	Mean + 3 Sigma	1.795	1.797		1.797	1.795		1.796	1.798		
				>1.7V					>1.7V		

		SDIO_Vol_50u_AV1.8_DV1.8		V	DCO_Vol_50u_AV1.8_DV1.8		V	OR_Vol_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.005	0.005		0.004	0.005		0.004	0.003		
	11	0.006	0.006		0.004	0.006		0.004	0.003		
	12	0.005	0.005		0.005	0.005		0.004	0.003		
	13	0.004	0.004		0.004	0.004		0.004	0.003		
	14	0.005	0.004		0.004	0.004		0.003	0.004		
	Min	0.004	0.004		0.004	0.004		0.003	0.003		
	Max	0.006	0.006		0.005	0.006		0.004	0.004		
	Mean	0.005	0.005		0.004	0.005		0.004	0.003		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.003	0.002		0.003	0.002		0.002	0.002		
	Mean + 3 Sigma	0.007	0.008		0.006	0.008		0.005	0.005		
				<0.05V					<0.05V		

		D0_Vol_50u_AV1.8_DV1.8		V	D1_Vol_50u_AV1.8_DV1.8		V	D2_Vol_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.005	0.004		0.004	0.005		0.005	0.004		
	11	0.005	0.005		0.005	0.005		0.004	0.004		
	12	0.005	0.004		0.005	0.004		0.003	0.004		
	13	0.005	0.005		0.004	0.004		0.004	0.004		
	14	0.004	0.003		0.005	0.005		0.003	0.004		
	Min	0.004	0.003		0.004	0.004		0.003	0.004		
	Max	0.005	0.005		0.005	0.005		0.004	0.004		
	Mean	0.005	0.004		0.005	0.005		0.004	0.004		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.000		
	Mean - 3 Sigma	0.003	0.001		0.003	0.003		0.002	0.004		
	Mean + 3 Sigma	0.006	0.007		0.006	0.006		0.005	0.004		
				<0.05V					<0.05V		

		D3_Vol_50u_AV1.8_DV1.8		V	D4_Vol_50u_AV1.8_DV1.8		V	D5_Vol_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.005	0.005		0.004	0.004		0.003	0.004		
	11	0.004	0.005		0.004	0.004		0.005	0.004		
	12	0.004	0.006		0.004	0.005		0.004	0.004		
	13	0.005	0.005		0.004	0.004		0.004	0.003		
	14	0.005	0.005		0.004	0.004		0.005	0.004		
	Min	0.004	0.005		0.004	0.004		0.004	0.003		
	Max	0.005	0.006		0.004	0.005		0.005	0.004		
	Mean	0.005	0.005		0.004	0.004		0.005	0.004		
	Std. Dev	0.001	0.001		0.000	0.001		0.001	0.001		
	Mean - 3 Sigma	0.003	0.004		0.004	0.003		0.003	0.002		
	Mean + 3 Sigma	0.006	0.007		0.004	0.006		0.006	0.005		
				<0.05V					<0.05V		

		D6_Vol_50u_AV1.8_DV1.8		V	D7_Vol_50u_AV1.8_DV1.8		V	D8_Vol_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.004		0.005	0.005		0.005	0.004		
	11	0.005	0.004		0.004	0.004		0.004	0.005		
	12	0.004	0.005		0.004	0.004		0.004	0.005		
	13	0.005	0.004		0.003	0.004		0.004	0.004		
	14	0.004	0.005		0.004	0.004		0.004	0.006		
	Min	0.004	0.004		0.003	0.004		0.004	0.004		
	Max	0.005	0.005		0.004	0.004		0.004	0.006		
	Mean	0.005	0.005		0.004	0.004		0.004	0.005		
	Std. Dev	0.001	0.001		0.001	0.000		0.000	0.001		
	Mean - 3 Sigma	0.003	0.003		0.002	0.004		0.004	0.003		
	Mean + 3 Sigma	0.006	0.006		0.005	0.004		0.004	0.007		
				<0.05V					<0.05V		

		D9_Vol_50u_AV1.8_DV1.8		V	D10_Vol_50u_AV1.8_DV1.8		V	D11_Vol_50u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.004		0.004	0.004		0.004	0.004		
	11	0.004	0.004		0.004	0.004		0.004	0.004		
	12	0.004	0.004		0.004	0.004		0.004	0.004		
	13	0.004	0.004		0.003	0.004		0.003	0.003		
	14	0.004	0.004		0.004	0.003		0.004	0.004		
	Min	0.004	0.004		0.003	0.003		0.003	0.003		
	Max	0.004	0.004		0.004	0.004		0.004	0.004		
	Mean	0.004	0.004		0.004	0.004		0.004	0.004		
	Std. Dev	0.000	0.000		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.004	0.004		0.002	0.002		0.002	0.002		
	Mean + 3 Sigma	0.004	0.004		0.005	0.005		0.005	0.005		
				<0.05V					<0.05V		

		D12_Vol_50u_AV1.8_DV1.8		V	D13_Vol_50u_AV1.8_DV1.8		V	SDIO_Voh_1mA_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.003	0.004		0.004	0.004		1.687	1.686		
	11	0.003	0.005		0.003	0.003		1.689	1.687		
	12	0.004	0.004		0.004	0.003		1.686	1.689		
	13	0.004	0.004		0.004	0.004		1.691	1.688		
	14	0.005	0.005		0.004	0.005		1.689	1.690		
	Min	0.003	0.004		0.003	0.003		1.686	1.687		
	Max	0.005	0.005		0.004	0.005		1.691	1.690		
	Mean	0.004	0.005		0.004	0.004		1.689	1.689		
	Std. Dev	0.001	0.001		0.001	0.001		0.002	0.001		
	Mean - 3 Sigma	0.002	0.003		0.002	0.001		1.683	1.685		
	Mean + 3 Sigma	0.006	0.006		0.005	0.007		1.695	1.692		
				<0.05V					<0.05V	>1.6V	

		DCO_Voh_1mA_AV1.8_DV1.8		V	OR_Voh_1mA_AV1.8_DV1.8		V	D0_Voh_1mA_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.728	1.727		1.729	1.728		1.729	1.728		
	11	1.727	1.727		1.727	1.727		1.728	1.726		
	12	1.729	1.729		1.728	1.727		1.729	1.729		
	13	1.729	1.730		1.728	1.728		1.729	1.728		
	14	1.729	1.729		1.727	1.728		1.729	1.728		
	Min	1.727	1.727		1.727	1.727		1.728	1.726		
	Max	1.729	1.730		1.728	1.728		1.729	1.729		
	Mean	1.729	1.729		1.728	1.728		1.729	1.728		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	1.726	1.725		1.726	1.726		1.727	1.724		
	Mean + 3 Sigma	1.732	1.733		1.729	1.729		1.730	1.732		
				>1.6V					>1.6V	>1.6V	

		D1_Voh_1mA_AV1.8_DV1.8		V	D2_Voh_1mA_AV1.8_DV1.8		V	D3_Voh_1mA_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.728	1.729		1.729	1.729		1.728	1.728		
	11	1.728	1.727		1.728	1.728		1.727	1.727		
	12	1.728	1.729		1.728	1.728		1.728	1.728		
	13	1.729	1.729		1.729	1.730		1.728	1.729		
	14	1.727	1.728		1.728	1.728		1.728	1.728		
	Min	1.727	1.727		1.728	1.728		1.727	1.727		
	Max	1.729	1.729		1.729	1.730		1.728	1.729		
	Mean	1.728	1.728		1.728	1.729		1.728	1.728		
	Std. Dev	0.001	0.001		0.001	0.001		0.000	0.001		
	Mean - 3 Sigma	1.726	1.725		1.727	1.726		1.726	1.726		
	Mean + 3 Sigma	1.730	1.731		1.730	1.732		1.729	1.730		
				>1.6V					>1.6V	>1.6V	

		D4_Voh_1mA_AV1.8_DV1.8		V	D5_Voh_1mA_AV1.8_DV1.8		V	D6_Voh_1mA_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.729	1.728		1.729	1.729		1.730	1.728		
	11	1.727	1.726		1.729	1.728		1.728	1.728		
	12	1.729	1.727		1.729	1.728		1.729	1.727		
	13	1.729	1.729		1.730	1.729		1.728	1.728		
	14	1.727	1.727		1.729	1.729		1.728	1.728		
	Min	1.727	1.726		1.729	1.728		1.728	1.727		
	Max	1.729	1.729		1.730	1.729		1.729	1.728		
	Mean	1.728	1.727		1.729	1.729		1.728	1.728		
	Std. Dev	0.001	0.001		0.000	0.001		0.001	0.000		
	Mean - 3 Sigma	1.725	1.723		1.728	1.727		1.727	1.726		
	Mean + 3 Sigma	1.731	1.731		1.731	1.730		1.730	1.729		
				>1.6V					>1.6V	>1.6V	

		D7_Voh_1mA_AV1.8_DV1.8		V	D8_Voh_1mA_AV1.8_DV1.8		V	D9_Voh_1mA_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.728	1.727		1.728	1.729		1.728	1.728		
	11	1.728	1.727		1.727	1.728		1.728	1.728		
	12	1.728	1.728		1.729	1.729		1.728	1.729		
	13	1.727	1.728		1.729	1.729		1.729	1.729		
	14	1.728	1.727		1.728	1.729		1.729	1.730		
	Min	1.727	1.727		1.727	1.728		1.728	1.728		
	Max	1.728	1.728		1.729	1.729		1.729	1.730		
	Mean	1.728	1.728		1.728	1.729		1.729	1.729		
	Std. Dev	0.000	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	1.726	1.726		1.725	1.727		1.727	1.727		
	Mean + 3 Sigma	1.729	1.729		1.731	1.730		1.730	1.731		
				>1.6V					>1.6V		

		D10_Voh_1mA_AV1.8_DV1.8		V	D11_Voh_1mA_AV1.8_DV1.8		V	D12_Voh_1mA_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.728	1.729		1.728	1.727		1.728	1.728		
	11	1.726	1.727		1.727	1.727		1.726	1.726		
	12	1.728	1.729		1.728	1.728		1.729	1.728		
	13	1.729	1.729		1.729	1.728		1.728	1.728		
	14	1.729	1.729		1.728	1.728		1.727	1.728		
	Min	1.726	1.727		1.727	1.727		1.726	1.726		
	Max	1.729	1.729		1.729	1.728		1.729	1.728		
	Mean	1.728	1.729		1.728	1.728		1.728	1.728		
	Std. Dev	0.001	0.001		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	1.724	1.726		1.726	1.726		1.724	1.725		
	Mean + 3 Sigma	1.732	1.732		1.730	1.729		1.731	1.731		
				>1.6V					>1.6V		

		D13_Voh_1mA_AV1.8_DV1.8		V	SDIO_Vol_500u_AV1.8_DV1.8		V	DCO_Vol_500u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	1.726	1.728		0.031	0.031		0.037	0.038		
	11	1.727	1.727		0.032	0.032		0.037	0.040		
	12	1.728	1.727		0.031	0.030		0.037	0.038		
	13	1.728	1.728		0.030	0.030		0.036	0.038		
	14	1.728	1.727		0.030	0.031		0.037	0.038		
	Min	1.727	1.727		0.030	0.030		0.036	0.038		
	Max	1.728	1.728		0.032	0.032		0.037	0.040		
	Mean	1.728	1.727		0.031	0.031		0.037	0.039		
	Std. Dev	0.000	0.000		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	1.726	1.726		0.028	0.028		0.035	0.036		
	Mean + 3 Sigma	1.729	1.729		0.034	0.034		0.038	0.042		
				>1.6V					<-0.2V		

		OR_Vol_500u_AV1.8_DV1.8		V	D0_Vol_500u_AV1.8_DV1.8		V	D1_Vol_500u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.037	0.037		0.038	0.037		0.038	0.038		
	11	0.038	0.038		0.038	0.037		0.037	0.038		
	12	0.038	0.037		0.037	0.037		0.038	0.039		
	13	0.037	0.037		0.037	0.036		0.037	0.037		
	14	0.037	0.037		0.037	0.038		0.037	0.038		
	Min	0.037	0.037		0.037	0.036		0.037	0.037		
	Max	0.038	0.038		0.038	0.038		0.038	0.039		
	Mean	0.038	0.037		0.037	0.037		0.037	0.038		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.036	0.036		0.036	0.035		0.036	0.036		
	Mean + 3 Sigma	0.039	0.039		0.039	0.039		0.039	0.040		
				<0.2V					<0.2V		

		D2_Vol_500u_AV1.8_DV1.8		V	D3_Vol_500u_AV1.8_DV1.8		V	D4_Vol_500u_AV1.8_DV1.8		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.037	0.038		0.038	0.038		0.038	0.038		
	11	0.038	0.038		0.039	0.038		0.039	0.039		
	12	0.037	0.038		0.038	0.038		0.038	0.037		
	13	0.037	0.038		0.037	0.038		0.037	0.037		
	14	0.038	0.038		0.038	0.038		0.038	0.039		
	Min	0.037	0.038		0.037	0.038		0.037	0.037		
	Max	0.038	0.038		0.039	0.038		0.039	0.039		
	Mean	0.038	0.038		0.038	0.038		0.038	0.038		
	Std. Dev	0.001	0.000		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	0.036	0.038		0.036	0.038		0.036	0.035		
	Mean + 3 Sigma	0.039	0.038		0.040	0.038		0.040	0.041		
				<0.2V					<0.2V		

		D5_Vol_500u_AV1.8_DV1.8		V	D6_Vol_500u_AV1.8_DV1.8		V	D7_Vol_500u_AV1.8_DV1.8		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.038	0.036		0.038	0.038		0.038	0.038	
	11	0.039	0.037		0.038	0.038		0.037	0.039	
	12	0.038	0.037		0.038	0.037		0.037	0.038	
	13	0.038	0.037		0.038	0.038		0.037	0.038	
	14	0.037	0.037		0.038	0.037		0.037	0.039	
	Min	0.037	0.037		0.038	0.037		0.037	0.038	
	Max	0.039	0.037		0.038	0.038		0.037	0.039	
	Mean	0.038	0.037		0.038	0.038		0.037	0.039	
	Std. Dev	0.001	0.000		0.000	0.001		0.000	0.001	
	Mean - 3 Sigma	0.036	0.037		0.038	0.036		0.037	0.037	
	Mean + 3 Sigma	0.040	0.037		0.038	0.039		0.037	0.040	
		<0.2V			<0.2V			<0.2V		

		D8_Vol_500u_AV1.8_DV1.8		V	D9_Vol_500u_AV1.8_DV1.8		V	D10_Vol_500u_AV1.8_DV1.8		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.038	0.038		0.039	0.038		0.038	0.037	
	11	0.038	0.039		0.037	0.039		0.038	0.038	
	12	0.038	0.037		0.038	0.038		0.038	0.038	
	13	0.037	0.037		0.037	0.038		0.036	0.036	
	14	0.038	0.037		0.038	0.037		0.037	0.037	
	Min	0.037	0.037		0.037	0.037		0.036	0.036	
	Max	0.038	0.039		0.038	0.039		0.038	0.038	
	Mean	0.038	0.038		0.038	0.038		0.037	0.037	
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001	
	Mean - 3 Sigma	0.036	0.035		0.036	0.036		0.034	0.034	
	Mean + 3 Sigma	0.039	0.041		0.039	0.040		0.040	0.040	
		<0.2V			<0.2V			<0.2V		

		D11_Vol_500u_AV1.8_DV1.8		V	D12_Vol_500u_AV1.8_DV1.8		V	D13_Vol_500u_AV1.8_DV1.8		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.037	0.038		0.038	0.038		0.037	0.037	
	11	0.038	0.039		0.038	0.038		0.038	0.038	
	12	0.038	0.038		0.038	0.037		0.038	0.038	
	13	0.037	0.037		0.037	0.038		0.037	0.037	
	14	0.037	0.037		0.037	0.039		0.037	0.038	
	Min	0.037	0.037		0.037	0.037		0.037	0.037	
	Max	0.038	0.039		0.038	0.039		0.038	0.038	
	Mean	0.038	0.038		0.038	0.038		0.038	0.038	
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001	
	Mean - 3 Sigma	0.036	0.035		0.036	0.036		0.036	0.036	
	Mean + 3 Sigma	0.039	0.041		0.039	0.040		0.039	0.039	
		<0.2V			<0.2V			<0.2V		

		SCLK_Vii_AV1.8_DV3.3		V	SCLK_Vih_AV1.8_DV3.3		V	CSB_Vii_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.793	0.793		0.941	0.941		0.859	0.866	
	11	0.797	0.797		0.944	0.945		0.856	0.864	
	12	0.789	0.772		0.949	0.949		0.858	0.868	
	13	0.768	0.779		0.944	0.944		0.861	0.865	
	14	0.795	0.794		0.948	0.947		0.863	0.867	
	Min	0.768	0.772		0.944	0.944		0.856	0.864	
	Max	0.797	0.797		0.949	0.949		0.863	0.868	
	Mean	0.787	0.786		0.946	0.946		0.860	0.866	
	Std. Dev	0.013	0.012		0.003	0.002		0.003	0.002	
	Mean - 3 Sigma	0.747	0.750		0.938	0.940		0.850	0.861	
	Mean + 3 Sigma	0.827	0.821		0.954	0.953		0.869	0.871	
		>0.7V			<1.1V			>0.7V		

		CSB_Vih_AV1.8_DV3.3		V	PWDN_Vii_AV1.8_DV3.3		V	PWDN_Vih_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.857	0.861		0.932	0.934		0.778	0.777	
	11	0.859	0.861		0.931	0.929		0.776	0.775	
	12	0.859	0.863		0.935	0.932		0.772	0.772	
	13	0.858	0.862		0.925	0.926		0.772	0.772	
	14	0.860	0.862		0.927	0.930		0.773	0.773	
	Min	0.858	0.861		0.925	0.926		0.772	0.772	
	Max	0.860	0.863		0.935	0.932		0.776	0.775	
	Mean	0.859	0.862		0.930	0.929		0.773	0.773	
	Std. Dev	0.001	0.001		0.004	0.003		0.002	0.001	
	Mean - 3 Sigma	0.857	0.860		0.916	0.922		0.768	0.769	
	Mean + 3 Sigma	0.861	0.864		0.943	0.937		0.779	0.777	
		<1.1V			>0.7V			<1.1V		

		OEB_Vii_AV1.8_DV3.3		V	OEB_Vih_AV1.8_DV3.3		V	SDIO_Vii_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.838	0.840		0.777	0.772		0.857	0.860		
	11	0.842	0.865		0.780	0.777		0.857	0.860		
	12	0.843	0.831		0.775	0.775		0.857	0.860		
	13	0.827	0.828		0.780	0.777		0.857	0.859		
	14	0.844	0.842		0.786	0.788		0.855	0.855		
	Min	0.827	0.828		0.775	0.775		0.855	0.855		
	Max	0.844	0.865		0.786	0.788		0.857	0.860		
	Mean	0.839	0.842		0.780	0.779		0.857	0.859		
	Std. Dev	0.008	0.017		0.005	0.006		0.001	0.002		
	Mean - 3 Sigma	0.815	0.791		0.767	0.762		0.854	0.851		
	Mean + 3 Sigma	0.863	0.892		0.794	0.797		0.860	0.866		
				>0.7V					<1.1V	>0.7V	

		SDIO_Vih_AV1.8_DV3.3		V	SDIO_Voh_50u_AV1.8_DV3.3		V	DCO_Voh_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.958	0.958		3.200	3.203		3.294	3.294		
	11	0.958	0.958		3.202	3.203		3.293	3.294		
	12	0.958	0.958		3.201	3.202		3.294	3.295		
	13	0.958	0.958		3.199	3.201		3.293	3.294		
	14	0.958	0.958		3.199	3.201		3.294	3.294		
	Min	0.958	0.958		3.199	3.201		3.293	3.294		
	Max	0.958	0.958		3.202	3.203		3.294	3.295		
	Mean	0.958	0.958		3.200	3.202		3.294	3.294		
	Std. Dev	0.000	0.000		0.002	0.001		0.001	0.000		
	Mean - 3 Sigma	0.958	0.958		3.196	3.199		3.292	3.293		
	Mean + 3 Sigma	0.958	0.958		3.205	3.205		3.295	3.296		
				<1.1V					>3.1V	>3.1V	

		OR_Voh_50u_AV1.8_DV3.3		V	D0_Voh_50u_AV1.8_DV3.3		V	D1_Voh_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.292	3.293		3.294	3.295		3.294	3.295		
	11	3.293	3.294		3.294	3.295		3.294	3.295		
	12	3.293	3.294		3.294	3.295		3.294	3.296		
	13	3.293	3.294		3.294	3.295		3.294	3.295		
	14	3.293	3.295		3.294	3.295		3.294	3.295		
	Min	3.293	3.294		3.294	3.295		3.294	3.295		
	Max	3.293	3.295		3.294	3.295		3.294	3.296		
	Mean	3.293	3.294		3.294	3.295		3.294	3.295		
	Std. Dev	0.000	0.000		0.000	0.000		0.000	0.000		
	Mean - 3 Sigma	3.293	3.293		3.294	3.295		3.294	3.294		
	Mean + 3 Sigma	3.293	3.296		3.294	3.295		3.294	3.297		
				>3.2V					>3.2V	>3.2V	

		D2_Voh_50u_AV1.8_DV3.3		V	D3_Voh_50u_AV1.8_DV3.3		V	D4_Voh_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.294	3.294		3.294	3.295		3.295	3.295		
	11	3.294	3.296		3.294	3.294		3.294	3.294		
	12	3.294	3.296		3.293	3.296		3.295	3.295		
	13	3.296	3.295		3.295	3.294		3.294	3.294		
	14	3.294	3.295		3.294	3.294		3.294	3.295		
	Min	3.294	3.295		3.293	3.294		3.294	3.294		
	Max	3.296	3.296		3.295	3.296		3.295	3.295		
	Mean	3.295	3.296		3.294	3.295		3.294	3.295		
	Std. Dev	0.001	0.001		0.001	0.001		0.000	0.001		
	Mean - 3 Sigma	3.292	3.294		3.292	3.292		3.293	3.293		
	Mean + 3 Sigma	3.298	3.297		3.296	3.298		3.296	3.296		
				>3.2V					>3.2V	>3.2V	

		D5_Voh_50u_AV1.8_DV3.3		V	D6_Voh_50u_AV1.8_DV3.3		V	D7_Voh_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.295	3.295		3.294	3.295		3.294	3.295		
	11	3.295	3.295		3.294	3.296		3.294	3.295		
	12	3.295	3.295		3.295	3.295		3.295	3.294		
	13	3.295	3.296		3.294	3.296		3.294	3.295		
	14	3.295	3.295		3.295	3.296		3.295	3.294		
	Min	3.295	3.295		3.294	3.295		3.294	3.294		
	Max	3.295	3.296		3.295	3.296		3.295	3.295		
	Mean	3.295	3.295		3.295	3.296		3.295	3.295		
	Std. Dev	0.000	0.000		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	3.295	3.294		3.293	3.294		3.293	3.293		
	Mean + 3 Sigma	3.295	3.297		3.296	3.297		3.296	3.296		
				>3.2V					>3.2V	>3.2V	

		D8_Voh_50u_AV1.8_DV3.3		V	D9_Voh_50u_AV1.8_DV3.3		V	D10_Voh_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.294	3.294		3.294	3.295		3.294	3.295		
	11	3.294	3.295		3.295	3.295		3.293	3.295		
	12	3.295	3.295		3.295	3.295		3.294	3.295		
	13	3.294	3.295		3.294	3.295		3.294	3.296		
	14	3.294	3.296		3.294	3.296		3.294	3.296		
	Min	3.294	3.295		3.294	3.295		3.293	3.295		
	Max	3.295	3.296		3.295	3.296		3.294	3.296		
	Mean	3.294	3.295		3.295	3.295		3.294	3.296		
	Std. Dev	0.000	0.000		0.001	0.000		0.000	0.001		
	Mean - 3 Sigma	3.293	3.294		3.293	3.294		3.292	3.294		
	Mean + 3 Sigma	3.296	3.297		3.296	3.297		3.295	3.297		
				>3.2V					>3.2V		

		D11_Voh_50u_AV1.8_DV3.3		V	D12_Voh_50u_AV1.8_DV3.3		V	D13_Voh_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.294	3.295		3.294	3.295		3.294	3.293		
	11	3.294	3.294		3.293	3.295		3.293	3.295		
	12	3.293	3.294		3.293	3.294		3.294	3.294		
	13	3.294	3.295		3.293	3.294		3.293	3.294		
	14	3.293	3.295		3.294	3.294		3.293	3.294		
	Min	3.293	3.294		3.293	3.294		3.293	3.294		
	Max	3.294	3.295		3.294	3.295		3.294	3.295		
	Mean	3.294	3.295		3.293	3.294		3.293	3.294		
	Std. Dev	0.001	0.001		0.000	0.000		0.000	0.000		
	Mean - 3 Sigma	3.292	3.293		3.292	3.293		3.292	3.293		
	Mean + 3 Sigma	3.295	3.296		3.295	3.296		3.295	3.296		
				>3.2V					>3.2V		

		SDIO_Vol_50u_AV1.8_DV3.3		V	DCO_Vol_50u_AV1.8_DV3.3		V	OR_Vol_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.002	0.002		0.005	0.005		0.003	0.003		
	11	0.004	0.003		0.004	0.004		0.003	0.003		
	12	0.002	0.002		0.004	0.006		0.002	0.002		
	13	0.000	0.000		0.005	0.004		0.004	0.003		
	14	0.000	0.000		0.005	0.004		0.001	0.002		
	Min	0.000	0.000		0.004	0.004		0.001	0.002		
	Max	0.004	0.003		0.005	0.006		0.004	0.003		
	Mean	0.001	0.001		0.005	0.005		0.003	0.003		
	Std. Dev	0.002	0.002		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	-0.004	-0.003		0.003	0.002		-0.001	0.001		
	Mean + 3 Sigma	0.007	0.006		0.006	0.008		0.006	0.004		
				<0.05V					<0.05V		

		D0_Vol_50u_AV1.8_DV3.3		V	D1_Vol_50u_AV1.8_DV3.3		V	D2_Vol_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.005	0.004		0.004	0.004		0.003	0.003		
	11	0.004	0.004		0.003	0.004		0.003	0.004		
	12	0.004	0.004		0.004	0.005		0.003	0.004		
	13	0.005	0.004		0.004	0.004		0.003	0.004		
	14	0.005	0.004		0.005	0.005		0.004	0.003		
	Min	0.004	0.004		0.003	0.004		0.003	0.003		
	Max	0.005	0.004		0.005	0.005		0.004	0.004		
	Mean	0.005	0.004		0.004	0.005		0.003	0.004		
	Std. Dev	0.001	0.000		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.003	0.004		0.002	0.003		0.002	0.002		
	Mean + 3 Sigma	0.006	0.004		0.006	0.006		0.005	0.005		
				<0.05V					<0.05V		

		D3_Vol_50u_AV1.8_DV3.3		V	D4_Vol_50u_AV1.8_DV3.3		V	D5_Vol_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.003	0.003		0.005	0.003		0.004	0.003		
	11	0.004	0.004		0.003	0.004		0.004	0.003		
	12	0.004	0.004		0.004	0.003		0.004	0.003		
	13	0.004	0.003		0.003	0.003		0.005	0.003		
	14	0.003	0.004		0.004	0.005		0.005	0.003		
	Min	0.003	0.003		0.003	0.003		0.004	0.003		
	Max	0.004	0.004		0.004	0.005		0.005	0.003		
	Mean	0.004	0.004		0.004	0.004		0.005	0.003		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.000		
	Mean - 3 Sigma	0.002	0.002		0.002	0.001		0.003	0.003		
	Mean + 3 Sigma	0.005	0.005		0.005	0.007		0.006	0.003		
				<0.05V					<0.05V		

		D6_Vol_50u_AV1.8_DV3.3		V	D7_Vol_50u_AV1.8_DV3.3		V	D8_Vol_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.003		0.004	0.004		0.003	0.005		
	11	0.003	0.004		0.003	0.003		0.004	0.003		
	12	0.004	0.004		0.005	0.004		0.004	0.004		
	13	0.004	0.004		0.003	0.004		0.004	0.003		
	14	0.004	0.004		0.003	0.004		0.004	0.004		
	Min	0.003	0.004		0.003	0.003		0.004	0.003		
	Max	0.004	0.004		0.005	0.004		0.004	0.004		
	Mean	0.004	0.004		0.004	0.004		0.004	0.004		
	Std. Dev	0.001	0.000		0.001	0.001		0.000	0.001		
	Mean - 3 Sigma	0.002	0.004		0.001	0.002		0.004	0.002		
	Mean + 3 Sigma	0.005	0.004		0.007	0.005		0.004	0.005		
				<0.05V					<0.05V		

		D9_Vol_50u_AV1.8_DV3.3		V	D10_Vol_50u_AV1.8_DV3.3		V	D11_Vol_50u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.004		0.003	0.004		0.004	0.003		
	11	0.004	0.004		0.003	0.003		0.004	0.003		
	12	0.003	0.003		0.003	0.004		0.003	0.003		
	13	0.004	0.004		0.003	0.004		0.003	0.003		
	14	0.003	0.004		0.003	0.004		0.004	0.003		
	Min	0.003	0.003		0.003	0.003		0.003	0.003		
	Max	0.004	0.004		0.003	0.004		0.004	0.003		
	Mean	0.004	0.004		0.003	0.004		0.004	0.003		
	Std. Dev	0.001	0.001		0.000	0.001		0.001	0.000		
	Mean - 3 Sigma	0.002	0.002		0.003	0.002		0.002	0.003		
	Mean + 3 Sigma	0.005	0.005		0.003	0.005		0.005	0.003		
				<0.05V					<0.05V		

		D12_Vol_50u_AV1.8_DV3.3		V	D13_Vol_50u_AV1.8_DV3.3		V	SDIO_Voh_1mA_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.002	0.003		0.002	0.003		3.157	3.158		
	11	0.003	0.003		0.003	0.003		3.154	3.158		
	12	0.002	0.002		0.003	0.002		3.160	3.161		
	13	0.003	0.003		0.003	0.003		3.155	3.158		
	14	0.002	0.004		0.002	0.002		3.157	3.158		
	Min	0.002	0.002		0.002	0.002		3.154	3.158		
	Max	0.003	0.004		0.003	0.003		3.160	3.161		
	Mean	0.003	0.003		0.003	0.003		3.157	3.159		
	Std. Dev	0.001	0.001		0.001	0.001		0.003	0.002		
	Mean - 3 Sigma	0.001	0.001		0.001	0.001		3.149	3.154		
	Mean + 3 Sigma	0.004	0.005		0.004	0.004		3.164	3.163		
				<0.05V					<0.05V	>3.05V	

		DCO_Voh_1mA_AV1.8_DV3.3		V	OR_Voh_1mA_AV1.8_DV3.3		V	D0_Voh_1mA_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.241	3.241		3.237	3.240		3.240	3.239		
	11	3.240	3.240		3.237	3.238		3.239	3.239		
	12	3.243	3.243		3.239	3.239		3.240	3.240		
	13	3.242	3.243		3.238	3.240		3.241	3.242		
	14	3.242	3.241		3.238	3.238		3.239	3.240		
	Min	3.240	3.240		3.237	3.238		3.239	3.239		
	Max	3.243	3.243		3.239	3.240		3.241	3.242		
	Mean	3.242	3.242		3.238	3.239		3.240	3.240		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	3.238	3.237		3.236	3.236		3.237	3.236		
	Mean + 3 Sigma	3.246	3.246		3.240	3.242		3.243	3.244		
				>3.1V					>3.1V	>3.1V	

		D1_Voh_1mA_AV1.8_DV3.3		V	D2_Voh_1mA_AV1.8_DV3.3		V	D3_Voh_1mA_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	3.239	3.239		3.239	3.240		3.239	3.241		
	11	3.240	3.240		3.238	3.240		3.239	3.240		
	12	3.239	3.241		3.239	3.240		3.239	3.239		
	13	3.239	3.242		3.240	3.241		3.240	3.241		
	14	3.239	3.241		3.239	3.240		3.238	3.240		
	Min	3.239	3.240		3.238	3.240		3.238	3.239		
	Max	3.240	3.242		3.240	3.241		3.240	3.241		
	Mean	3.239	3.241		3.239	3.240		3.239	3.240		
	Std. Dev	0.001	0.001		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	3.238	3.239		3.237	3.239		3.237	3.238		
	Mean + 3 Sigma	3.241	3.243		3.241	3.242		3.241	3.242		
				>3.1V					>3.1V	>3.1V	



		D4_Voh_1mA_AV1.8_DV3.3		V	D5_Voh_1mA_AV1.8_DV3.3		V	D6_Voh_1mA_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.240	3.240		3.240	3.240		3.240	3.240	
	11	3.239	3.239		3.240	3.240		3.239	3.240	
	12	3.239	3.239		3.241	3.240		3.239	3.240	
	13	3.240	3.240		3.240	3.240		3.239	3.240	
	14	3.239	3.238		3.239	3.241		3.239	3.240	
	Min	3.239	3.238		3.239	3.240		3.239	3.240	
	Max	3.240	3.240		3.241	3.241		3.239	3.240	
	Mean	3.239	3.239		3.240	3.240		3.239	3.240	
	Std. Dev	0.001	0.001		0.001	0.000		0.000	0.000	
	Mean - 3 Sigma	3.238	3.237		3.238	3.239		3.239	3.240	
	Mean + 3 Sigma	3.241	3.241		3.242	3.242		3.239	3.240	
		>3.1V			>3.1V			>3.1V		

		D7_Voh_1mA_AV1.8_DV3.3		V	D8_Voh_1mA_AV1.8_DV3.3		V	D9_Voh_1mA_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.238	3.240		3.239	3.241		3.238	3.240	
	11	3.239	3.239		3.240	3.240		3.240	3.239	
	12	3.238	3.239		3.240	3.240		3.238	3.240	
	13	3.239	3.239		3.241	3.240		3.241	3.240	
	14	3.239	3.239		3.240	3.241		3.239	3.241	
	Min	3.238	3.239		3.240	3.240		3.238	3.239	
	Max	3.239	3.239		3.241	3.241		3.241	3.241	
	Mean	3.239	3.239		3.240	3.240		3.240	3.240	
	Std. Dev	0.000	0.000		0.000	0.000		0.001	0.001	
	Mean - 3 Sigma	3.237	3.239		3.239	3.239		3.236	3.238	
	Mean + 3 Sigma	3.240	3.239		3.242	3.242		3.243	3.242	
		>3.1V			>3.1V			>3.1V		

		D10_Voh_1mA_AV1.8_DV3.3		V	D11_Voh_1mA_AV1.8_DV3.3		V	D12_Voh_1mA_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.240	3.240		3.237	3.239		3.238	3.239	
	11	3.239	3.240		3.237	3.239		3.237	3.237	
	12	3.240	3.241		3.238	3.239		3.239	3.239	
	13	3.241	3.240		3.239	3.240		3.239	3.239	
	14	3.240	3.241		3.238	3.240		3.238	3.239	
	Min	3.239	3.240		3.237	3.239		3.237	3.237	
	Max	3.241	3.241		3.239	3.240		3.239	3.239	
	Mean	3.240	3.241		3.238	3.240		3.238	3.239	
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001	
	Mean - 3 Sigma	3.238	3.239		3.236	3.238		3.235	3.236	
	Mean + 3 Sigma	3.242	3.242		3.240	3.241		3.241	3.242	
		>3.1V			>3.1V			>3.1V		

		D13_Voh_1mA_AV1.8_DV3.3		V	SDIO_Vol_500u_AV1.8_DV3.3		V	DCO_Vol_500u_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	3.237	3.239		0.051	0.052		0.031	0.032	
	11	3.237	3.238		0.054	0.054		0.032	0.033	
	12	3.238	3.238		0.051	0.051		0.031	0.032	
	13	3.239	3.239		0.048	0.049		0.031	0.031	
	14	3.237	3.239		0.048	0.049		0.031	0.031	
	Min	3.237	3.238		0.048	0.049		0.031	0.031	
	Max	3.239	3.239		0.054	0.054		0.032	0.033	
	Mean	3.238	3.239		0.050	0.051		0.031	0.032	
	Std. Dev	0.001	0.001		0.003	0.002		0.001	0.001	
	Mean - 3 Sigma	3.235	3.237		0.042	0.044		0.030	0.029	
	Mean + 3 Sigma	3.241	3.240		0.059	0.058		0.033	0.035	
		>3.1V			<-0.2V			<-0.2V		

		OR_Vol_500u_AV1.8_DV3.3		V	D0_Vol_500u_AV1.8_DV3.3		V	D1_Vol_500u_AV1.8_DV3.3		V
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.030	0.029		0.032	0.031		0.031	0.032	
	11	0.030	0.031		0.032	0.031		0.033	0.032	
	12	0.029	0.029		0.032	0.032		0.031	0.031	
	13	0.030	0.030		0.030	0.031		0.031	0.032	
	14	0.030	0.030		0.032	0.032		0.031	0.031	
	Min	0.029	0.029		0.030	0.031		0.031	0.031	
	Max	0.030	0.031		0.032	0.032		0.033	0.032	
	Mean	0.030	0.030		0.032	0.032		0.032	0.032	
	Std. Dev	0.000	0.001		0.001	0.001		0.001	0.001	
	Mean - 3 Sigma	0.028	0.028		0.029	0.030		0.029	0.030	
	Mean + 3 Sigma	0.031	0.032		0.035	0.033		0.035	0.033	
		<0.2V			<-0.2V			<-0.2V		

		D2_Vol_500u_AV1.8_DV3.3		V	D3_Vol_500u_AV1.8_DV3.3		V	D4_Vol_500u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.032	0.031		0.031	0.031		0.031	0.031		
	11	0.032	0.031		0.032	0.032		0.032	0.031		
	12	0.031	0.032		0.031	0.032		0.031	0.031		
	13	0.031	0.032		0.031	0.030		0.031	0.031		
	14	0.031	0.032		0.030	0.032		0.031	0.031		
	Min	0.031	0.031		0.030	0.030		0.031	0.031		
	Max	0.032	0.032		0.032	0.032		0.032	0.031		
	Mean	0.031	0.032		0.031	0.032		0.031	0.031		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.000		
	Mean - 3 Sigma	0.030	0.030		0.029	0.029		0.030	0.031		
	Mean + 3 Sigma	0.033	0.033		0.033	0.035		0.033	0.031		
				<0.2V					<0.2V		

		D5_Vol_500u_AV1.8_DV3.3		V	D6_Vol_500u_AV1.8_DV3.3		V	D7_Vol_500u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.031	0.031		0.032	0.032		0.031	0.031		
	11	0.031	0.031		0.033	0.032		0.032	0.033		
	12	0.032	0.032		0.032	0.032		0.032	0.031		
	13	0.031	0.031		0.032	0.032		0.032	0.032		
	14	0.031	0.030		0.031	0.034		0.031	0.032		
	Min	0.031	0.030		0.031	0.032		0.031	0.031		
	Max	0.032	0.032		0.033	0.034		0.032	0.033		
	Mean	0.031	0.031		0.032	0.033		0.032	0.032		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.030	0.029		0.030	0.030		0.030	0.030		
	Mean + 3 Sigma	0.033	0.033		0.034	0.036		0.033	0.034		
				<0.2V					<0.2V		

		D8_Vol_500u_AV1.8_DV3.3		V	D9_Vol_500u_AV1.8_DV3.3		V	D10_Vol_500u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.031	0.031		0.031	0.031		0.031	0.031		
	11	0.032	0.032		0.032	0.031		0.032	0.032		
	12	0.032	0.031		0.032	0.031		0.031	0.032		
	13	0.031	0.031		0.031	0.032		0.031	0.031		
	14	0.031	0.032		0.032	0.031		0.030	0.031		
	Min	0.031	0.031		0.031	0.031		0.030	0.031		
	Max	0.032	0.032		0.032	0.032		0.032	0.032		
	Mean	0.032	0.032		0.032	0.031		0.031	0.032		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.030	0.030		0.030	0.030		0.029	0.030		
	Mean + 3 Sigma	0.033	0.033		0.033	0.033		0.033	0.033		
				<0.2V					<0.2V		

		D11_Vol_500u_AV1.8_DV3.3		V	D12_Vol_500u_AV1.8_DV3.3		V	D13_Vol_500u_AV1.8_DV3.3		V	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.032	0.031		0.031	0.031		0.032	0.030		
	11	0.031	0.031		0.031	0.030		0.031	0.032		
	12	0.031	0.031		0.031	0.030		0.032	0.031		
	13	0.030	0.031		0.030	0.030		0.030	0.031		
	14	0.031	0.031		0.030	0.031		0.031	0.031		
	Min	0.030	0.031		0.030	0.030		0.030	0.031		
	Max	0.031	0.031		0.031	0.031		0.032	0.032		
	Mean	0.031	0.031		0.031	0.030		0.031	0.031		
	Std. Dev	0.001	0.000		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.029	0.031		0.029	0.029		0.029	0.030		
	Mean + 3 Sigma	0.032	0.031		0.032	0.032		0.033	0.033		
				<0.2V					<0.2V		

		I_avdd_DC_AV1.7_DV1.8		mA	I_drvdd_DC_AV1.7_DV1.8		mA	power_DC_AV1.7_DV1.8		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	145.032	144.915		0.126	0.112		246.781	246.557		
	11	141.553	141.724		0.122	0.106		240.859	241.122		
	12	146.398	146.621		0.202	0.109		249.240	249.452		
	13	143.460	143.445		0.122	0.180		244.102	244.180		
	14	142.991	143.170		0.114	0.177		243.289	243.706		
	Min	141.553	141.724		0.114	0.106		240.859	241.122		
	Max	146.398	146.621		0.202	0.180		249.240	249.452		
	Mean	143.600	143.740		0.140	0.143		244.373	244.615		
	Std. Dev	2.034	2.064		0.041	0.041		3.525	3.494		
	Mean - 3 Sigma	137.498	137.549		0.016	0.020		233.797	234.135		
	Mean + 3 Sigma	149.702	149.931		0.264	0.266		254.948	255.096		
				<200mA					<5mA	<425mW	

		Iq_avdd_125M_AV1.7_DV1.8		mA	Iq_drvdd_125M_AV1.7_DV1.8		mA	power_125M_AV1.7_DV1.8		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	237.959	238.275		51.930	54.068		498.003	502.390		
	11	233.913	234.010		52.271	52.409		491.739	492.154		
	12	240.156	240.556		52.089	50.195		502.026	499.296		
	13	237.192	237.571		53.274	54.069		499.119	501.195		
	14	236.782	237.257		55.470	52.533		502.375	497.896		
	Min	233.913	234.010		52.089	50.195		491.739	492.154		
	Max	240.156	240.556		55.470	54.069		502.375	501.195		
	Mean	237.011	237.348		53.276	52.302		498.815	497.635		
	Std. Dev	2.555	2.676		1.553	1.594		4.938	3.896		
	Mean - 3 Sigma	229.347	229.320		48.618	47.519		484.001	485.946		
	Mean + 3 Sigma	244.674	245.377		57.934	57.085		513.629	509.324		
				<350mA				<175mA			<800mW

		Iq_avdd_OEoff_AV1.7_DV1.8		mA	Iq_drvdd_OEoff_AV1.7_DV1.8		mA	power_OEoff_AV1.7_DV1.8		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	237.994	238.345		1.233	1.295		406.809	407.517		
	11	233.934	234.053		1.261	1.262		399.957	400.162		
	12	240.181	240.563		1.243	1.223		410.545	411.159		
	13	237.240	237.630		1.296	1.291		405.640	406.296		
	14	236.856	237.284		1.368	1.291		405.118	405.706		
	Min	233.934	234.053		1.243	1.223		399.957	400.162		
	Max	240.181	240.563		1.368	1.291		410.545	411.159		
	Mean	237.053	237.382		1.292	1.267		405.315	405.831		
	Std. Dev	2.555	2.663		0.055	0.032		4.328	4.500		
	Mean - 3 Sigma	229.388	229.393		1.126	1.170		392.331	392.330		
	Mean + 3 Sigma	244.718	245.371		1.458	1.364		418.299	419.331		
				<300mA				<5mA			<560mW

		Iq_avdd_AV1.7_DV1.8		mA	Iq_drvdd_AV1.7_DV1.8		mA	power_PWDN_AV1.7_DV1.8		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.121	0.120		0.050	0.052		0.296	0.297		
	11	0.118	0.119		0.052	0.052		0.294	0.297		
	12	0.355	0.367		0.052	0.052		0.697	0.718		
	13	0.120	0.121		0.052	0.053		0.297	0.301		
	14	0.148	0.147		0.053	0.052		0.347	0.345		
	Min	0.118	0.119		0.052	0.052		0.294	0.297		
	Max	0.355	0.367		0.053	0.053		0.697	0.718		
	Mean	0.185	0.189		0.052	0.052		0.409	0.415		
	Std. Dev	0.114	0.119		0.001	0.000		0.194	0.203		
	Mean - 3 Sigma	-0.156	-0.170		0.050	0.051		-0.172	-0.194		
	Mean + 3 Sigma	0.526	0.547		0.054	0.053		0.989	1.024		
				<5mA				<5mA			<25mW

		Iq_avdd_StdBy_AV1.7_DV1.8		mA	Iq_drvdd_StdBy_AV1.7_DV1.8		mA	power_StdBy_AV1.7_DV1.8		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	40.725	40.642		0.054	0.052		69.330	69.184		
	11	40.415	40.291		0.053	0.052		68.800	68.589		
	12	41.027	40.928		0.053	0.052		69.842	69.672		
	13	40.758	40.636		0.054	0.053		69.385	69.176		
	14	41.086	40.991		0.053	0.052		69.942	69.779		
	Min	40.415	40.291		0.053	0.052		68.800	68.589		
	Max	41.086	40.991		0.054	0.053		69.942	69.779		
	Mean	40.821	40.711		0.053	0.052		69.492	69.304		
	Std. Dev	0.307	0.320		0.001	0.000		0.522	0.545		
	Mean - 3 Sigma	39.902	39.750		0.051	0.051		67.928	67.670		
	Mean + 3 Sigma	41.741	41.673		0.055	0.054		71.057	70.937		
				<100mA				<5mA			<200mW

		I_avdd_DC_AV1.9_DV3.3		mA	I_drvdd_DC_AV1.9_DV3.3		mA	power_DC_AV1.9_DV3.3		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	149.614	150.663		1.205	0.688		288.244	288.532		
	11	146.612	146.922		0.641	1.217		280.679	283.170		
	12	150.657	151.041		0.347	0.751		287.395	289.456		
	13	147.568	147.808		0.805	0.624		283.034	282.896		
	14	146.834	147.073		0.142	0.735		279.452	281.863		
	Min	146.612	146.922		0.142	0.624		279.452	281.863		
	Max	150.657	151.041		0.805	1.217		287.395	289.456		
	Mean	147.918	148.211		0.484	0.832		282.640	284.346		
	Std. Dev	1.871	1.926		0.296	0.263		3.501	3.453		
	Mean - 3 Sigma	142.304	142.433		-0.405	0.042		272.138	273.988		
	Mean + 3 Sigma	153.531	153.989		1.373	1.621		293.142	294.705		
				<200mA				<5mA			<425mW

		Iq_avdd_125M_AV1.9_DV3.3		mA	Iq_drvdd_125M_AV1.9_DV3.3		mA	power_125M_AV1.9_DV3.3		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	246.630	246.737		110.815	115.362		834.285	849.496		
	11	242.472	242.891		112.543	116.094		832.088	844.602		
	12	248.957	249.231		115.951	111.676		855.654	842.068		
	13	246.137	246.603		114.907	118.583		846.854	859.871		
	14	245.404	246.009		116.576	116.924		850.969	853.266		
	Min	242.472	242.891		112.543	111.676		832.088	842.068		
	Max	248.957	249.231		116.576	118.583		855.654	859.871		
	Mean	245.742	246.183		114.994	115.819		846.391	849.952		
	Std. Dev	2.665	2.604		1.773	2.950		10.191	8.168		
	Mean - 3 Sigma	237.748	238.373		109.675	106.970		815.819	825.448		
	Mean + 3 Sigma	253.736	253.994		120.314	124.669		876.964	874.455		
				<350mA				<175mA			<975mW

		Iq_avdd_OEoff_AV1.9_DV3.3		mA	Iq_drvdd_OEoff_AV1.9_DV3.3		mA	power_OEoff_AV1.9_DV3.3		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	246.593	246.827		2.360	2.440		476.313	477.023		
	11	242.502	242.886		2.502	2.525		469.009	469.816		
	12	248.991	249.259		2.513	2.414		481.375	481.559		
	13	246.187	246.679		2.544	2.550		476.153	477.103		
	14	245.413	246.100		2.553	2.553		474.710	476.016		
	Min	242.502	242.886		2.502	2.414		469.009	469.816		
	Max	248.991	249.259		2.553	2.553		481.375	481.559		
	Mean	245.773	246.231		2.528	2.511		475.312	476.123		
	Std. Dev	2.668	2.619		0.024	0.065		5.084	4.841		
	Mean - 3 Sigma	237.769	238.374		2.455	2.314		460.058	461.601		
	Mean + 3 Sigma	253.778	254.087		2.601	2.707		490.565	490.646		
				<300mA				<5mA			<560mW

		Iq_avdd_AV1.9_DV3.3		mA	Iq_drvdd_AV1.9_DV3.3		mA	power_PWDN_AV1.9_DV3.3		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.154	0.154		0.107	0.108		0.647	0.649		
	11	0.142	0.146		0.108	0.108		0.627	0.633		
	12	0.531	0.533		0.108	0.107		1.363	1.364		
	13	0.144	0.137		0.110	0.110		0.635	0.623		
	14	0.220	0.232		0.109	0.109		0.778	0.801		
	Min	0.142	0.137		0.108	0.107		0.627	0.623		
	Max	0.531	0.533		0.110	0.110		1.363	1.364		
	Mean	0.259	0.262		0.109	0.108		0.851	0.855		
	Std. Dev	0.185	0.186		0.001	0.001		0.349	0.349		
	Mean - 3 Sigma	-0.294	-0.295		0.106	0.104		-0.195	-0.191		
	Mean + 3 Sigma	0.813	0.819		0.111	0.112		1.897	1.902		
				<5mA				<5mA			<25mW

		Iq_avdd_StndBy_AV1.9_DV3.3		mA	Iq_drvdd_StndBy_AV1.9_DV3.3		mA	power_StndBy_AV1.9_DV3.3		mW	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	42.142	42.078		0.107	0.108		80.424	80.302		
	11	41.830	41.734		0.107	0.107		79.832	79.650		
	12	42.429	42.328		0.108	0.108		80.969	80.778		
	13	42.234	42.116		0.110	0.109		80.607	80.382		
	14	42.529	42.430		0.108	0.109		81.163	80.975		
	Min	41.830	41.734		0.107	0.107		79.832	79.650		
	Max	42.529	42.430		0.110	0.109		81.163	80.975		
	Mean	42.256	42.152		0.108	0.108		80.643	80.446		
	Std. Dev	0.309	0.307		0.001	0.001		0.588	0.585		
	Mean - 3 Sigma	41.329	41.230		0.105	0.106		78.880	78.691		
	Mean + 3 Sigma	43.182	43.074		0.111	0.111		82.406	82.201		
				<100mA				<5mA			<200mW

		IIL_d0_0V		uA	IIL_d1_0V		uA	IIL_d10_0V		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.002	-0.002		-0.002	-0.003		-0.003	-0.003		
	11	-0.003	-0.004		-0.003	-0.004		-0.003	-0.004		
	12	-0.002	-0.005		-0.003	-0.005		-0.003	-0.004		
	13	-0.003	-0.004		-0.003	-0.004		-0.003	-0.005		
	14	-0.002	-0.004		-0.002	-0.002		-0.002	-0.005		
	Min	-0.003	-0.005		-0.003	-0.005		-0.003	-0.005		
	Max	-0.002	-0.004		-0.002	-0.002		-0.002	-0.004		
	Mean	-0.003	-0.004		-0.003	-0.004		-0.003	-0.005		
	Std. Dev	0.000	0.001		0.001	0.001		0.000	0.000		
	Mean - 3 Sigma	-0.004	-0.006		-0.005	-0.007		-0.004	-0.006		
	Mean + 3 Sigma	-0.001	-0.002		-0.001	-0.001		-0.002	-0.003		
				+/-10uA				+/-10uA			+/-10uA

		iIL_d11_0V		uA	iIL_d12_0V		uA	iIL_d13_0V		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.002	-0.002		-0.001	-0.003		-0.002	-0.003		
	11	-0.003	-0.005		-0.002	-0.004		-0.004	-0.004		
	12	-0.002	-0.003		-0.002	-0.004		-0.004	-0.003		
	13	-0.004	-0.005		-0.003	-0.003		-0.003	-0.003		
	14	-0.003	-0.004		-0.004	-0.003		-0.003	-0.004		
	Min	-0.004	-0.005		-0.004	-0.004		-0.004	-0.004		
	Max	-0.002	-0.003		-0.002	-0.003		-0.003	-0.003		
	Mean	-0.003	-0.005		-0.003	-0.004		-0.003	-0.004		
	Std. Dev	0.001	0.001		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	-0.006	-0.007		-0.005	-0.005		-0.005	-0.005		
	Mean + 3 Sigma	0.000	-0.002		0.000	-0.002		-0.002	-0.002		
				+/-10uA					+/-10uA		

		iIL_d2_0V		uA	iIL_d3_0V		uA	iIL_d4_0V		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.003	-0.002		-0.003	-0.003		-0.001	-0.002		
	11	-0.001	-0.005		-0.002	-0.005		-0.002	-0.004		
	12	-0.002	-0.005		-0.003	-0.005		-0.002	-0.005		
	13	-0.005	-0.004		-0.003	-0.004		-0.003	-0.004		
	14	-0.002	-0.003		-0.002	-0.005		-0.002	-0.006		
	Min	-0.005	-0.005		-0.003	-0.005		-0.003	-0.006		
	Max	-0.001	-0.003		-0.002	-0.004		-0.002	-0.004		
	Mean	-0.003	-0.004		-0.003	-0.005		-0.002	-0.005		
	Std. Dev	0.002	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	-0.008	-0.007		-0.005	-0.006		-0.004	-0.008		
	Mean + 3 Sigma	0.002	-0.001		-0.001	-0.003		0.000	-0.002		
				+/-10uA					+/-10uA		

		iIL_d5_0V		uA	iIL_d6_0V		uA	iIL_d7_0V		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.003	-0.002		-0.001	-0.002		-0.004	-0.003		
	11	-0.003	-0.004		-0.002	-0.003		-0.003	-0.005		
	12	-0.003	-0.004		-0.002	-0.005		-0.003	-0.005		
	13	-0.005	-0.005		-0.005	-0.005		-0.004	-0.005		
	14	-0.003	-0.004		-0.003	-0.004		-0.003	-0.005		
	Min	-0.005	-0.005		-0.005	-0.005		-0.004	-0.005		
	Max	-0.003	-0.004		-0.002	-0.003		-0.003	-0.005		
	Mean	-0.003	-0.004		-0.003	-0.004		-0.003	-0.005		
	Std. Dev	0.001	0.001		0.001	0.001		0.000	0.000		
	Mean - 3 Sigma	-0.006	-0.006		-0.007	-0.007		-0.004	-0.005		
	Mean + 3 Sigma	0.000	-0.002		0.001	-0.002		-0.002	-0.004		
				+/-10uA					+/-10uA		

		iIL_d8_0V		uA	iIL_d9_0V		uA	iIL_dco_0V		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.001	-0.001		-0.003	-0.002		-0.002	-0.002		
	11	-0.002	-0.004		-0.003	-0.005		-0.002	-0.003		
	12	-0.002	-0.005		-0.004	-0.005		-0.002	-0.005		
	13	-0.002	-0.004		-0.002	-0.004		-0.004	-0.004		
	14	-0.003	-0.004		-0.003	-0.006		-0.002	-0.003		
	Min	-0.003	-0.005		-0.004	-0.006		-0.004	-0.005		
	Max	-0.002	-0.004		-0.002	-0.004		-0.002	-0.003		
	Mean	-0.002	-0.004		-0.003	-0.005		-0.003	-0.004		
	Std. Dev	0.000	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	-0.003	-0.006		-0.006	-0.006		-0.006	-0.006		
	Mean + 3 Sigma	-0.002	-0.002		0.000	-0.003		0.000	-0.002		
				+/-10uA					+/-10uA		

		iIL_or_0V		uA	iIL_scik_0V		uA	iIL_sdio_0V		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	-0.003	-0.001		-0.082	-0.077		-2.515	-2.496		
	11	-0.002	-0.004		-0.077	-0.064		-2.515	-2.496		
	12	-0.003	-0.005		-0.084	-0.066		-2.515	-2.496		
	13	-0.003	-0.004		-0.082	-0.073		-2.515	-2.496		
	14	-0.003	-0.004		-0.072	-0.066		-2.515	-2.496		
	Min	-0.003	-0.005		-0.084	-0.073		-2.515	-2.496		
	Max	-0.002	-0.004		-0.072	-0.064		-2.515	-2.496		
	Mean	-0.003	-0.004		-0.079	-0.067		-2.515	-2.496		
	Std. Dev	0.000	0.000		0.005	0.004		0.000	0.000		
	Mean - 3 Sigma	-0.004	-0.005		-0.095	-0.079		-2.515	-2.496		
	Mean + 3 Sigma	-0.002	-0.004		-0.062	-0.056		-2.515	-2.496		
				+/-10uA					+/-10uA	-12/+10 uA	

		IIH_d0_DRVDD		uA	IIH_d1_DRVDD		uA	IIH_d10_DRVDD		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.003	0.004		0.003	0.003		0.002	0.003		
	11	0.003	0.003		0.002	0.005		0.001	0.004		
	12	0.001	0.005		0.003	0.006		0.003	0.005		
	13	0.003	0.006		0.005	0.006		0.004	0.006		
	14	0.003	0.003		0.004	0.003		0.003	0.005		
	Min	0.001	0.003		0.002	0.003		0.001	0.004		
	Max	0.003	0.006		0.005	0.006		0.004	0.006		
	Mean	0.002	0.004		0.003	0.005		0.003	0.005		
	Std. Dev	0.001	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	-0.001	0.000		0.000	0.001		0.000	0.003		
	Mean + 3 Sigma	0.005	0.008		0.007	0.008		0.006	0.007		
				+/-10 uA					+/-10 uA		

		IIH_d11_DRVDD		uA	IIH_d12_DRVDD		uA	IIH_d13_DRVDD		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.002	0.002		0.003	0.002		0.003	0.003		
	11	0.004	0.004		0.003	0.004		0.002	0.005		
	12	0.002	0.004		0.004	0.005		0.003	0.005		
	13	0.003	0.004		0.004	0.005		0.002	0.006		
	14	0.003	0.004		0.003	0.005		0.003	0.005		
	Min	0.002	0.004		0.003	0.004		0.002	0.005		
	Max	0.004	0.004		0.004	0.005		0.003	0.006		
	Mean	0.003	0.004		0.004	0.005		0.002	0.005		
	Std. Dev	0.001	0.000		0.000	0.001		0.001	0.000		
	Mean - 3 Sigma	0.000	0.003		0.002	0.003		0.001	0.004		
	Mean + 3 Sigma	0.006	0.005		0.005	0.006		0.004	0.006		
				+/-10 uA					+/-10 uA		

		IIH_d2_DRVDD		uA	IIH_d3_DRVDD		uA	IIH_d4_DRVDD		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.002	0.003		0.003	0.002		0.002	0.002		
	11	0.003	0.004		0.003	0.004		0.003	0.004		
	12	0.003	0.006		0.003	0.005		0.004	0.005		
	13	0.003	0.005		0.004	0.004		0.003	0.006		
	14	0.003	0.005		0.003	0.005		0.004	0.004		
	Min	0.003	0.004		0.003	0.004		0.003	0.004		
	Max	0.003	0.006		0.004	0.005		0.004	0.006		
	Mean	0.003	0.005		0.003	0.004		0.004	0.005		
	Std. Dev	0.000	0.001		0.001	0.000		0.001	0.001		
	Mean - 3 Sigma	0.003	0.003		0.001	0.004		0.002	0.002		
	Mean + 3 Sigma	0.003	0.007		0.005	0.005		0.006	0.008		
				+/-10 uA					+/-10 uA		

		IIH_d5_DRVDD		uA	IIH_d6_DRVDD		uA	IIH_d7_DRVDD		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.002	0.002		0.002	0.004		0.002	0.003		
	11	0.002	0.005		0.002	0.006		0.003	0.005		
	12	0.004	0.006		0.003	0.007		0.002	0.006		
	13	0.004	0.005		0.004	0.005		0.003	0.004		
	14	0.003	0.005		0.004	0.005		0.003	0.004		
	Min	0.002	0.005		0.002	0.005		0.002	0.004		
	Max	0.004	0.006		0.004	0.007		0.003	0.006		
	Mean	0.003	0.005		0.003	0.006		0.003	0.005		
	Std. Dev	0.001	0.001		0.001	0.001		0.000	0.001		
	Mean - 3 Sigma	0.000	0.003		0.001	0.004		0.002	0.003		
	Mean + 3 Sigma	0.006	0.007		0.006	0.008		0.004	0.007		
				+/-10 uA					+/-10 uA		

		IIH_d8_DRVDD		uA	IIH_d9_DRVDD		uA	IIH_dco_DRVDD		uA	
Wafer	SN	PRE	100k		PRE	100k		PRE	100k		
CTRL	17	0.004	0.003		0.002	0.002		0.003	0.003		
	11	0.003	0.005		0.004	0.005		0.002	0.004		
	12	0.002	0.006		0.004	0.005		0.005	0.006		
	13	0.002	0.004		0.002	0.006		0.004	0.006		
	14	0.003	0.005		0.003	0.004		0.003	0.004		
	Min	0.002	0.004		0.002	0.004		0.002	0.004		
	Max	0.003	0.006		0.004	0.006		0.005	0.006		
	Mean	0.003	0.005		0.003	0.005		0.003	0.005		
	Std. Dev	0.000	0.001		0.001	0.001		0.001	0.001		
	Mean - 3 Sigma	0.002	0.003		0.000	0.001		0.000	0.002		
	Mean + 3 Sigma	0.003	0.007		0.006	0.008		0.007	0.008		
				+/-10 uA					+/-10 uA		

		iiH_or_DRVDD		uA	iiH_sclk_DRVDD		uA	iiH_sdio_DRVDD		uA
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	0.003	0.002		106.593	106.653		0.201	0.160	
	11	0.001	0.003		106.402	106.851		0.194	0.160	
	12	0.003	0.004		105.945	106.394		0.196	0.155	
	13	0.003	0.004		108.443	108.807		0.370	0.333	
	14	0.002	0.003		107.430	107.863		0.199	0.172	
	Min	0.001	0.003		105.945	106.394		0.194	0.155	
	Max	0.003	0.004		108.443	108.807		0.370	0.333	
	Mean	0.002	0.003		107.055	107.479		0.240	0.205	
	Std. Dev	0.001	0.001		1.114	1.077		0.087	0.086	
	Mean - 3 Sigma	0.000	0.002		103.712	104.246		-0.020	-0.052	
	Mean + 3 Sigma	0.005	0.005		110.399	110.711		0.499	0.462	
				+/-10 uA			<140 uA			-12/10 uA

		iiL_csb_0V		uA	iiL_oeb_0V		uA	iiL_pwdn_0V		uA
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	-63.403	-63.487		-0.194	-0.331		-0.108	-0.113	
	11	-63.616	-63.586		-0.203	-0.328		-0.110	-0.108	
	12	-63.319	-63.297		-0.339	-0.356		-0.130	-0.109	
	13	-64.119	-64.279		-0.347	-0.349		-0.125	-0.117	
	14	-63.715	-63.746		-0.201	-0.343		-0.113	-0.115	
	Min	-64.119	-64.279		-0.347	-0.356		-0.130	-0.117	
	Max	-63.319	-63.297		-0.201	-0.328		-0.110	-0.108	
	Mean	-63.692	-63.727		-0.272	-0.344		-0.120	-0.112	
	Std. Dev	0.330	0.412		0.082	0.012		0.010	0.004	
	Mean - 3 Sigma	-64.683	-64.964		-0.517	-0.380		-0.148	-0.125	
	Mean + 3 Sigma	-62.701	-62.490		-0.027	-0.307		-0.091	-0.099	
				>-140 uA			+/-10 uA			+/-10 uA

		iiH_csb_DRVDD		uA	iiH_oeb_DRVDD		uA	iiH_pwdn_DRVDD		uA
Wafer	SN	PRE	100k		PRE	100k		PRE	100k	
CTRL	17	46.669	46.750		106.415	106.446		107.256	107.223	
	11	46.837	46.917		106.179	106.628		106.775	107.254	
	12	46.700	46.758		106.454	106.902		106.676	107.109	
	13	47.324	47.443		108.603	109.071		108.575	108.997	
	14	46.974	47.070		107.673	108.059		107.492	108.053	
	Min	46.700	46.758		106.179	106.628		106.676	107.109	
	Max	47.324	47.443		108.603	109.071		108.575	108.997	
	Mean	46.959	47.047		107.227	107.665		107.380	107.853	
	Std. Dev	0.268	0.293		1.124	1.124		0.876	0.868	
	Mean - 3 Sigma	46.154	46.168		103.856	104.293		104.752	105.249	
	Mean + 3 Sigma	47.763	47.926		110.598	111.037		110.008	110.457	
				<100uA			<140 uA			<140uA

		0		uA	iiH_pwdn_DRVDD		uA
Wafer	SN	PRE	100k		PRE	100k	
CTRL	17	106.415	106.446		107.256	107.223	
	11	106.179	106.628		106.775	107.254	
	12	106.454	106.902		106.676	107.109	
	13	108.603	109.071		108.575	108.997	
	14	107.673	108.059		107.492	108.053	
	Min	106.179	106.628		106.676	107.109	
	Max	108.603	109.071		108.575	108.997	
	Mean	107.227	107.665		107.380	107.853	
	Std. Dev	1.124	1.124		0.876	0.868	
	Mean - 3 Sigma	103.856	104.293		104.752	105.249	
	Mean + 3 Sigma	110.598	111.037		110.008	110.457	
				Limit: <140uA			Limit: <140uA































































































































































