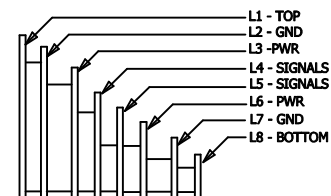


SIZE	QTY	SYM	PLATED	TOL
0.006	309	⊗	YES	+/-0.003
0.01	321	⊗	YES	+/-0.003
0.035	49	⊗	YES	+/-0.003
0.055	15	⊕	YES	+/-0.003
0.067	26	□	YES	+/-0.003
0.187	4	+	NO	+/-0.003
0.189	6	◇	YES	+/-0.003

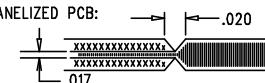
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	1	PRODUCTION	SG 07-23-21

LAYER STRUCTURE



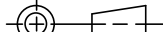


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 8 LAYERS WITH 2 OZ. CU ON ALL THE LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1
WITH 0.024 PAD, 0.020 SOLDER MASK, AND 0.019 SOLDER PASTE.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- PLUG ALL VIA'S WITH NON-CONDUCTIVE COPPER PASTE AND PLATE OVER. PLANARIZE FINAL SURFACE.

UNLESS OTHERWISE SPECIFIED		APPROVALS		  POWER BY LINEAR™ www.linear.com FOR ADI CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION					
PCB DES.		LT		TITLE: FABRICATION DRAWING DUAL 50A OR SINGLE 100A uMODULE REGULATOR WITH DIGITAL POWER MANAGEMENT	
APP ENG.		SG			
		SIZE		IC NO.	
		N/A		LTM4700EY DC2702B(A,B)	
SCALE = NONE		FILENAME: DC2702B-1.PCB		REV 1	
				SHT 1 OF 1	