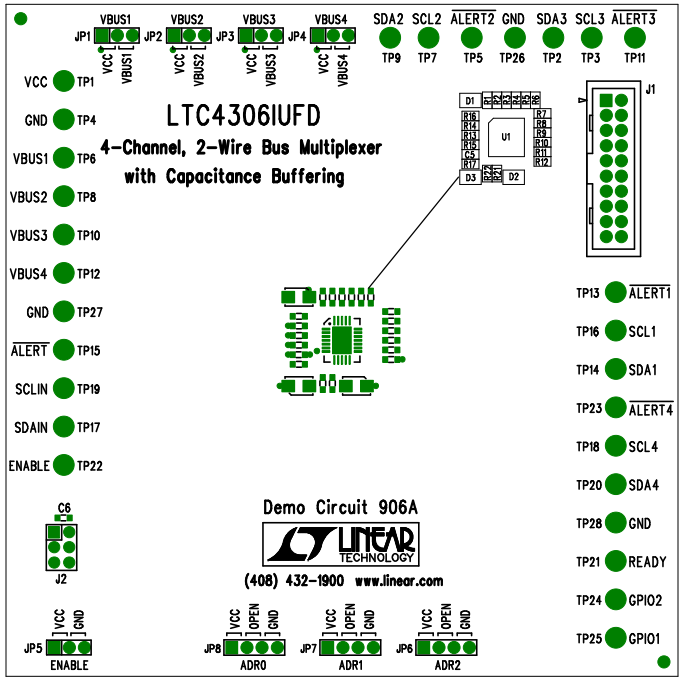
REVISIONS			
REV		APPR	DATE

Silkscreen Top



Install Shunt : JP1 = VBUS1
JP2 = VBUS2
JP3 = VBUS3
JP4 = VBUS4
JP5 = VCC
JP6 = OPEN
JP7 = OPEN
JP8 = OPEN
J2 = DO NOT INSTALLED SHUNT

Assembly Drawing Top

APPROVALS		
	INIT	DATE
DRAWN		
CHECK		
DESIGN		
ENGR		
Scale = None		

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

Title: LTC4306IUFD 4-Channel, 2-Wire Bus Multiplexer with Capacitance Buffering

Size:	Demo Board:	Rev.
None	DC906A	
Design #		Sht: 1 of 1