



Customer:
Part No:

CONCISYS
AD9645CE01 REV. A

U.S. Circuit Cat#:
Work Order:

TBD
TBD

Date: 4/24/2012
Prepared By: Tarun Chavda

Layer #		Cu. Wht.	Design	Estimated	Layer #	Single-Ended				Differential					
			S / P / M	Thickness		Ref. Plane	Orig. L/W	Fin. L/W	Calc. Imped.	Ref. Plane	Orig. L/W	Orig. Spc	Fin. L/W	Fin. Spc	Calc. Imped.
1	0.5 Ounce Foil + Plating	0.5	TOP	2	1	2	10	10	49.6	2	8	12	8	12	98.65
	Pre-Preg (370HR)			6											
2	0.5 Ounce	0.5	LAYER 2	0.6	2										
	3 CORE (370HR)			3											
3	0.5 Ounce	0.5	LAYER 3	0.6	3										
	Pre-Preg (370HR)			35											
4	0.5 Ounce	0.5	LAYER 4	0.6	4										
	3 CORE (370HR)			3											
5	0.5 Ounce	0.5	LAYER 5	0.6	5										
	Pre-Preg (370HR)			6											
6	0.5 Ounce Foil + Plating	0.5	BOTTOM	2	6	5	10	10	49.6	5	8	12	8	12	98.65

Single-Ended Required Impedance: 50 OHMS

Differential Required Impedance: 100 OHMS

Estimated Overall Thickness: 61.4
Required Overall Thickness: 62.0

+/- 005

Notes:

Please confirm this stack-up is acceptable for production.