アナログ・デバイセスのインターフェース・ソリューションをお選びください！

このガイドには、アナログ・デバイセスと旧リニアテクノロジーの両方のインターフェース・ソリューションを収録しています。統合されたポートフォリオでは、フィールド・ベースとベリフェラル通信から、バッファ、アクセラレータ、マルチプレクサなどのPC・全般、I/Oリンクトランスシーバまで、幅広いソリューションを提供しています。

アナログ・デバイセスのインターフェース・デバイス向け設計ソリューションには、キー・パッド・コントローラ、レベル変換器、高電圧保護製品（チャンネル保護、故障保護、過電圧保護）、ラッチアップ・スイッチとマルチプレクサがあります。

旧リニアテクノロジーのインターフェース製品には、最速データレート、高ノード数、低電圧電源に容易に対応できる幅広い業界標準通信デバイスがあります。長距離通信を可能にするRS-485トランシーバや、チップ間通信を向上するPCI/SMBusデジタル・インターフェース・デバイスを用意しています。

アナログ・デバイセスはデータ伝送の課題を解決し、インターフェースをコンパクトで信頼できるソリューションとして提供します。

幅広いアプリケーション
アナログ・デバイセスの革新的なインターフェース・ソリューションは10年以上に渡り、以下のようなアプリケーションで使用されています。

► プロセス制御
► モーター駆動
► 工業用フィールド・ベース
► 計測器
► 通信インフラストラクチャ
► 車載システム
► 太陽光／風力エネルギー

analog.com/jp、linear-tech.co.jp/interface
### Fault Protected RS-485 Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate</th>
<th>Duplex</th>
<th>Number of Nodes</th>
<th>Driver/Receiver Enable</th>
<th>ESD HBM (kV)</th>
<th>Fail-Safe</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3095E</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
<tr>
<td>LT1785A</td>
<td>250 kbps</td>
<td>Half</td>
<td>128</td>
<td>•</td>
<td>±15</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>4.75 to 5.25</td>
<td>125</td>
</tr>
<tr>
<td>LT1791A</td>
<td>250 kbps</td>
<td>Full</td>
<td>128</td>
<td>•</td>
<td>±15</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>4.75 to 5.25</td>
<td>125</td>
</tr>
<tr>
<td>LTC2862A-1/</td>
<td>20 Mbps/250 kbps</td>
<td>Full</td>
<td>224</td>
<td>•</td>
<td>±40</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
<tr>
<td>LTC2862A-2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC2863-1/</td>
<td>20 Mbps/250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
<tr>
<td>LTC2863-2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC2864-1/</td>
<td>20 Mbps/250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
<tr>
<td>LTC2864-2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC2885*</td>
<td>20 Mbps/250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
<tr>
<td>LTC2876</td>
<td>20 Mbps</td>
<td>Half</td>
<td>200</td>
<td>•</td>
<td>±52</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
<tr>
<td>LTC2877</td>
<td>20 Mbps</td>
<td>Half</td>
<td>200</td>
<td>•</td>
<td>±52</td>
<td>Level 4</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
</tr>
</tbody>
</table>

### 3.3 V RS-485 Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate</th>
<th>Duplex</th>
<th>Number of Nodes</th>
<th>Driver/Receiver Enable</th>
<th>ESD HBM (kV)</th>
<th>Fail-Safe</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3065E</td>
<td>50 Mbps</td>
<td>Half</td>
<td>128</td>
<td>•</td>
<td>±12</td>
<td>Idle, open, short</td>
<td>3.0 to 5.5</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3070E</td>
<td>250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3071E</td>
<td>250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3072E</td>
<td>250 kbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3073E</td>
<td>500 kbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3074E</td>
<td>500 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3075E</td>
<td>16 Mbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3076E</td>
<td>16 Mbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3077E</td>
<td>16 Mbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3078E</td>
<td>16 Mbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>ADM3079E</td>
<td>10 Mbps</td>
<td>Half</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3080E</td>
<td>10 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3081E</td>
<td>10 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3082E</td>
<td>10 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3083E</td>
<td>12 Mbps</td>
<td>Half</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3084E</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>32</td>
<td>•</td>
<td>±3.5</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3085E</td>
<td>250 kbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3086E</td>
<td>250 kbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3087E</td>
<td>10 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3088E</td>
<td>12 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3089E</td>
<td>12 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3090E</td>
<td>12 Mbps</td>
<td>Full</td>
<td>32</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>ADM3091E</td>
<td>250 kbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>LTC1480</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>32</td>
<td>•</td>
<td>±3.5</td>
<td>Open</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td></td>
</tr>
<tr>
<td>LTC2850*</td>
<td>20 Mbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>LTC2851</td>
<td>20 Mbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>LTC2852*</td>
<td>20 Mbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>LTC2854*</td>
<td>20 Mbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±25</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
<tr>
<td>LTC2855</td>
<td>20 Mbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td></td>
</tr>
</tbody>
</table>

* 車載アプリケーション認定モデルが利用可能です。詳細については、製品ページをご参照ください。
<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate</th>
<th>Duplex</th>
<th>Number of Nodes</th>
<th>Driver/ Receiver Enable</th>
<th>ESD (V)</th>
<th>Fail-Safe</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM1485</td>
<td>30 Mbps</td>
<td>Half</td>
<td>32</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP, 8-lead PDIP</td>
<td></td>
</tr>
<tr>
<td>ADM1486</td>
<td>30 Mbps</td>
<td>Half</td>
<td>50</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM1487E</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>128</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM1490E</td>
<td>16 Mbps</td>
<td>Full</td>
<td>32</td>
<td>±8</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead MSOP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM1491E</td>
<td>16 Mbps</td>
<td>Full</td>
<td>32</td>
<td>±8</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>14-lead SOIC, 10-lead MSOP</td>
<td></td>
</tr>
<tr>
<td>ADM3095E</td>
<td>50 Mbps</td>
<td>Half</td>
<td>128</td>
<td>±12</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead MSOP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM483</td>
<td>250 kbps</td>
<td>Half</td>
<td>32</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM483E</td>
<td>250 kbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM485</td>
<td>5 Mbps</td>
<td>Half</td>
<td>32</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP, 8-lead PDIP</td>
<td></td>
</tr>
<tr>
<td>ADM4850</td>
<td>115 kbps</td>
<td>Half</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead LFCS, 8-lead SOIC, 8-lead MSOP</td>
<td></td>
</tr>
<tr>
<td>ADM4851</td>
<td>500 kbps</td>
<td>Half</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP</td>
<td></td>
</tr>
<tr>
<td>ADM4852</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP, 8-pad LFCS</td>
<td></td>
</tr>
<tr>
<td>ADM4853T</td>
<td>10 Mbps</td>
<td>Half</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP, 8-lead LFCS</td>
<td></td>
</tr>
<tr>
<td>ADM4854</td>
<td>115 kbps</td>
<td>Full</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM4855</td>
<td>500 kbps</td>
<td>Full</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM4856</td>
<td>2.5 Mbps</td>
<td>Full</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM4857</td>
<td>10 Mbps</td>
<td>Full</td>
<td>256</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>Open, short</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM4858E</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>ADM488</td>
<td>250 kbps</td>
<td>Full</td>
<td>32</td>
<td>—</td>
<td>4.5 to 5.5</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP, 8-lead PDIP</td>
<td></td>
</tr>
<tr>
<td>ADM488A</td>
<td>250 kbps</td>
<td>Full</td>
<td>32</td>
<td>—</td>
<td>4.5 to 5.5</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead MSOP</td>
<td></td>
</tr>
<tr>
<td>ADM489</td>
<td>250 kbps</td>
<td>Full</td>
<td>32</td>
<td>—</td>
<td>4.5 to 5.5</td>
<td>Open</td>
<td>85</td>
<td>14-lead PDIP, 14-lead SOIC, 16-lead TSOP</td>
<td></td>
</tr>
<tr>
<td>ADM489A</td>
<td>250 kbps</td>
<td>Full</td>
<td>32</td>
<td>—</td>
<td>4.5 to 5.5</td>
<td>Open</td>
<td>85</td>
<td>14-lead SOIC, 10-lead MSOP</td>
<td></td>
</tr>
<tr>
<td>LTC1481</td>
<td>2.5 Mbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1482</td>
<td>4 Mbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1483</td>
<td>150 kbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1484</td>
<td>4 Mbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1485</td>
<td>10 Mbps</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1487</td>
<td>250 kbps</td>
<td>Half</td>
<td>256</td>
<td>±10</td>
<td>4.75 to 5.25</td>
<td>Open</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1685</td>
<td>52 Mbps</td>
<td>Half</td>
<td>50</td>
<td>±4</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1686</td>
<td>52 Mbps</td>
<td>Full</td>
<td>50</td>
<td>±4</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>8-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1687</td>
<td>52 Mbps</td>
<td>Full</td>
<td>50</td>
<td>±4</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>14-lead SOIC</td>
<td></td>
</tr>
<tr>
<td>LTC1690</td>
<td>5 Mbps</td>
<td>Full</td>
<td>32</td>
<td>±15</td>
<td>4.75 to 5.25</td>
<td>Idle, open, short</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP, 8-lead SOIC</td>
<td></td>
</tr>
</tbody>
</table>
### 5.0 V RS-485 Transceivers (Continued)

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate</th>
<th>Duplex</th>
<th>Number of Nodes</th>
<th>Driver/Receiver Enable</th>
<th>ESD (HBM kV)</th>
<th>Fail-Safe</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC2856-1/</td>
<td>20 Mbps/250 kbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>4.5 to 5.5</td>
<td>125</td>
<td>8-lead MSOP, 8-lead DFN</td>
</tr>
<tr>
<td>LTC2856-2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC2857-1/</td>
<td>20 Mbps/250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>4.5 to 5.5</td>
<td>125</td>
<td>10-lead MSOP, 10-lead DFN</td>
</tr>
<tr>
<td>LTC2857-2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC2858-1/</td>
<td>20 Mbps/250 kbps</td>
<td>Full</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>4.5 to 5.5</td>
<td>125</td>
<td>10-lead DFN</td>
</tr>
<tr>
<td>LTC2858-2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC2859</td>
<td>20 Mbps/250 kbps</td>
<td>Half</td>
<td>256</td>
<td>•</td>
<td>±15</td>
<td>Idle, open, short</td>
<td>4.5 to 5.5</td>
<td>125</td>
<td>10-lead DFN</td>
</tr>
<tr>
<td>LTC2861</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC490</td>
<td>2.5 Mbps</td>
<td>Full</td>
<td>32</td>
<td>±2</td>
<td>Open</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>8-lead SOIC, 8-lead PDIP</td>
</tr>
<tr>
<td>LTC491</td>
<td>2.5 Mbps</td>
<td>Full</td>
<td>32</td>
<td>±2</td>
<td>Open</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>14-lead SOIC, 14-lead PDIP</td>
</tr>
</tbody>
</table>

### Dual and Quad RS-485 Drivers and Receivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Drivers</th>
<th>Receivers</th>
<th>Max Data Rate</th>
<th>Driver/Receiver Enable</th>
<th>ESD (kV)</th>
<th>Fail-Safe</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM4168</td>
<td>2</td>
<td>2</td>
<td>10 Mbps</td>
<td>•</td>
<td>±15</td>
<td>Open</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead TSSOP</td>
</tr>
<tr>
<td>LTC1518</td>
<td>0</td>
<td>4</td>
<td>52 Mbps</td>
<td>•</td>
<td>±4</td>
<td>Idle, open, short</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC</td>
</tr>
<tr>
<td>LTC1519</td>
<td>0</td>
<td>4</td>
<td>52 Mbps</td>
<td>•</td>
<td>±4</td>
<td>Idle, open, short</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC</td>
</tr>
<tr>
<td>LTC1520</td>
<td>4</td>
<td>0</td>
<td>50 Mbps</td>
<td>•</td>
<td>±4</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>70</td>
<td>16-lead SOIC</td>
</tr>
<tr>
<td>LTC1688</td>
<td>4</td>
<td>0</td>
<td>100 Mbps</td>
<td>•</td>
<td>±4</td>
<td>—</td>
<td>3.0 to 5.25</td>
<td>85</td>
<td>16-lead SOIC</td>
</tr>
<tr>
<td>LTC1689</td>
<td>4</td>
<td>0</td>
<td>100 Mbps</td>
<td>•</td>
<td>±4</td>
<td>—</td>
<td>3.0 to 5.25</td>
<td>85</td>
<td>16-lead SOIC</td>
</tr>
<tr>
<td>LTC486</td>
<td>4</td>
<td>0</td>
<td>10 Mbps</td>
<td>•</td>
<td>±2</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>LTC487</td>
<td>4</td>
<td>0</td>
<td>10 Mbps</td>
<td>•</td>
<td>±2</td>
<td>—</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>LTC488</td>
<td>0</td>
<td>4</td>
<td>10 Mbps</td>
<td>•</td>
<td>±2</td>
<td>Open</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>LTC489</td>
<td>0</td>
<td>4</td>
<td>10 Mbps</td>
<td>•</td>
<td>±4</td>
<td>Open</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC, 16-lead PDIP</td>
</tr>
</tbody>
</table>

### Isolated RS-485 Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Insulation Rating (kV rms)</th>
<th>Full Duplex</th>
<th>Half Duplex</th>
<th>Max Data Rate</th>
<th>Integrated Isolated Power</th>
<th>Isolated Power Output</th>
<th>Integrated Transformer Driver</th>
<th>Power Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM2481</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>500 kbps</td>
<td>•</td>
<td>3.0 to 5.0</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2482E</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>16 Mbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2483</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>500 kbps</td>
<td>•</td>
<td>2.7 to 5.5</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2484E</td>
<td>5</td>
<td>•</td>
<td>•</td>
<td>500 kbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2485</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>16 Mbps</td>
<td>•</td>
<td>2.7 to 5.5</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2486</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>20 Mbps</td>
<td>•</td>
<td>2.7 to 5.5</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2487E</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>500 kbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2490E</td>
<td>5</td>
<td>•</td>
<td>•</td>
<td>16 Mbps</td>
<td>•</td>
<td>2.7 to 5.5</td>
<td>4.75 to 5.25</td>
<td>105</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2491E</td>
<td>5</td>
<td>•</td>
<td>•</td>
<td>16 Mbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM2582E</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>16 Mbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>20-lead SOIC_W</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ADM2587E</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>500 kbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>20-lead SOIC_W</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ADM2682E</td>
<td>5</td>
<td>•</td>
<td>•</td>
<td>16 Mbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>16-lead SOIC, IC</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ADM2687E</td>
<td>5</td>
<td>•</td>
<td>•</td>
<td>500 kbps</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>16-lead SOIC, IC</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ADM2799E</td>
<td>5</td>
<td>•</td>
<td>•</td>
<td>2.5 Mbps</td>
<td>•</td>
<td>1.7 to 5.5</td>
<td>3.0 to 5.5</td>
<td>125</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1535</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>250 kbps</td>
<td>•</td>
<td>4.5 to 5.5</td>
<td>4.5 to 7.5</td>
<td>85</td>
<td>28-ball SOIC</td>
<td></td>
</tr>
<tr>
<td>LTM2881-3E</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>20 Mbps</td>
<td>1 W (5 V)</td>
<td>3.0 to 3.6</td>
<td>105</td>
<td>32-ball BGA, 32-pad LGA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTM2881-5E</td>
<td>2.5</td>
<td>•</td>
<td>•</td>
<td>20 Mbps</td>
<td>1 W (5 V)</td>
<td>4.5 to 5.5</td>
<td>105</td>
<td>32-ball BGA, 32-pad LGA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTM2885E</td>
<td>6.5</td>
<td>•</td>
<td>•</td>
<td>20 Mbps</td>
<td>1 W (5 V)</td>
<td>4.5 to 5.5</td>
<td>105</td>
<td>42-ball BGA</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*1  車載アプリケーション向けのモデルが利用可能です。詳細については、製品ページをご参照ください。
*2  ロジック電源電圧範囲は、または VLO = 1.8 V ～ 5.5 V
RS-232 トランシーバ / RS-232 Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Drivers/ Receivers</th>
<th>Data Rate (kbps)</th>
<th>ESD (kV)</th>
<th>Rx Active in Shutdown</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>AD7306</td>
<td>3/2</td>
<td>100</td>
<td></td>
<td>1</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM101E*</td>
<td>1/1</td>
<td>460</td>
<td>±15</td>
<td>1</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>10-lead MSOP</td>
</tr>
<tr>
<td>ADM1181A</td>
<td>2/2</td>
<td>230</td>
<td>±15</td>
<td>1</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead PDIP, 16-lead SOIC_W</td>
</tr>
<tr>
<td>ADM1385</td>
<td>2/2</td>
<td>460</td>
<td>±15</td>
<td>2</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADM202</td>
<td>2/2</td>
<td>120</td>
<td></td>
<td>2</td>
<td>4.5 to 5.5</td>
<td>70</td>
<td>16-lead PDIP, 16-lead SOIC_W</td>
</tr>
<tr>
<td>ADM202E</td>
<td>2/2</td>
<td>230</td>
<td>±15</td>
<td>2</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC, 16-lead PDIP, 16-lead SOIC_W</td>
</tr>
<tr>
<td>ADM206</td>
<td>4/3</td>
<td>120</td>
<td>±15</td>
<td>0</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SSOP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM206E</td>
<td>4/3</td>
<td>230</td>
<td>±15</td>
<td>0</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM207</td>
<td>5/3</td>
<td>120</td>
<td>±15</td>
<td>5</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SSOP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM207E</td>
<td>5/3</td>
<td>230</td>
<td>±15</td>
<td>5</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SSOP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM208</td>
<td>4/4</td>
<td>120</td>
<td>±15</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SSOP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM208E</td>
<td>4/4</td>
<td>230</td>
<td>±15</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SSOP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM211</td>
<td>4/5</td>
<td>120</td>
<td>±15</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>28-lead SOIC_W, 28-lead SSOP</td>
</tr>
<tr>
<td>ADM211E</td>
<td>4/5</td>
<td>230</td>
<td>±15</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>28-lead SOIC_W, 28-lead SSOP</td>
</tr>
<tr>
<td>ADM213</td>
<td>4/5</td>
<td>120</td>
<td>±15</td>
<td>2</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>28-lead SOIC_W, 28-lead SSOP</td>
</tr>
<tr>
<td>ADM213E</td>
<td>4/5</td>
<td>230</td>
<td>±15</td>
<td>2</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>28-lead SOIC_W, 28-lead SSOP</td>
</tr>
<tr>
<td>ADM2209E</td>
<td>6/10</td>
<td>920</td>
<td>±15</td>
<td>2</td>
<td>3.15 to 13.2</td>
<td>85</td>
<td>38-lead SSOP</td>
</tr>
<tr>
<td>ADM222</td>
<td>2/2</td>
<td>200</td>
<td>±15</td>
<td>0</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>18-lead PDIP, 18-lead SOIC_W</td>
</tr>
<tr>
<td>ADM232A</td>
<td>2/2</td>
<td>200</td>
<td></td>
<td>0</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC, 16-lead SOIC_W</td>
</tr>
<tr>
<td>ADM232L</td>
<td>2/2</td>
<td>120</td>
<td>±15</td>
<td>2</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC, 16-lead SOIC_W</td>
</tr>
<tr>
<td>ADM233L</td>
<td>2/2</td>
<td>120</td>
<td></td>
<td>2</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>20-lead PDIP</td>
</tr>
<tr>
<td>ADM237L</td>
<td>5/3</td>
<td>120</td>
<td>±15</td>
<td>5</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM238L</td>
<td>4/4</td>
<td>120</td>
<td>±15</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>24-lead PDIP, 24-lead SOIC_W</td>
</tr>
<tr>
<td>ADM241L</td>
<td>4/5</td>
<td>120</td>
<td>±15</td>
<td>0</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>28-lead SSOP</td>
</tr>
<tr>
<td>ADM242</td>
<td>2/2</td>
<td>200</td>
<td>±15</td>
<td>2</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>18-lead PDIP, 18-lead SOIC_W</td>
</tr>
<tr>
<td>ADM3101E</td>
<td>1/1</td>
<td>460</td>
<td>±15</td>
<td>1</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>12-lead LFCSOP, 16-lead SSOP</td>
</tr>
<tr>
<td>ADM3202</td>
<td>2/2</td>
<td>460</td>
<td>±15</td>
<td>2</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>16-lead SOIC_W, 16-lead SOIC_W</td>
</tr>
</tbody>
</table>

* 車載アプリケーション認定モデルが利用可能です。詳細については、製品ページをご参照ください。
* ロジック電源電圧範囲は、または VCC=1.8 V〜5.5 V
### RS-232 Transceivers (Continued)

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Drivers/ Receivers</th>
<th>Data Rate</th>
<th>ESD (kV)</th>
<th>Driver Disable</th>
<th>Rx Active in Shutdown</th>
<th>Shutdown Mode</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3222</td>
<td>2/2</td>
<td>460 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td></td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>18-lead PDIP, 20-lead SSOP, 20-lead TSSOP, 18-lead SOIC_W</td>
</tr>
<tr>
<td>ADM3232E</td>
<td>2/2</td>
<td>460 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td></td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>16-lead SOIC, 16-lead SOIC_W, 16-lead TSSOP</td>
</tr>
<tr>
<td>ADM3307E</td>
<td>5/3</td>
<td>1 Mbps</td>
<td>±15</td>
<td>1</td>
<td>•</td>
<td>2.7 to 3.8</td>
<td>85</td>
<td></td>
<td>28-lead LFCS, 28-lead TSSOP</td>
</tr>
<tr>
<td>ADM3310E</td>
<td>3/5</td>
<td>460 kbps</td>
<td>±15</td>
<td>2</td>
<td>•</td>
<td>2.7 to 3.8</td>
<td>85</td>
<td></td>
<td>32-lead LFCS, 28-lead TSSOP</td>
</tr>
<tr>
<td>ADM3311E</td>
<td>3/5</td>
<td>460 kbps</td>
<td>±15</td>
<td>1</td>
<td>•</td>
<td>2.7 to 3.8</td>
<td>85</td>
<td></td>
<td>28-lead LFCS, 28-lead SOIC_W, 28-lead TSSOP</td>
</tr>
<tr>
<td>ADM3312E</td>
<td>3/3</td>
<td>460 kbps</td>
<td>±15</td>
<td>1</td>
<td>•</td>
<td>2.7 to 3.8</td>
<td>85</td>
<td></td>
<td>32-lead LFCS, 24-lead TSSOP</td>
</tr>
<tr>
<td>ADM3315E</td>
<td>3/3</td>
<td>460 kbps</td>
<td>±15</td>
<td>1</td>
<td>•</td>
<td>2.7 to 3.8</td>
<td>85</td>
<td></td>
<td>28-lead LFCS, 24-lead TSSOP</td>
</tr>
<tr>
<td>ADM5170</td>
<td>8/0</td>
<td>116 kbps</td>
<td>±2</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>ADM560</td>
<td>4/5</td>
<td>116 kbps</td>
<td>±2</td>
<td>2</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 18-lead PDIP</td>
</tr>
<tr>
<td>ADM561</td>
<td>4/5</td>
<td>116 kbps</td>
<td>±2</td>
<td>0</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>LT1032</td>
<td>4/0</td>
<td>250 kbps</td>
<td>±2</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>28-lead PDIP, 28-lead PLCC</td>
</tr>
<tr>
<td>LT1039A</td>
<td>3/3</td>
<td>250 kbps</td>
<td>±15</td>
<td>1</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 18-lead PDIP</td>
</tr>
<tr>
<td>LT1039A-16</td>
<td>3/3</td>
<td>250 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 18-lead PDIP</td>
</tr>
<tr>
<td>LT1060</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±2</td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 18-lead PDIP</td>
</tr>
<tr>
<td>LT1061</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±2</td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>LT1130A</td>
<td>5/5</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1131A</td>
<td>5/4</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1132A</td>
<td>5/3</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1133A</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1134A</td>
<td>4/4</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1135A</td>
<td>5/3</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1136A</td>
<td>4/5</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1137A</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1138A</td>
<td>5/3</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1139A</td>
<td>4/4</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1140A</td>
<td>5/3</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1141A</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td></td>
<td>24-lead PDIP</td>
</tr>
<tr>
<td>LT1180A</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td></td>
<td>18-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>LT1181A</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td></td>
<td>16-lead SOIC, 16-lead PDIP</td>
</tr>
<tr>
<td>Part Number</td>
<td>Drivers/ Receivers</td>
<td>Data Rate</td>
<td>ESD (kV)</td>
<td>Driver Disable</td>
<td>Rx Active in Shutdown</td>
<td>Shutdown Mode</td>
<td>Supply (V)</td>
<td>Max Temp (°C)</td>
<td>Packages</td>
</tr>
<tr>
<td>-------------</td>
<td>--------------------</td>
<td>-----------</td>
<td>----------</td>
<td>----------------</td>
<td>-----------------------</td>
<td>---------------</td>
<td>------------</td>
<td>---------------</td>
<td>-------------------</td>
</tr>
<tr>
<td>LT1237</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±15</td>
<td>•</td>
<td>1</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LT1280A</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>18-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>18-lead PDIP</td>
</tr>
<tr>
<td>LT1281A</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td>LT1330</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±10</td>
<td>•</td>
<td>1</td>
<td>•</td>
<td>V_{IL} 4.75 to 5.25, V_{IL} 3.0 to 5.0</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LT1331</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±10</td>
<td>•</td>
<td>1</td>
<td>•</td>
<td>3.0 to 6.0</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LT1342</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±10</td>
<td>•</td>
<td></td>
<td></td>
<td>V_{IL} 4.75 to 5.25, V_{IL} 3.0 to 5.0</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LT1381</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td>LT1537</td>
<td>3/5</td>
<td>250 kbps</td>
<td>±5</td>
<td>•</td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP</td>
</tr>
<tr>
<td>LT1780</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>18-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>18-lead PDIP</td>
</tr>
<tr>
<td>LT1781</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±15</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td>LTC1327</td>
<td>3/5</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>3.0 to 3.6</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1337</td>
<td>3/5</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1338</td>
<td>5/3</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1347</td>
<td>3/5</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td>5</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>70</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1348</td>
<td>3/5</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td>5</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1349</td>
<td>3/5</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td>2</td>
<td>•</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1350</td>
<td>3/5</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td>2</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>28-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>28-lead PDIP_W</td>
</tr>
<tr>
<td>LTC1382</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td>LTC1383</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>16-lead SOIC,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td>LTC1384</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td>2</td>
<td>•</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>20-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead SOIC_W</td>
</tr>
<tr>
<td>LTC1385</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>20-lead SSOP,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead SOIC_W,</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>16-lead PDIP</td>
</tr>
<tr>
<td>LTC1386</td>
<td>2/2</td>
<td>120 kbps</td>
<td>±10</td>
<td></td>
<td></td>
<td></td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC</td>
</tr>
<tr>
<td>LTC2801</td>
<td>1/1</td>
<td>250 kbps</td>
<td>±10</td>
<td>•</td>
<td>1</td>
<td>•</td>
<td>1.8 to 5.5</td>
<td>85</td>
<td>12-lead DFN</td>
</tr>
<tr>
<td>LTC2802</td>
<td>1/1</td>
<td>1 Mbps</td>
<td>±10</td>
<td>•</td>
<td>1</td>
<td>•</td>
<td>1.8 to 5.5</td>
<td>85</td>
<td>12-lead DFN</td>
</tr>
<tr>
<td>LTC2803</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td>•</td>
<td>2</td>
<td>•</td>
<td>1.8 to 5.5</td>
<td>85</td>
<td>12-lead DFN</td>
</tr>
<tr>
<td>LTC2803-1</td>
<td>2/2</td>
<td>250 kbps</td>
<td>±10</td>
<td>•</td>
<td></td>
<td></td>
<td>1.8 to 5.5</td>
<td>85</td>
<td>16-lead SSOP_N</td>
</tr>
<tr>
<td>LTC2804</td>
<td>2/2</td>
<td>1 Mbps</td>
<td>±10</td>
<td>•</td>
<td>2</td>
<td>•</td>
<td>1.8 to 5.5</td>
<td>85</td>
<td>16-lead DFN</td>
</tr>
<tr>
<td>LTC2804-1</td>
<td>2/2</td>
<td>1 Mbps</td>
<td>±10</td>
<td>•</td>
<td></td>
<td></td>
<td>1.8 to 5.5</td>
<td>85</td>
<td>16-lead SSOP_N</td>
</tr>
</tbody>
</table>
### 絶縁型 RS-232 トランシーバ / Isolated RS-232 Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Insulation Rating (kV rms)</th>
<th>ESD Protection (kV)</th>
<th>Max Data Rate (kbps)</th>
<th>Number Tx</th>
<th>Number Rx</th>
<th>Integrated Isolated Power</th>
<th>Isolated Power Output</th>
<th>Supply Voltage (V)</th>
<th>Aux Channel (Mbps)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3251E</td>
<td>2.5</td>
<td>15</td>
<td>460</td>
<td>1</td>
<td>1</td>
<td>•</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>20-lead SOIC_W</td>
<td>85</td>
<td>20-lead SOIC_W</td>
</tr>
<tr>
<td>ADM3252E</td>
<td>2.5</td>
<td>15</td>
<td>460</td>
<td>2</td>
<td>2</td>
<td>•</td>
<td>3.0 to 5.5</td>
<td>85</td>
<td>44-ball BGA</td>
<td>85</td>
<td>44-ball BGA</td>
</tr>
<tr>
<td>LTM2882-3</td>
<td>2.5</td>
<td>10</td>
<td>1000</td>
<td>2</td>
<td>2</td>
<td>•</td>
<td>1 W (5 V)</td>
<td>3.0 to 3.6</td>
<td>10</td>
<td>105</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2882-5</td>
<td>2.5</td>
<td>10</td>
<td>1000</td>
<td>2</td>
<td>2</td>
<td>•</td>
<td>1 W (5 V)</td>
<td>4.5 to 5.5</td>
<td>10</td>
<td>105</td>
<td>32-ball BGA</td>
</tr>
</tbody>
</table>

### マルチプロトコルトランシーバ / Multiprotocol Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Drivers/ Receivers</th>
<th>Supported Data Rate</th>
<th>Supported ESD</th>
<th>V.35 (Async) (Mbps)</th>
<th>V.35 (Sync) (Mbps)</th>
<th>ESD (kV)</th>
<th>Termination</th>
<th>DCE or DTE</th>
<th>Signals</th>
<th>Additional Signals</th>
<th>Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC1321</td>
<td>2/2</td>
<td>120 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock, or control</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>44-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1322</td>
<td>4/4</td>
<td>120 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC1323</td>
<td>2/3</td>
<td>120 kbps</td>
<td>2 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, control</td>
<td>DTE or DCE</td>
<td>Data, control</td>
<td>4.75 to 5.25</td>
<td>70</td>
<td>28-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1334</td>
<td>4/4</td>
<td>120 kbps</td>
<td>5 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>28-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1335</td>
<td>4/4</td>
<td>120 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC1343</td>
<td>4/4</td>
<td>10</td>
<td>4</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>28-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1344</td>
<td>2/2</td>
<td>120 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC1345</td>
<td>3/3</td>
<td>150 kbps/  5 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC1346A</td>
<td>3/3</td>
<td>120 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC1347</td>
<td>2/2</td>
<td>120 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>20-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1354</td>
<td>3/3</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>28-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1346</td>
<td>4/4</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC1345</td>
<td>5/5</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC1346</td>
<td>3/3</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>24-lead SOIC_S</td>
<td></td>
</tr>
<tr>
<td>LTC2844</td>
<td>4/4</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Control</td>
<td>DTE or DCE</td>
<td>Control</td>
<td>3.0 to 3.6/ 4.75 to 5.25</td>
<td>85</td>
<td>28-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2845</td>
<td>5/5</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Control</td>
<td>DTE or DCE</td>
<td>Control</td>
<td>3.0 to 3.6/ 4.75 to 5.25</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2846</td>
<td>3/3</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Control</td>
<td>DTE or DCE</td>
<td>Control</td>
<td>3.0 to 3.6/ 4.75 to 5.25</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2847</td>
<td>3/3</td>
<td>100 kbps</td>
<td>10 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Control</td>
<td>DTE or DCE</td>
<td>Control</td>
<td>3.0 to 3.6/ 4.75 to 5.25</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2870</td>
<td>2/2</td>
<td>500 kbps</td>
<td>20 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>3.0 to 5.5/ VL 1.7 to VCC</td>
<td>85</td>
<td>28-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2871</td>
<td>2/2</td>
<td>500 kbps</td>
<td>20 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>3.0 to 5.5/ VL 1.7 to VCC</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2872</td>
<td>2/2</td>
<td>500 kbps</td>
<td>20 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>3.0 to 5.5/ VL 1.7 to VCC</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>LTC2873</td>
<td>1/1</td>
<td>1 Mbps/ 500 kbps</td>
<td>20 Mbps</td>
<td>±±0</td>
<td></td>
<td></td>
<td>Data, clock</td>
<td>DTE or DCE</td>
<td>Data, clock</td>
<td>3.0 to 5.5/ VL 1.7 to VCC</td>
<td>85</td>
<td>36-lead SOIC_W</td>
<td></td>
</tr>
</tbody>
</table>
### LVDS (Low Voltage Differential Signaling) Drivers- Receivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate (Mbps)</th>
<th>Drivers/ Receivers</th>
<th>Fail-Safe</th>
<th>ESD (kV)</th>
<th>Voltage Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADN4661</td>
<td>600</td>
<td>1/0</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4662</td>
<td>400</td>
<td>0/1</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4663</td>
<td>600</td>
<td>2/0</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4664</td>
<td>400</td>
<td>0/2</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4665</td>
<td>400</td>
<td>4/0</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead TSSOP, 16-lead SOIC</td>
</tr>
<tr>
<td>ADN4666</td>
<td>400</td>
<td>0/4</td>
<td>Open, short</td>
<td>±8</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC, 16-lead TSSOP</td>
</tr>
<tr>
<td>ADN4667*</td>
<td>400</td>
<td>4/0</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC, 16-lead TSSOP</td>
</tr>
<tr>
<td>ADN4668</td>
<td>400</td>
<td>0/4</td>
<td>Open, short</td>
<td>±15</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>16-lead SOIC, 16-lead TSSOP</td>
</tr>
</tbody>
</table>

### M-LVDS (Multipoint Low Voltage Differential Signaling) Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate (Mbps)</th>
<th>Duplex</th>
<th>Number of Nodes</th>
<th>ESD (kV)</th>
<th>Interface Features</th>
<th>Shutdown</th>
<th>Voltage Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADN4690E</td>
<td>100</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 1</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4691E</td>
<td>200</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 1</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4692E</td>
<td>100</td>
<td>Full</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 1</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>14-lead SOIC</td>
</tr>
<tr>
<td>ADN4693E</td>
<td>200</td>
<td>Full</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 2</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>14-lead SOIC</td>
</tr>
<tr>
<td>ADN4694E</td>
<td>100</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 2</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4695E</td>
<td>200</td>
<td>Half</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 2</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADN4696E</td>
<td>100</td>
<td>Full</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 2</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>14-lead SOIC</td>
</tr>
<tr>
<td>ADN4697E</td>
<td>200</td>
<td>Full</td>
<td>32</td>
<td>±15</td>
<td>M-LVDS Rx Type 2</td>
<td>•</td>
<td>3.0 to 3.6</td>
<td>85</td>
<td>14-lead SOIC</td>
</tr>
</tbody>
</table>

### LVDS Isolators

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate (Mbps)</th>
<th>Max Prop Delay (ns)</th>
<th>Number of Channels</th>
<th>Inputs</th>
<th>Insulation Rating (kV rms)</th>
<th>Working Voltage (V rms/V peak)</th>
<th>Special Features</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADN4650</td>
<td>600</td>
<td>4.5</td>
<td>2</td>
<td>2</td>
<td>5</td>
<td>300/424</td>
<td>—</td>
<td>125</td>
<td>20-lead SOIC, 20-lead SSOP</td>
</tr>
<tr>
<td>ADN4651</td>
<td>600</td>
<td>4.5</td>
<td>2</td>
<td>1</td>
<td>1</td>
<td>300/424</td>
<td>Fail safe</td>
<td>125</td>
<td>20-lead SOIC, 20-lead SSOP</td>
</tr>
<tr>
<td>ADN4652</td>
<td>600</td>
<td>4.5</td>
<td>2</td>
<td>1</td>
<td>1</td>
<td>300/424</td>
<td>Fail safe</td>
<td>125</td>
<td>20-lead SOIC, 20-lead SSOP</td>
</tr>
</tbody>
</table>

### Controller Area Network (CAN) Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Max Data Rate (Mbps)</th>
<th>Number of Nodes</th>
<th>Fault Protection (V)</th>
<th>ESD (kV)</th>
<th>Common-Mode Voltage (V)</th>
<th>Voltage Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3051</td>
<td>1</td>
<td>110</td>
<td>±24</td>
<td>4</td>
<td>~7.0 to +12.0</td>
<td>4.5 to 5.5</td>
<td>125</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>LT1796</td>
<td>0.125</td>
<td>256</td>
<td>±60</td>
<td>15 Level 4</td>
<td>~7.0 to +12.0</td>
<td>4.75 to 5.25</td>
<td>85</td>
<td>8-lead SOIC, 8-lead PDIP</td>
</tr>
<tr>
<td>LTC2875</td>
<td>4</td>
<td>166</td>
<td>±60</td>
<td>25</td>
<td>~36.0 to +36.0</td>
<td>3.0 to 3.6 or 4.5 to 5.5</td>
<td>125</td>
<td>8-lead SOIC, 8-lead DFN</td>
</tr>
</tbody>
</table>

### Isolated CAN Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Fault Protection (V)</th>
<th>Insulation Rating (kV rms)</th>
<th>High Voltage Bus Side Regulator</th>
<th>Max Data Rate (Mbps)</th>
<th>Integrated Isolated Power</th>
<th>Isolated Power Output</th>
<th>Power Supply (V)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3052</td>
<td>±36</td>
<td>5</td>
<td>•</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>3.0 to 5.5</td>
<td>11 to 25</td>
<td>8-lead SOIC_W</td>
</tr>
<tr>
<td>ADM3053</td>
<td>±36</td>
<td>2.5</td>
<td>1</td>
<td>•</td>
<td>4</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>85</td>
<td>20-lead SOIC_W</td>
</tr>
<tr>
<td>ADM3054†</td>
<td>±36</td>
<td>5</td>
<td>1</td>
<td>•</td>
<td>3</td>
<td>3</td>
<td>3.0 to 5.5</td>
<td>5</td>
<td>16-lead SOIC_W</td>
</tr>
<tr>
<td>LTM2889-3†</td>
<td>±60</td>
<td>2.5</td>
<td>4</td>
<td>•</td>
<td>0.75 W (adj 3 V to 5 V)</td>
<td>3</td>
<td>3.0 to 3.6</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2889-5†</td>
<td>±60</td>
<td>2.5</td>
<td>4</td>
<td>•</td>
<td>(adj 3 V to 5 V)</td>
<td>4</td>
<td>4.5 to 5.5</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
</tbody>
</table>

1. 車載アプリケーション向けモデルが利用可能です。詳細については、製品ページをご参照ください。
2. ロジック電源電圧範囲（または V(I)); 1.8 V 〜 5.5 V
### I²C バッファと立ち上がり時間アクセラレータ / I²C Buffers and Rise Time Accelerators

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Hot Swappable</th>
<th>Rise Time Accelerator</th>
<th>Bidirectional Level Translation (V)</th>
<th>Stuck Bus Disconnect/Recovery</th>
<th>Enable</th>
<th>Ready</th>
<th>V_{cc}</th>
<th>GPIO or Fault</th>
<th>HBM ESD (kV)</th>
<th>Bus Frequency</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC4300A-1</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>*</td>
<td></td>
<td>±2</td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4300A-2</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td></td>
<td></td>
<td>±2</td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4300A-3</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td></td>
<td></td>
<td>±2</td>
<td>400</td>
<td>85</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4301</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td></td>
<td></td>
<td>±10</td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
</tr>
<tr>
<td>LTC4301L</td>
<td>*</td>
<td>*</td>
<td>1 to 2.7/5.5</td>
<td>*</td>
<td></td>
<td></td>
<td>±10</td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
</tr>
<tr>
<td>LTC4302-1</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>±2</td>
<td></td>
<td>400</td>
<td>85</td>
<td>10-lead MSOP</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4302-2</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>±2</td>
<td></td>
<td>400</td>
<td>85</td>
<td>10-lead MSOP</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4303</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>±15</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4304</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>±5</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4307</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>±5</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4307-1</td>
<td>*</td>
<td>*</td>
<td>2.7 to 5.5</td>
<td>*</td>
<td>±5</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4308</td>
<td>*</td>
<td>*</td>
<td>1.8 to 2.3/5.5</td>
<td>*</td>
<td>±6</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4309</td>
<td>*</td>
<td>*</td>
<td>1 to 2.3/5.5</td>
<td>*</td>
<td>±6</td>
<td></td>
<td>400</td>
<td>85</td>
<td>16-lead SSOP_N, 12-lead SSOP</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4310-1</td>
<td>*</td>
<td>*</td>
<td>3 to 5.5</td>
<td>*</td>
<td>±5</td>
<td></td>
<td>100</td>
<td>85</td>
<td>10-lead MSOP</td>
<td>10-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4310-2</td>
<td>*</td>
<td>*</td>
<td>3 to 5.5</td>
<td>*</td>
<td>±5</td>
<td></td>
<td>100</td>
<td>85</td>
<td>10-lead MSOP</td>
<td>10-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4311</td>
<td>*</td>
<td>*</td>
<td></td>
<td>*</td>
<td>±8</td>
<td></td>
<td>400</td>
<td>85</td>
<td>6-lead DFN</td>
<td>6-lead SOT-2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4313-1</td>
<td>*</td>
<td>*</td>
<td>3.3 to 5.0</td>
<td>*</td>
<td>±4</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4313-2</td>
<td>*</td>
<td>*</td>
<td>1.5 to 5.5</td>
<td>*</td>
<td>±4</td>
<td></td>
<td>400</td>
<td>85</td>
<td>8-lead MSOP</td>
<td>8-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4315</td>
<td>*</td>
<td>*</td>
<td>1.5 to 5.5</td>
<td>*</td>
<td>±4</td>
<td></td>
<td>400</td>
<td>85</td>
<td>12-lead MSOP</td>
<td>12-lead DFN</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### I²C マルチプレクサとアドレス変換器 / I²C Multiplexers and Address Translators

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Mux</th>
<th>Data Rate (kHz)</th>
<th>Supply Voltage (V)</th>
<th>Bus Voltage (V)</th>
<th>Channel Select</th>
<th>Bus Buffer</th>
<th>Rise Time Accelerator Options</th>
<th>GPIO</th>
<th>Stuck Bus Circuitry</th>
<th>HBM ESD (kV)</th>
<th>Address Translation</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTC4305</td>
<td>1:2</td>
<td>400</td>
<td>2.7 to 5.5</td>
<td>2.2 to 5.5</td>
<td>I²C bus</td>
<td>*</td>
<td>Strong/off Disconnect</td>
<td>±10</td>
<td></td>
<td>85</td>
<td>16-lead DFN, 16-lead SSOP_N</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4306</td>
<td>1:4</td>
<td>400</td>
<td>2.7 to 5.5</td>
<td>2.2 to 5.5</td>
<td>I²C bus</td>
<td>*</td>
<td>Strong/off 2</td>
<td>±10</td>
<td></td>
<td>85</td>
<td>24-lead DFN, 24-lead SSOP</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4312</td>
<td>1:2</td>
<td>400</td>
<td>2.9 to 5.5</td>
<td>1.5 to 5.5</td>
<td>Enable pin</td>
<td>*</td>
<td>Strong/2 mAloff Disconnect and recovery</td>
<td>±4</td>
<td></td>
<td>85</td>
<td>14-lead DFN, 16-lead MSOP</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4314</td>
<td>1:4</td>
<td>400</td>
<td>2.9 to 5.5</td>
<td>1.5 to 5.5</td>
<td>Enable pin</td>
<td>*</td>
<td>Strong/2 mAloff Disconnect and recovery</td>
<td>±4</td>
<td></td>
<td>85</td>
<td>20-lead DFN, 20-lead SSOP_N</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4316</td>
<td>1:1</td>
<td>400</td>
<td>2.25 to 5.5</td>
<td>2.5 to 5.5</td>
<td>Enable pin</td>
<td>*</td>
<td>Timeout</td>
<td>±4</td>
<td></td>
<td>*</td>
<td>10-lead DFN, 10-lead MSOP</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4317</td>
<td>1:2</td>
<td>400</td>
<td>2.25 to 5.5</td>
<td>2.5 to 5.5</td>
<td>Enable pin</td>
<td>*</td>
<td>Timeout</td>
<td>±4</td>
<td></td>
<td>*</td>
<td>16-lead DFN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LTC4318</td>
<td>2:1</td>
<td>400</td>
<td>2.25 to 5.5</td>
<td>2.5 to 5.5</td>
<td>Enable pin</td>
<td>*</td>
<td>Timeout</td>
<td>±4</td>
<td></td>
<td>*</td>
<td>20-lead DFN</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

1. 車載アプリケーション用モデルが利用可能です。詳細については、製品ページをご参照ください。
2. 立ち上がり時間アクセラレータ回路は無効化できます。
3. SCL_{IN}およびSDA_{IN}1Vまでは、SDA_{OUT}2.7V〜5.5V
4. SCL_{IN}およびSDA_{IN}1.8Vまでは、SDA_{OUT}およびSCL_{OUT}2.3V〜5.5V
5. SCL_{IN}およびSDA_{IN}1Vまでは、SDA_{OUT}およびSCL_{OUT}2.3V〜5.5V

---

なお、このページは、I²Cインターフェースに関する情報を提供しています。I²Cバッファと立ち上がり時間アクセラレータ、I²Cマルチプレクサとアドレス変換器について詳しく説明しています。各モデルの仕様やスペック、利用シーンの適性までを考慮に入れ、適切な選択をサポートしています。
### I²C アイソレータ / I²C Isolators

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Insulation Rating (kV rms)</th>
<th>Power Supply (V)</th>
<th>Serial Data</th>
<th>Serial Clock</th>
<th>Max Frequency (kHz)</th>
<th>Integrated, Isolated Power</th>
<th>Isolated Power Output</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3260</td>
<td>2.5</td>
<td>3.0 to 5.5</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>1000</td>
<td>•</td>
<td></td>
<td>105</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADuM1250 ²</td>
<td>2.5</td>
<td>3.0 to 5.5</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>1000</td>
<td></td>
<td></td>
<td>125</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADuM1251</td>
<td>2.5</td>
<td>3.0 to 5.5</td>
<td>Bidirectional</td>
<td>Unidirectional</td>
<td>1000</td>
<td>16-lead SOIC_W, 16-lead SOIC_IC</td>
<td></td>
<td>125</td>
<td>8-lead SOIC</td>
</tr>
<tr>
<td>ADuM2250 ²</td>
<td>5</td>
<td>3.0 to 5.5</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>1000</td>
<td></td>
<td></td>
<td>105</td>
<td>16-lead SOIC_W, 16-lead SOIC_IC</td>
</tr>
<tr>
<td>ADuM2251 ²</td>
<td>5</td>
<td>3.0 to 5.5</td>
<td>Bidirectional</td>
<td>Unidirectional</td>
<td>1000</td>
<td></td>
<td></td>
<td>105</td>
<td>16-lead SOIC_W, 16-lead SOIC_IC</td>
</tr>
<tr>
<td>LTM2883-3P</td>
<td>2.5</td>
<td>3.0 to 3.6</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>400</td>
<td>•</td>
<td>0.6 W (adj 3 V to 5 V, adj 12 V, adj –12 V)</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2883-5P</td>
<td>2.5</td>
<td>4.5 to 5.5</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>400</td>
<td>•</td>
<td>0.6 W (adj 3 V to 5 V, adj 12 V, adj –12 V)</td>
<td>105</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2886-3P</td>
<td>2.5</td>
<td>3.0 to 3.6</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>400</td>
<td>•</td>
<td>1 W (adj 3 V to 5 V, 5 V, –5 V)</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2886-5P</td>
<td>2.5</td>
<td>4.5 to 5.5</td>
<td>Bidirectional</td>
<td>Bidirectional</td>
<td>400</td>
<td>•</td>
<td>1 W (adj 3 V to 5 V, 5 V, –5 V)</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2887-3P</td>
<td>2.5</td>
<td>3.0 to 3.6</td>
<td>Bidirectional</td>
<td>Unidirectional</td>
<td>400</td>
<td>•</td>
<td>1 W (adj 1.8 V to 5 V, adj 0.6 V to 5 V)</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2887-5P</td>
<td>2.5</td>
<td>4.5 to 5.5</td>
<td>Bidirectional</td>
<td>Unidirectional</td>
<td>400</td>
<td>•</td>
<td>1 W (adj 1.8 V to 5 V, adj 0.6 V to 5 V)</td>
<td>125</td>
<td>32-ball BGA</td>
</tr>
<tr>
<td>LTM2892-2</td>
<td>3.5</td>
<td>3.0 to 5.5</td>
<td>Bidirectional</td>
<td>Unidirectional</td>
<td>400</td>
<td></td>
<td></td>
<td>125</td>
<td>24-ball BGA</td>
</tr>
</tbody>
</table>

### IO リンクトランシーバ / IO-Link Transceivers

<table>
<thead>
<tr>
<th>Part Number</th>
<th>IO-Link Type</th>
<th>Drivers/ Receivers</th>
<th>Max Data Rate (Mbps)</th>
<th>Supply Voltage (V)</th>
<th>Overvoltage Protection (V)</th>
<th>Load Current (mA)</th>
<th>LDO (mA)</th>
<th>Adj</th>
<th>Slew</th>
<th>Wake-Up Pulse</th>
<th>L+</th>
<th>Hot Swap</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>LT3669</td>
<td>Device</td>
<td>1/1</td>
<td>0.23</td>
<td>7.5 to 40</td>
<td>±60</td>
<td>100</td>
<td>150</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>150</td>
<td>28-ball QFN</td>
</tr>
<tr>
<td>LT3669-2</td>
<td>Device</td>
<td>1/1</td>
<td>0.23</td>
<td>7.5 to 40</td>
<td>±60</td>
<td>50</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>150</td>
<td>28-ball QFN</td>
</tr>
<tr>
<td>LTC2874</td>
<td>Master</td>
<td>4/4</td>
<td>0.23</td>
<td>8.0 to 34.0, VL 2.9 to 5.5</td>
<td>±50</td>
<td></td>
<td>•</td>
<td>•</td>
<td>•</td>
<td></td>
<td></td>
<td></td>
<td>85</td>
<td>38-ball TSSOP</td>
</tr>
</tbody>
</table>

### USB 2.0 アイソレータ / USB 2.0 Isolators

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Insulation Rating (kV rms)</th>
<th>Data Rate (Mbps)</th>
<th>Low Speed</th>
<th>Full Speed</th>
<th>Integrated Isolated Power</th>
<th>Isolated Power Output</th>
<th>ESD Protection (kV)</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM3160</td>
<td>2.5</td>
<td>1.5</td>
<td>1</td>
<td>12</td>
<td>12</td>
<td>10</td>
<td>105</td>
<td>16-lead SOIC_W</td>
<td></td>
</tr>
<tr>
<td>ADM4160</td>
<td>5</td>
<td>1.5</td>
<td>1</td>
<td>12</td>
<td>12</td>
<td>10</td>
<td>105</td>
<td>16-lead SOIC_W, 16-lead SOIC_IC</td>
<td></td>
</tr>
<tr>
<td>LTM2884</td>
<td>2.5</td>
<td>1.5</td>
<td>12</td>
<td>•</td>
<td>2.5 W @ 5 V (VCC 8.6 V to 16.5 V) or 1 W @ 5 V (VCC 4.4 V to 8.6 V)</td>
<td>15</td>
<td>105</td>
<td>44-ball BGA</td>
<td></td>
</tr>
<tr>
<td>LTM2884</td>
<td>7.5</td>
<td>1.5</td>
<td>12</td>
<td>•</td>
<td>2.5 W @ 5 V (VCC 8.6 V to 16.5 V) or 1 W @ 5 V (VCC 4.4 V to 8.6 V)</td>
<td>20</td>
<td>125</td>
<td>24-ball BGA</td>
<td></td>
</tr>
</tbody>
</table>

### SPI デジタル・アイソレータ SPIsolator® と絶縁型 SPI µModule / SPI Digital Isolators—SPIsolator® or µModule Isolated SPI

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Product Feature</th>
<th>Power Supply (V)</th>
<th>Insulation Rating (kV rms)</th>
<th>No. Auxiliary Inputs</th>
<th>Slave Ports</th>
<th>Max SPI CLK Rate (MHz)</th>
<th>Integrated Isolated Power</th>
<th>Isolated Power Output</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADuM3150</td>
<td>High speed</td>
<td>3.0 to 5.5</td>
<td>3.75</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>40</td>
<td></td>
<td>125</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADuM3151</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>3.75</td>
<td>2</td>
<td>1</td>
<td>1</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADuM3152</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>3.75</td>
<td>1</td>
<td>2</td>
<td>1</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADuM3153</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>3.75</td>
<td>0</td>
<td>3</td>
<td>1</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADuM3154</td>
<td>Multiple slave</td>
<td>3.0 to 5.5</td>
<td>3.75</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SSOP</td>
</tr>
<tr>
<td>ADuM4150</td>
<td>High speed</td>
<td>3.0 to 5.5</td>
<td>5</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>40</td>
<td></td>
<td>125</td>
<td>20-lead SOIC_IC</td>
</tr>
<tr>
<td>ADuM4151</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>5</td>
<td>2</td>
<td>1</td>
<td>1</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SOIC_IC</td>
</tr>
<tr>
<td>ADuM4152</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>5</td>
<td>1</td>
<td>2</td>
<td>1</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SOIC_IC</td>
</tr>
<tr>
<td>ADuM4153</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>5</td>
<td>0</td>
<td>3</td>
<td>1</td>
<td>17</td>
<td></td>
<td>125</td>
<td>20-lead SOIC_IC</td>
</tr>
</tbody>
</table>

¹ 車載アプリケーション認定モデルが利用可能です。詳細については、製品ページをご参照ください。
² ロジック電源電圧制限は、または VL: 3 V ~ 5 V、LTM2883, LTM2887, LTM2892 には補助チャンネルが含まれます。
<table>
<thead>
<tr>
<th>Part Number</th>
<th>Product Feature</th>
<th>Power Supply (V)</th>
<th>Insulation Rating (kV rms)</th>
<th>No. Auxiliary Inputs</th>
<th>Slave Ports</th>
<th>Max SPI CLK Rate (MHz)</th>
<th>Integrated Isolated Power</th>
<th>Isolated Power Output</th>
<th>Max Temp (°C)</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADuM4154</td>
<td>Multiple slave support</td>
<td>3.0 to 5.5</td>
<td>5</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>17</td>
<td>125</td>
<td>20-lead SOIC, IC</td>
<td></td>
</tr>
<tr>
<td>LTM2883-3S</td>
<td>Aux channels</td>
<td>3.0 to 3.6</td>
<td>2.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>105</td>
<td>32-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2883-5S</td>
<td>Aux channels</td>
<td>4.5 to 5.5</td>
<td>2.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>105</td>
<td>32-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2886-3S</td>
<td>Aux channels</td>
<td>3.0 to 3.6</td>
<td>2.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>105</td>
<td>32-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2886-5S</td>
<td>Aux channels</td>
<td>4.5 to 5.5</td>
<td>2.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>105</td>
<td>32-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2887-3S</td>
<td>Aux channels</td>
<td>3.0 to 3.6</td>
<td>2.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>105</td>
<td>32-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2887-5S</td>
<td>Aux channels</td>
<td>4.5 to 5.5</td>
<td>2.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>105</td>
<td>32-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2892-30</td>
<td>Aux channels</td>
<td>3.0 to 5.5</td>
<td>3.5</td>
<td>0</td>
<td>2</td>
<td>1</td>
<td>8</td>
<td>125</td>
<td>24-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2893</td>
<td>ADC</td>
<td>3.0 to 5.5</td>
<td>6</td>
<td>0 to 3</td>
<td>0 to 3</td>
<td>2</td>
<td>100</td>
<td>125</td>
<td>36-ball PGA</td>
<td></td>
</tr>
<tr>
<td>LTM2895</td>
<td>DAC</td>
<td>3.0 to 5.5</td>
<td>6</td>
<td>0 to 3.3</td>
<td>0 to 3</td>
<td>2</td>
<td>100</td>
<td>125</td>
<td>36-ball PGA</td>
<td></td>
</tr>
</tbody>
</table>

*: ロックグラス電圧範囲 | 上限または下限 | 1.6 V ～ 5.5 V
*: ロックグラス電圧範囲 | 上限または下限 | 1.71 V ～ 5.5 V

WHEN ANALOG DEVICES
AND LINEAR TECHNOLOGY
COMBINE ...

さらなるイノベーションの創出を可能人に
アナログ・デバイセスと旧リニアテクノロジーは組み合わせ、最高度のイノベーションを生み出し、お客様の戦略的パートナーとなります。

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