

HIGH SPEED DESIGN SEMINAR

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ANALOG DEVICES TECHNICAL REFERENCE BOOKS

Published by Prentice Hall

Analog-Digital Conversion Handbook
Digital Signal Processing Applications Using the ADSP-2100 Family
Digital Signal Processing in VLSI

Published by Analog Devices

High-Speed Design Seminar
Nonlinear Circuits Handbook
Transducer Interfacing Handbook
Synchro & Resolver Conversion

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DEDICATION

This seminar is respectfully dedicated to the memory of the late Edward L. Graves, whose long range vision and insights regarding future trends in high-speed data acquisition technology have stood the test of time.

Walt Kester
Greensboro, NC
1990

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SECTION I HIGH SPEED A/D CONVERSION

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References

Technical Article:

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