

## **EVAL-ADPL21501-AZ**

Evaluates: ADPL21501 in a 30V Output Voltage Application

## **General Description**

The EVAL-ADPL21501-AZ evaluation board features the ADPL21501, a micropower step-up DC/DC converter in a ThinSOT™ package.

The ADPL21501 is designed for higher power systems with a 100mA current limit. A current-limited, fixed off-time control scheme conserves operating current, resulting in high efficiency over a broad range of load current.

The ADPL21501's low off-time of 400ns permits the use of tiny, low-profile inductors and capacitors to minimize footprint and cost in space-conscious applications.

The EVAL-ADPL21501-AZ evaluation board features an application circuit with a 30V output voltage, operating within an input voltage range of 1V to 6V and a load current of 500µA.

The ADPL21501 data sheet must be read in conjunction with this user guide to properly use or modify the evaluation board.

### **Features and Benefits**

- $V_{OUT} = 30V$ ,  $V_{IN}$  Range = 1V to 6V,  $I_{OUT} = 500\mu$ A
- Resistor Programmable Output Voltage
- Features a Current-Limited, Fixed Off-Time Control Scheme
- Internal Compensation and Soft-Start
- Proven PCB Layout
- Fully Assembled and Tested

### **Quick Start**

#### **Required Equipment**

- One 10V, 1A DC Power Supply
- Digital Multimeters (DMM)
- Load Resistors capable of sinking up to 500µA at 30V output

#### **Procedure**

The evaluation board is fully assembled and tested. Use the following steps to verify the board operation.

Note: Do not turn on the power supply until all connections are completed.

- 1. Ensure that the JP1 jumper is in the ON position.
- 2. Disable the power supply and set the input power supply at a voltage between 1V to 6V.
- 3. Connect the positive terminal of the power supply to the VIN PCB pad and the negative terminal to the nearest GND PCB pad.
- 4. Connect the positive terminal of the load to the VOUT pad and the negative terminal to the nearest GND PCB

Caution: Do not enable the load until the power supply is turned on.

- 5. Connect the DMM across the VOUT PCB pad and the nearest GND PCB pad.
- 6. Ensure that the EN/UVLO JP1 jumper is in the ON position.
- 7. Turn on the input power supply.
- 8. Enable the load.
- 9. Verify that the DMM displays the expected terminal voltage with respect to GND.

Ordering Information appears at end of data sheet.

# **EVAL-ADPL21501-AZ Evaluation Board Configuration**

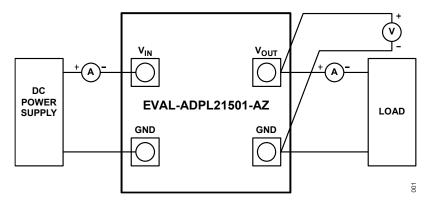


Figure 1. EVAL-ADPL21501-AZ Evaluation Board Connections

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## **Typical Performance Characteristics**

T<sub>A</sub> = 25°C, all measurements are in reference to <u>EVAL-ADPL21501-AZ Evaluation Board Schematic Diagram</u>, unless otherwise noted.

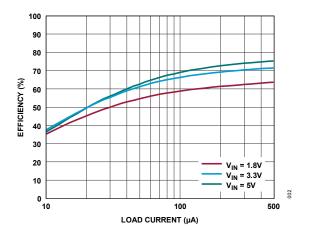


Figure 2. Efficiency vs. Output Current

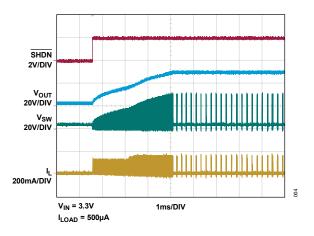


Figure 4. Start-up Waveform

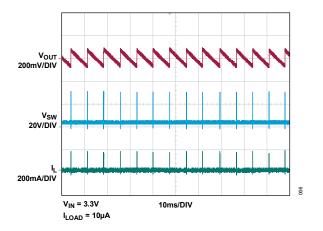


Figure 6. Steady-state Waveform at Light Load

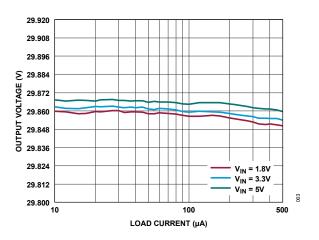


Figure 3. Output Voltage vs. Output Current

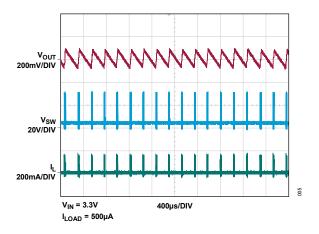


Figure 5. Steady-state Waveforms at Full Load

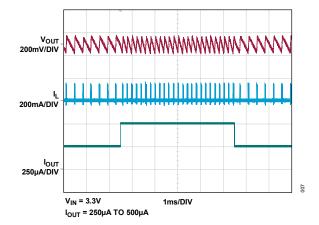


Figure 7. Transient Response 250µA to 500µA

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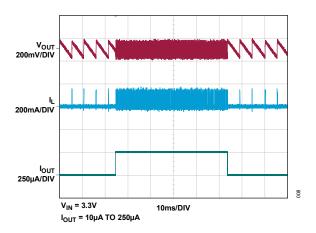


Figure 8. Transient Response Light Load to 250μA

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### **Detailed Description**

The EVAL-ADPL21501-AZ evaluation board is designed to demonstrate the salient features of ADPL21501, a micropower step-up DC/DC converters in a ThinSOT package. The evaluation board is preset for 30V output from 1V to 6V input and can deliver load current up to 500μA.

### **SHDN** Programming

The EVAL-ADPL21501-AZ evaluation board offers a jumper JP1 for the SHDN pin feature. When Jumper JP1 is connected across pins 2-3, the EVAL-ADPL21501-AZ is enabled. To disable, install the shunt across pins 1-2 on Jumper J1. See *Table 1* for Jumper settings.

Table 1. Jumper Connection Guide (JP1)

SHUNT POSITION	SHDN PIN	ОИТРИТ
1-2	Connected to GND	OFF
2-3*	Connected to V <sub>IN</sub>	ON

<sup>\*</sup>Default position.

### **Adjusting Output Voltage**

The ADPL21501 supports an adjustable output voltage of up to 30V. EVAL-ADPL21501-AZ evaluation board is configured to 30V output. The output voltage is programmed using the resistor dividers R1 and R2. Refer to the <u>ADPL21501 data</u> sheet for more details.

### **Input Capacitor Selection**

The input capacitor, C2, reduces current peaks drawn from the input power supply and reduces the switching frequency ripple at the input. Refer to the *Capacitor Selection* section in the <u>ADPL21501 data sheet</u> for more details. The input capacitor is chosen to be 4.7µF.

#### **Output Capacitor Selection**

The output capacitor C3 is chosen as  $1\mu F/100V$ . Refer to the *Capacitor selection* section in the <u>ADPL21501 data sheet</u> for more details.

#### **Diode Selection**

The EVAL-ADPL21501-AZ evaluation board circuit uses a low-leakage rectifier diode to further minimize input current at light loads and no load. Refer to the *Diode Selection* section in the *ADPL21501 data sheet* for more details.

#### Hot Plug-In and Long input cables

The EVAL-ADPL21501-AZ evaluation board provides an electrolytic capacitor to dampen input voltage peaks and oscillations that can arise during hot-plug-in and/or due to long input cables. These capacitors limit the peak voltage at the input of the DC-DC converters when the evaluation board is powered directly from a precharged capacitive source or an industrial backplane Printed circuit board (PCB). Long input cables between an input power source and the evaluation board circuit can cause input-voltage oscillations due to the inductance of the cables. The Equivalent series resistance (ESR) of the electrolytic capacitor helps damp out the oscillations caused by long input cables.

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# **Ordering Information**

PART	TYPE	
EVAL-ADPL21501-AZ	Evaluation Board	

Z = RoHS-compliant part.

# **Component Suppliers**

SUPPLIER	WEBSITE
Murata Americas	www.murata.com
Coilcraft	www.coilcraft.com
Würth Elektronik	www.we-online.com
Panasonic	www.industrial.panasonic.com
Vishay	www.vishay.com
Onsemi	www.onsemi.com

**Note:** When contacting these component suppliers, indicate that the ADPL21501 is used.

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## **EVAL-ADPL21501-AZ Evaluation Board Bill of Materials**

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER, PART NUMBER	
1	1	C1	CAP, ALUM 33µF 25V 20%	Panasonic, 25SVPK33M	
1	1	C2	CAP CER, X7R 4.7µF 16V 10% 0805	Murata, GRM21BR71C475KE51L	
2	1	C3	CAP CER X7R 1µF 100V 10% 1206	Murata, GRM31CR72A105KA01K	
3	1	L1	IND, 22µH 20% 0.85A 1.04OHM DCR	Würth Electronik, 74438335220	
4	1	D1	Diode, Schottky 40V 0.5A	Onsemi, MBR0540T1G	
5	1	R1	Res, 10MegΩ 1% 1/16W 0402	Vishay, CRCW040210M0FKED	
6	1	R2	Res, 432kΩ 1% 1/16W 0402	Vishay, CRCW0402432KFKED	
7	5	J1-J5	Turret	MILL-MAX 2501-2	
8	1	JP1	HEADER, 3PIN	Sullins, PBC03SABN	
9	1	U1	ADPL21501 IC	Analog Devices IC, ADPL21501IS5#TRPBF	
10	1	C4	OPEN: Capacitor (0402)	_	

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# **EVAL-ADPL21501-AZ Evaluation Board Schematic Diagram**

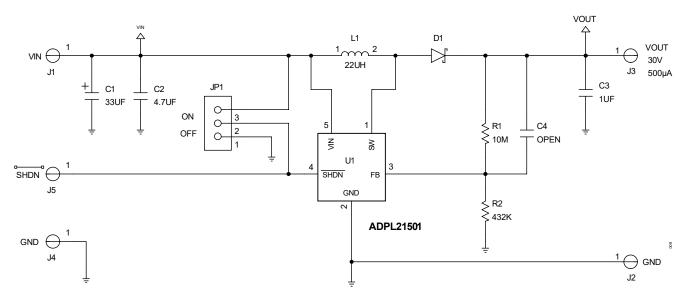


Figure 9. EVAL-ADPL21501-AZ Evaluation Board Schematic Diagram

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## **EVAL-ADPL21501-AZ Evaluation Board PCB Layout Diagrams**

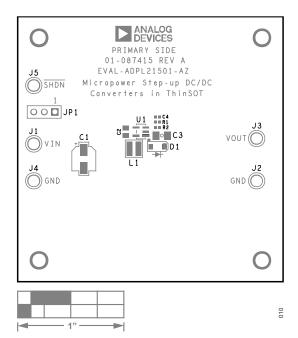


Figure 10. EVAL-ADPL21501-AZ Evaluation Board Component Placement Guide—Top Silkscreen

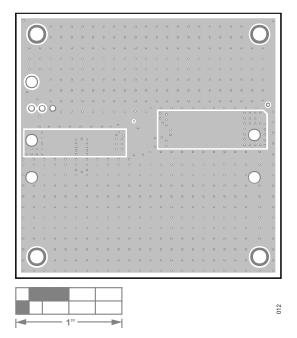


Figure 12. EVAL-ADPL21501-AZ Evaluation Board PCB Layout—Layer 2

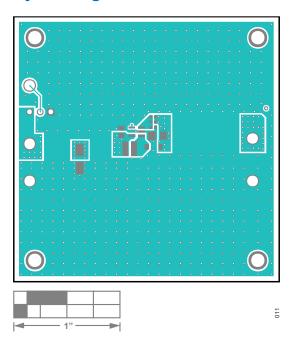


Figure 11. EVAL-ADPL21501-AZ Evaluation Board PCB Layout—Layer 1

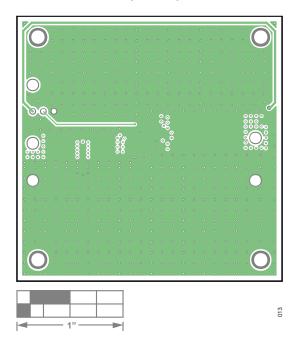


Figure 13. EVAL-ADPL21501-AZ Evaluation Board PCB Layout—Layer 3

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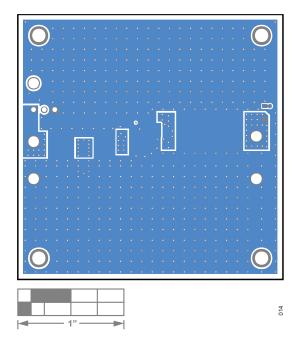


Figure 14. EVAL-ADPL21501-AZ Evaluation Board PCB Layout—Layer 4

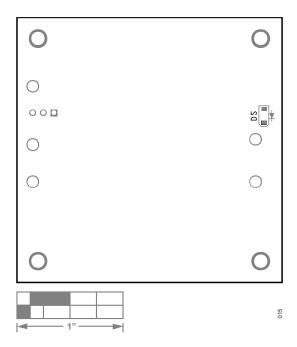


Figure 15. EVAL-ADPL21501-AZ Evaluation Board Component Placement Guide—Bottom Silkscreen

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# **Revision History**

REVISION	REVISION	DESCRIPTION	PAGES
NUMBER	DATE		CHANGED
0	06/25	Initial release	_

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#### **Notes**

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