Evaluating the **AD9838** 11 mW Power, 2.3 V to 5.5 V, 16 MHz Complete DDS

### FEATURES
- Full featured evaluation board for the **AD9838**
- Graphical user interface software for board control and data analysis
- Connector to EVAL-SDP-CB1Z system demonstration platform (SDP) board
- Various power supply and reference link options

### APPLICATIONS
- Biomedical sensors
- Bioelectrical impedance analysis
- Electrochemical analysis
- Impedance spectroscopy
- Complex impedance measurement
- Nondestructive testing

### GENERAL DESCRIPTION
The **AD9838** is a 16 MHz low power DDS device capable of producing high performance sine and triangular outputs. It also has an on-board comparator that allows a square wave to be produced for clock generation. Consuming only 20 mW of power at 3 V makes the **AD9838** an ideal candidate for power-sensitive applications.

The EVAL-AD9838SDZ board is used in conjunction with an EVAL-SDP-CB1Z SDP board, available from Analog Devices, Inc. The USB-to-SPI communication to the **AD9838** is completed using this Blackfin®-based development board.

A high performance, on-board 16 MHz trimmed general oscillator is available to use as the master clock for the **AD9838** system. Various links and SMB connectors are also available on the EVAL-AD9838SDZ board to maximize usability.

Complete specifications for the **AD9838** are provided in the **AD9838** data sheet, available from Analog Devices, and should be consulted in conjunction with this user guide when using the evaluation board.

---

**FUNCTIONAL BLOCK DIAGRAM**

![Functional Block Diagram](image)

*Figure 1.*
TABLE OF CONTENTS

Features .............................................................................................. 1
Applications ....................................................................................... 1
General Description ......................................................................... 1
Functional Block Diagram .............................................................. 1
Revision History ............................................................................... 2
Evaluation Board Software .............................................................. 3
   Installing the Software ................................................................. 3
   Running the Software ................................................................. 4
Using the Evaluation Board Software ............................................ 5
   Setting Up the Digital Interface .................................................. 5
   Select External MCLK Frequency ............................................. 6

Programming Method: Hardware or Software .........................6
Loading Frequency and Phase Registers .....................................6
FSK and PSK Functionality ............................................................6
Waveform Options ........................................................................6
Hardware Options ..........................................................................7
Reset and Sweep ..........................................................................7
Example of Operation .....................................................................8
Evaluation Board Schematics and Layout ......................................9
Ordering Information ......................................................................11
Bill of Materials ............................................................................11

REVISION HISTORY

   Changed 75 MHz to 16 MHz, Title ................................................ 1
   Changes to Setting Up the Digital Interface Section ................... 5
   Changes to Select External MCLK Frequency Section,
      Programming Method: Hardware or Software Section,
      and Loading Frequency and Phase Registers Section ............. 6
   Changes to Example of Operation Section ................................... 8
   Changes to Figure 16................................................................. 9

8/12—Rev. 0 to Rev. A
   Changed LK3 Position from In to Out, Table 1 ............................ 4

4/11—Revision 0: Initial Version
EVALUATION BOARD SOFTWARE

INSTALLING THE SOFTWARE

The EVAL-AD9838SDZ evaluation kit includes the software and drivers on a CD. The software is compatible with Windows® XP, Windows Vista, and Windows 7.

To install the software, follow these steps:

1. Install the software before connecting the SDP board to the USB port of the PC.
2. Start the Windows operating system and insert the EVAL-AD9838SDZ evaluation kit CD.
3. Download the AD9838SDZ LabVIEW® software. The correct driver, SDPDriversNET, for the SDP board should download automatically after LabVIEW is downloaded, supporting both 32- and 64-bit systems. However, if the drivers do not download automatically, the driver executable file can also be found in the Program Files/Analog Devices folder. Follow the on-screen prompts to install the SDPDriverNet Version 1.3.6.0.
4. After installation of the software and drivers is complete, plug the EVAL-AD9838SDZ into the SDP board and the SDP board into the PC using the USB cable included in the box.
5. When the software detects the evaluation board, proceed through any dialog boxes that appear to finalize the installation (Found New Hardware Wizard/Install the Software Automatically and so on).

Figure 2. Hardware Device Manager Window with SDP Board Plugged In
RUNNING THE SOFTWARE

To run the evaluation board program, do the following:

1. Click Start/All Programs/Analog Devices/AD9838/AD9838 Eval Board.

2. If the SDP board is not connected to the USB port when the software is launched, a connectivity error displays (see Figure 3). Simply connect the evaluation board to the USB port of the PC, wait a few seconds, click Rescan, and follow the instructions.

3. Ensure that all links are in the correct positions (see Table 1). The main window of the AD9838DBZ evaluation software then opens, as shown in Figure 4.

<table>
<thead>
<tr>
<th>Link No.</th>
<th>Position</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>LK1</td>
<td>A</td>
<td>On-board linear regulator selected to supply power to the general oscillator.</td>
</tr>
<tr>
<td>LK2</td>
<td>B</td>
<td>3.3 V digital supply for the AD9838 supplied from the EVAL-SDP-CB1Z board.</td>
</tr>
<tr>
<td>LK3</td>
<td>Out</td>
<td>Decouple the CAP/2.5V pin to ground because VDD is &gt;2.7 V.</td>
</tr>
<tr>
<td>LK4</td>
<td>B</td>
<td>3.3 V analog supply for the AD9838 supplied from the EVAL-SDP-CB1Z board.</td>
</tr>
</tbody>
</table>

Figure 3. Pop-Up Window Error
USING THE EVALUATION BOARD SOFTWARE

SETTING UP THE DIGITAL INTERFACE

To set up the AD9838 to perform measurements, first plug the EVAL-SDP-CB1Z board into the EVAL-AD9838SDZ board and connect the system to the USB port of a PC. Then launch the evaluation software and set the DIGITAL INTERFACE. The EVAL-SDP-CB1Z has two connector plugs: connectorA and connectorB. Select which connector you want to use with the AD9838 evaluation board from the Connector drop-down menu.

The SPI Frame Frequency and SCLK Frequency boxes can also be set in this window. If the SPI interface speed has not been decided upon, leave the default values shown in Figure 5.
SELECT EXTERNAL MCLK FREQUENCY

Having selected the digital interface specifics, next use the EXTERNAL MCLK box to choose which frequency to use. The boards are supplied with a 16 MHz general oscillator. If a different clock source is required, the CLK1 SMB connector can be used to supply a different MCLK value.

Two options for the general oscillator include the AEL3013 oscillators from AEL Crystals and the SG-310SCN oscillators from Epson Electronics.

PROGRAMMING METHOD: HARDWARE OR SOFTWARE

Functions that select frequency and phase registers, reset internal registers, and power down the DAC can be implemented using either software or hardware. Figure 7 shows how to select the source of control for these functions. Alternatively, you can set the PIN/SW bit to 1 to select that these functions be controlled using the appropriate hardware control pins, or you can set the PIN/SW bit to 0 to select that these functions be controlled using the appropriate software control bits.

LOADING FREQUENCY AND PHASE REGISTERS

The desired output frequency and output phase can be loaded using the inputs shown in Figure 8. Either the FREQ0 register or the FREQ1 register can be loaded with frequency data. The frequency data is loaded in megahertz, and the equivalent hexadecimal code is shown to the right after data is entered; press the ENTER key to load data. After data is loaded, the output appears on the IOUT and IOUTB pins. Similarly, either the PHASE0 register or PHASE1 register can be selected, and the phase data is loaded in degrees.

The analog output frequency from the AD9838 is defined by

\[ f_{\text{MCLK}}/2^n \times FREQREG \]

where \( FREQREG \) is the value loaded into the selected frequency register in decimals. This signal is phase shifted by

\[ 2\pi/4096 \times PHASEREG \]

where \( PHASEREG \) is the value contained in the selected phase register in decimals.

FSK AND PSK FUNCTIONALITY

In software mode, the AD9838 can be set up for FSK or PSK functionality by simply entering the bit rate in milliseconds and clicking FSK or PSK (see Figure 9).

WAVEFORM OPTIONS

The output waveform can be selected as a sinusoidal waveform or a ramp waveform. The internal comparator in the AD9838 can be disabled or enabled (see Figure 10). The MSB or the MSB/2 of the phase accumulator can be selected as the output on the SIGN BIT OUT pin.
Power-Down Options

The AD9838 has various power-down options selected through the control register. The part can disable the MCLK or disable the DAC if just the MSB output is used on the SIGN BIT OUT pin, or it can power down both sections for a lower power sleep mode (see Figure 11).

![Figure 11. Power-Down Options](image1)

HARDWARE OPTIONS

If the hardware programming method is selected as shown in Figure 7, the following pin functions can be toggled: FSELECT, PSELECT, RESET, and SLEEP (by clicking FSEL, PSEL, Reset, and SLEEP, respectively; see Figure 12). These functions are controlled by the GPIO outputs on the EVAL-SDP-CB1Z board. For example, frequency data can be loaded to the FREQ0 register and the FREQ1 register, and the output can be toggled by the FSELECT pin, that is, 2FSK functionality.

![Figure 12. GPIO Hardware Pin Control](image2)

RESET AND SWEEP

The reset software command is set using the buttons shown in Figure 13. To set up a DDS sweep, click **Sweep**.

![Figure 13. Software Reset and Sweep Select](image3)

The sweep function allows users to load a start frequency, stop frequency, increment size, number of loops, and delay between each frequency increment. These commands are then loaded to the part automatically from the **EVAL-SDP-CB1Z** board.

![Figure 14. Sweep Functionality](image4)
EXAMPLE OF OPERATION

An example of configuring the AD9838 to output 10 kHz follows:

1. Plug the EVAL-SDP-CB1Z board into the EVAL-AD9838SDZ board and connect to the USB port.
2. Start up the software located at Start/All Programs/Analog Devices/AD9838/AD9838 Eval Board. You should see the SDP board communicating with the PC.
3. Select connectorA or connectorB; this must match what the AD9838 test chip is connected to.
4. Define MCLK; the default is an on-board 16 MHz oscillator.
5. Ensure that all links are in the correct positions (see Table 1).
6. Select the FREQ1 register.
7. Load a 10 kHz excitation frequency and press ENTER.

The output should appear on the IOOUT and IOOUTB outputs on the evaluation board.

For the FREQ0 register, follow the steps in the example operation, but with the following exceptions:

- In Step 6, select the FREQ0 register.
- In Step 7, load the FREQ0 register with 20 kHz and press ENTER.

Figure 15. 10 kHz Output Signals on an IOOUT Test Point
Figure 16. AD9838 Schematic Part A

Figure 17. AD9838 Schematic Part B
Figure 18. Component Side View Layer 1
### ORDERING INFORMATION

#### BILL OF MATERIALS

<table>
<thead>
<tr>
<th>Reference Designator</th>
<th>Description</th>
<th>Manufacturer</th>
<th>Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C2, C4 to C6, C8, C11, C12, C14, C15</td>
<td>0.1 µF ceramic capacitor, 50 V, X7R, ±10%, 0603</td>
<td>Murata</td>
<td>GRM188R71H104KA93D</td>
</tr>
<tr>
<td>C3</td>
<td>0.01 µF capacitor, 0603, 10 V, X5R, 10%</td>
<td>Kemet</td>
<td>C0603C103K5RACTU</td>
</tr>
<tr>
<td>C7, C9</td>
<td>10 µF tantalum capacitor, ±10%, 10 V, SMD, RTAJ_A</td>
<td>AVX</td>
<td>TAJA106K010R</td>
</tr>
<tr>
<td>C13, C16</td>
<td>1 µF capacitor, 10 V, Y5V, 0603, +80%, −20%</td>
<td>Yageo</td>
<td>CC0603ZRY5V6BB105</td>
</tr>
<tr>
<td>C17, C19</td>
<td>0.1 µF ceramic capacitor, 10 V, X7R, ±10%</td>
<td>Multicomp</td>
<td>B0603R104KCT</td>
</tr>
<tr>
<td>C18</td>
<td>10 µF ceramic capacitor, 10 V, 10%, X5R, 0805</td>
<td>Murata</td>
<td>GRM21BR61A106KE19L</td>
</tr>
<tr>
<td>CLK1, 1 F SEL1, 1 IOUT, IOUTB, PSEL1, 1 SBOUT, SLEEP</td>
<td>Straight PCB mount SMB jack, 50 Ω</td>
<td>Tyco</td>
<td>1-1337482-0</td>
</tr>
<tr>
<td>C3</td>
<td>0.01 µF capacitor, 0603, 10 V, X5R, 10%</td>
<td>Kemet</td>
<td>C0603C103K5RACTU</td>
</tr>
<tr>
<td>C3</td>
<td>0.01 µF capacitor, 0603, 10 V, X5R, 10%</td>
<td>Kemet</td>
<td>C0603C103K5RACTU</td>
</tr>
<tr>
<td>C7, C9</td>
<td>10 µF tantalum capacitor, ±10%, 10 V, SMD, RTAJ_A</td>
<td>AVX</td>
<td>TAJA106K010R</td>
</tr>
<tr>
<td>C13, C16</td>
<td>1 µF ceramic capacitor, 10 V, Y5V, 0603, +80%, −20%</td>
<td>Yageo</td>
<td>CC0603ZRY5V6BB105</td>
</tr>
<tr>
<td>C17, C19</td>
<td>0.1 µF ceramic capacitor, 10 V, X7R, ±10%</td>
<td>Multicomp</td>
<td>B0603R104KCT</td>
</tr>
<tr>
<td>C18</td>
<td>10 µF ceramic capacitor, 10 V, 10%, X5R, 0805</td>
<td>Murata</td>
<td>GRM21BR61A106KE19L</td>
</tr>
<tr>
<td>CLK1, 1 F SEL1, 1 IOUT, IOUTB, PSEL1, 1 SBOUT, SLEEP</td>
<td>Straight PCB mount SMB jack, 50 Ω</td>
<td>Tyco</td>
<td>1-1337482-0</td>
</tr>
<tr>
<td>FSYNC, IOUT_, IOUT_B, REF, RESET, SBOUT_, SCLK, SDATA</td>
<td>Red test point</td>
<td>Vero</td>
<td>20-313137</td>
</tr>
<tr>
<td>G1</td>
<td>Ground link, copper short</td>
<td>Not applicable</td>
<td>Not applicable</td>
</tr>
<tr>
<td>J1</td>
<td>120-way connector, 0.6 mm pitch, receptacle</td>
<td>HRS (Hirose)</td>
<td>FXB-120S-SV(21)</td>
</tr>
<tr>
<td>J2, J3</td>
<td>2-pin terminal block (5 mm pitch)</td>
<td>Campden</td>
<td>CTB5000/2</td>
</tr>
<tr>
<td>JK1, JK2, JK4</td>
<td>3-pin SIL header and shorting link</td>
<td>Harwin</td>
<td>M20-9990345 and M7567-05</td>
</tr>
<tr>
<td>JK3</td>
<td>2-pin SIL header and shorting link, SIP-2P</td>
<td>Harwin</td>
<td>M20-9990246</td>
</tr>
<tr>
<td>R1, R2, R8</td>
<td>10 kΩ SMD resistor, 0603</td>
<td>Multicomp</td>
<td>MC 0.063W 0603 10K</td>
</tr>
<tr>
<td>R3</td>
<td>50 Ω SMD resistor, 0603</td>
<td>Multicomp</td>
<td>MC 0.063W 0603 50r</td>
</tr>
<tr>
<td>R4</td>
<td>6.8 kΩ SMD resistor, 0603</td>
<td>Multicomp</td>
<td>MC 0.063W 0603 6K8</td>
</tr>
<tr>
<td>R5, R6</td>
<td>200 Ω SMD resistor, 0603</td>
<td>Multicomp</td>
<td>MC 0.063W 0603 200R</td>
</tr>
<tr>
<td>R7</td>
<td>300 Ω SMD resistor, 0603</td>
<td>Multicomp</td>
<td>MC 0.063W 0603 300R</td>
</tr>
<tr>
<td>R9, R10, R11, R12, R13,</td>
<td>0 Ω, 0603, SMD resistor</td>
<td>Multicomp</td>
<td>MC 0.063W 0603 0R</td>
</tr>
<tr>
<td>R14, R15, R16, R17</td>
<td></td>
<td>Analog Devices</td>
<td>AD9838BCPZ</td>
</tr>
<tr>
<td>U1</td>
<td>11 mW power, 2.3 V to 5.5 V, 16 MHz complete DDS, 20-lead LFCSP</td>
<td>Analog Devices</td>
<td>AD9838BCPZ</td>
</tr>
<tr>
<td>U2</td>
<td>64k I²C serial EEPROM, MSOP-8</td>
<td>Microchip</td>
<td>24LC64-I/MS</td>
</tr>
<tr>
<td>U3</td>
<td>Precision micropower, low dropout, low voltage references, 8-lead TSSOP</td>
<td>Analog Devices</td>
<td>REF196GRUZ</td>
</tr>
<tr>
<td>X1, X2</td>
<td>3 mm NPTH hole</td>
<td>Not applicable</td>
<td>MTHOLE-3mm</td>
</tr>
<tr>
<td>Y1</td>
<td>16 MHz, 3 mm x 2 mm SMD clock oscillator</td>
<td>Epson</td>
<td>SG-310 Series</td>
</tr>
</tbody>
</table>

1 Do not install.
ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

Legal Terms and Conditions

By using the evaluation board discussed herein (together with any tools, components documentation or support materials, the “Evaluation Board”), you are agreeing to be bound by the terms and conditions set forth below (“Agreement”) unless you have purchased the Evaluation Board, in which case the Analog Devices Standard Terms and Conditions of Sale shall govern. Do not use the Evaluation Board until you have read and agreed to the Agreement. Your use of the Evaluation Board shall signify your acceptance of the Agreement. This Agreement is made by and between you (“Customer”) and Analog Devices, Inc. (“ADI”), with its principal place of business at One Technology Way, Norwood, MA 02062, USA. Subject to the terms and conditions of the Agreement, ADI hereby grants to Customer a free, limited, personal, temporary, non-exclusive, non-sublicensable, non-transferable license to use the Evaluation Board FOR EVALUATION PURPOSES ONLY. Customer understands and agrees that the Evaluation Board is provided for the sole and exclusive purpose referenced above, and agrees not to use the Evaluation Board for any other purpose. Furthermore, the license granted is expressly made subject to the following additional limitations: Customer shall not (i) rent, lease, display, sell, transfer, assign, sublicense, or distribute the Evaluation Board; and (ii) permit any Third Party to access the Evaluation Board. As used herein, the term “Third Party” includes any entity other than ADI, Customer, their employees, affiliates and in-house consultants. The Evaluation Board is NOT sold to Customer; all rights not expressly granted herein, including ownership of the Evaluation Board, are reserved by ADI. CONFIDENTIALITY. This Agreement and the Evaluation Board shall all be considered the confidential and proprietary information of ADI. Customer may not disclose or transfer any portion of the Evaluation Board to any other party for any reason. Upon discontinuation of use of the Evaluation Board or termination of this Agreement, Customer agrees to promptly return the Evaluation Board to ADI. ADDITIONAL RESTRICTIONS. Customer may not disassemble, decompile or reverse engineer chips on the Evaluation Board. Customer shall inform ADI of any occurred damages or any modifications or alterations it makes to the Evaluation Board, including but not limited to soldering or any other activity that affects the material content of the Evaluation Board. Modifications to the Evaluation Board must comply with applicable law, including but not limited to the RoHS Directive. TERMINATION. ADI may terminate this Agreement at any time upon giving written notice to Customer. Customer agrees to return to ADI the Evaluation Board at that time. LIMITATION OF LIABILITY. THE EVALUATION BOARD PROVIDED HEREUNDER IS PROVIDED “AS IS” AND ADI MAKES NO WARRANTIES OR REPRESENTATIONS OF ANY KIND WITH RESPECT TO IT. ADI SPECIFICALLY DISCLAIMS ANY REPRESENTATIONS, ENDORSEMENTS, GUARANTEES, OR WARRANTIES, EXPRESS OR IMPLIED, RELATED TO THE EVALUATION BOARD INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, TITLE, FITNESS FOR A PARTICULAR PURPOSE OR NONINFRINGEMENT OF INTELLECTUAL PROPERTY RIGHTS. IN NO EVENT WILL ADI AND ITS LICENSORS BE LIABLE FOR ANY INCIDENTAL, SPECIAL, INDIRECT, OR CONSEQUENTIAL DAMAGES RESULTING FROM CUSTOMER’S POSSESSION OR USE OF THE EVALUATION BOARD, INCLUDING BUT NOT LIMITED TO LOST PROFITS, DELAY COSTS, LABOR COSTS OR LOSS OF GOODWILL. ADI’S TOTAL LIABILITY FROM ANY AND ALL CAUSES SHALL BE LIMITED TO THE AMOUNT OF ONE HUNDRED US DOLLARS ($100.00). EXPORT. Customer agrees that it will not directly or indirectly export the Evaluation Board to another country, and that it will comply with all applicable United States federal laws and regulations relating to exports. GOVERNING LAW. This Agreement shall be governed by and construed in accordance with the substantive laws of the Commonwealth of Massachusetts (excluding conflict of law rules). Any legal action regarding this Agreement will be heard in the state or federal courts having jurisdiction in Suffolk County, Massachusetts, and Customer hereby submits to the personal jurisdiction and venue of such courts. The United Nations Convention on Contracts for the International Sale of Goods shall not apply to this Agreement and is expressly disclaimed.