RedyKits for the ADP7104 Low Dropout (LDO) Linear Regulator

FEATURES
Several voltage options for evaluation
Surface-mount ADP7104 8-lead, exposed pad SOIC and LFCSP parts that can be easily soldered to the evaluation boards
Additional parts for prototyping

ADP7104RDZ-REDYKIT CONTENTS
1 ADP7104RD-EVALZ SOIC evaluation board with 3.3 V output
6 ADP7104 SOIC voltage options in a labeled bag

ADP7104CPZ-REDYKIT CONTENTS
1 ADP7104CP-EVALZ LFCSP evaluation board with 3.3 V output
6 ADP7104 LFCSP voltage options in a labeled bag

GENERAL DESCRIPTION
The ADP7104RDZ-REDYKIT™ and ADP7104CPZ-REDYKIT allow simplified prototyping and evaluation and are available for several standard fixed output voltage options in the ADP7104 product family. The RedyKits allow the user to evaluate the voltage options with two easy-to-order kits.

All surface-mount ADP7104 parts are sorted and stored in the kits with the Analog Devices, Inc., part number and fixed output voltage clearly printed on each zip-top bag.

The kits can be used in the engineering lab to evaluate different voltage options. If other voltage options must be evaluated, a different part from the kits can be easily soldered onto one of the evaluation boards supplied with each kit.

The ADP7104RDZ-REDYKIT and the ADP7104CPZ-REDYKIT are available with a fixed 3.3 V output voltage evaluation board. The kits also contain additional parts for prototyping.

The kit enclosure, which contains individually packaged parts and the evaluation board, is packaged in an ESD foam pad inside a treated cardboard box that measures 8.7” (L) × 7.9” (W) × 1.75” (H), that is, 22 cm (L) × 20 cm (W) × 4.5 cm (H).

The user guide for the ADP7104 evaluation boards is the UG-218.

PLEASE SEE THE LAST PAGE FOR AN IMPORTANT WARNING AND LEGAL TERMS AND CONDITIONS.
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REVISION HISTORY
10/11—Revision 0: Initial Version
EVALUATION BOARD LAYOUT AND CIRCUIT DIAGRAM

Figure 3. ADP7104 Evaluation Board Layout, SOIC

Figure 4. ADP7104 Evaluation Board Circuit, SOIC
Figure 5. ADP7104 Evaluation Board Layout, LFCSP

Figure 6. ADP7104 Evaluation Board Schematic, LFCSP
# ORDERING INFORMATION

## BILL OF MATERIALS

Table 1. ADP7104RDZ-REDYKIT

<table>
<thead>
<tr>
<th>Qty</th>
<th>Model</th>
<th>Output Voltage (V)</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>ADP7104ARDZ-1.5-R7</td>
<td>1.5</td>
<td>8-Lead SOIC_N_EP</td>
</tr>
<tr>
<td>3</td>
<td>ADP7104ARDZ-1.8-R7</td>
<td>1.8</td>
<td>8-Lead SOIC_N_EP</td>
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<tr>
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<td>ADP7104ARDZ-2.5-R7</td>
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<td>8-Lead SOIC_N_EP</td>
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</tr>
<tr>
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<td>ADP7104ARDZ-5.0-R7</td>
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<td>8-Lead SOIC_N_EP</td>
</tr>
<tr>
<td>3</td>
<td>ADP7104ARDZ-9.0-R7</td>
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<td>8-Lead SOIC_N_EP</td>
</tr>
<tr>
<td>1</td>
<td>ADP7104RD-EVALZ</td>
<td>3.3</td>
<td>SOIC evaluation board</td>
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Table 2. ADP7104CPZ-REDYKIT

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<th>Qty</th>
<th>Model</th>
<th>Output Voltage (V)</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>ADP7104ACPZ-1.5-R7</td>
<td>1.5</td>
<td>8-Lead LFCSP_WD</td>
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<td>ADP7104ACPZ-1.8-R7</td>
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<td>8-Lead LFCSP_WD</td>
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<td>ADP7104ACPZ-2.5-R7</td>
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<td>ADP7104ACPZ-3.0-R7</td>
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<tr>
<td>3</td>
<td>ADP7104ACPZ-5.0-R7</td>
<td>5.0</td>
<td>8-Lead LFCSP_WD</td>
</tr>
<tr>
<td>3</td>
<td>ADP7104ACPZ-9.0-R7</td>
<td>9.0</td>
<td>8-Lead LFCSP_WD</td>
</tr>
<tr>
<td>1</td>
<td>ADP7104CP-EVALZ</td>
<td>3.3</td>
<td>LFCSP evaluation board</td>
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</table>
Legal Terms and Conditions

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