Evaluation Board for Single, High Speed Op Amps
Offered in 5-Lead and 6-Lead SC-70 Packages

FEATURES
- Enables quick breadboarding/prototyping
- User defined circuit configuration
- Edge mounted SMA connector provisions
- Easy connection to test equipment and other circuits
- RoHS compliant

GENERAL DESCRIPTION
The Analog Devices, Inc., SC-70 evaluation board is designed to evaluate single, high speed op amps offered in 5-lead and 6-lead SC-70 packages. The evaluation board is a bare board that enables users to quickly prototype a variety of single op amp circuits, which minimizes risk and reduces time to market. Figure 1 shows the component side of the bare evaluation board. Figure 2 shows the circuit side of the bare evaluation board.

The 6-layer evaluation board accepts SMA edge-mounted connectors on the inputs and outputs for efficient connection to test equipment or other circuitry. The ground plane, component placement, and supply bypassing are designed to minimize parasitic inductance and capacitance. The evaluation board components are primarily SMT 0603 case size, with the exception of the electrolytic bypass capacitors (C1 and C2), which are 3528 case size.

Figure 3 shows the evaluation board schematic. The printed circuit board (PCB) assembly drawings are shown in Figure 4 and Figure 5. The layout pattern for the PCB is shown in Figure 6 and Figure 7.
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REVISION HISTORY
10/15—Rev. B to Rev. C
Changed C4 to C2, General Description Section ................ 1
Changed +VS to Vcc, Figure 3 .................................. 3

5/14—Rev. A to Rev. B
Changes to User Guide Title and General Description Section .... 1
Changes to Figure 1 and Figure 2 .............................. 1
Changes to Figure 3, Figure 4, and Figure 5 .................. 3
Changes to Figure 6 and Figure 7 ............................ 4
Changes to Table 1 ................................................ 5

4/11—Rev. 0 to Rev. A
Changes to User Guide Title and General Description Section, 1
Changes to Figure 3 Captions .................................. 3

4/10—Revision 0: Initial Version
EVALUATION BOARD SCHEMATIC, ASSEMBLY DRAWINGS, AND LAYOUT PATTERNS

*USER-DEFINED VALUE.

Figure 3. Evaluation Board Schematic

Figure 4. Component Side Assembly Drawing

Figure 5. Circuit Side Assembly Drawing
Figure 6. Component Side Layout Pattern

Figure 7. Circuit Side Layout Pattern
ORDERING INFORMATION

BILL OF MATERIALS

Table 1.

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<thead>
<tr>
<th>Quantity</th>
<th>Reference Designator</th>
<th>Description</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VEE, VCC, GND</td>
<td>Power connector</td>
<td>3-pin power connector</td>
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<tr>
<td>1</td>
<td>PD</td>
<td>Power-down/disable pin</td>
<td>2-pin header</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
<td>Test point</td>
<td>Test point pin</td>
</tr>
<tr>
<td>2</td>
<td>C1, C2</td>
<td>Capacitor, user defined</td>
<td>3528</td>
</tr>
<tr>
<td>2</td>
<td>C3, C4</td>
<td>SMA SMT</td>
<td>C603</td>
</tr>
<tr>
<td>3</td>
<td>INP, INN, OUT</td>
<td>Amplifier</td>
<td>SMA SMT</td>
</tr>
<tr>
<td>8</td>
<td>R1 to R8</td>
<td>Resistor, user defined</td>
<td>R603</td>
</tr>
<tr>
<td>1</td>
<td>DUT</td>
<td></td>
<td>SC-70</td>
</tr>
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