Evaluation Board for Quad, High Speed Op Amps
Offered in 14-Lead SOIC Packages

FEATURES
- Enables quick breadboarding/prototyping
- User-defined circuit configuration
- Edge-mounted SMA connector provisions
- Easy connection to test equipment and other circuits
- RoHS compliant

GENERAL DESCRIPTION
The Analog Devices, Inc., 14-lead SOIC evaluation board is designed to help users evaluate quad, high speed op amps offered in 14-lead SOIC packages. The evaluation board is a bare board (that is, there are no components or amplifier soldered to the board; these must be ordered separately) that enables users to quickly prototype a variety of quad op amp circuits. The evaluation board minimizes risk and reduces time to market. Figure 1 shows the component side of the bare evaluation board, and Figure 2 shows the circuit side.

The 2-layer evaluation board accepts SMA edge-mounted connectors on the inputs and outputs for efficient connection to test equipment or other circuitry. The ground plane, component placement, and supply bypassing are designed to minimize parasitic inductance and capacitance. The evaluation board components are primarily SMT 0805 case size, with the exception of the electrolytic bypass capacitors (C1 and C2), which are 3528 case size.

Figure 3 shows the evaluation board schematic. The PCB assembly drawings are shown in Figure 4 and Figure 5. The layout patterns for the PCB are shown in Figure 6 and Figure 7.
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REVISION HISTORY

4/11—Rev. 0 to Rev. A
Changes to General Description Section ................. 1
Deleted EB-O14R-4Z Throughout ......................... 1

3/10—Revision 0: Initial Version

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Figure 3. Evaluation Board Schematic

Figure 4. Component Side Assembly Drawing

Figure 5. Circuit Side Assembly Drawing
Figure 6. Component Side Layout Pattern

Figure 7. Circuit Side Layout Pattern
# ORDERING INFORMATION

## BILL OF MATERIALS

Table 1.

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<th>Quantity</th>
<th>Reference Designator</th>
<th>Description</th>
<th>Package</th>
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<td>+IN2, +IN3, −IN2, −IN3, IN1+, IN1−, IN4+, IN4−, OUT1, OUT2, OUT3, OUT4</td>
<td>SMA/SMT</td>
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<td>+VS, −VS, GND1, GND2, GND3, GND4</td>
<td>Test point</td>
<td>TP</td>
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<td>2</td>
<td>C1, C2</td>
<td>10 μF capacitor</td>
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<td>3</td>
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<td>Amplifier</td>
<td>14-lead SOIC</td>
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NOTES

ESD Caution
ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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