Evaluating the **AD8003** Triple, 1.5 GHz Op Amp

**FEATURES**
- Enables quick breadboarding/prototyping
- User-defined circuit configuration
- Edge-mounted SMA connector provisions
- Easy connection to test equipment and other circuits

**GENERAL DESCRIPTION**

The AD8003 evaluation board is specifically designed to evaluate the **AD8003**, a triple high speed, current feedback amplifier. The evaluation board is a bare board (that is, there are no components soldered to the board) that enables users to quickly prototype a variety of amplifier circuits, which minimizes risk and reduces time to market.

Figure 1 and Figure 2 show component side and circuit side of the bare evaluation board. Figure 3 shows the evaluation board schematic.

The evaluation board is a 4-layer printed circuit board (PCB) that accepts edge-mounted Subminiature A (SMA) connectors on the inputs and outputs, which allows efficient connection to test equipment and other circuits. The ground plane, component placement, and supply bypassing have been optimized for performance and circuit flexibility. The evaluation board uses a variety of surface-mount technology (SMT) component case sizes: 0402, 0603, and 3528.

Figure 4 and Figure 6 show the component side and circuit side of the evaluation board assembly drawing. The recommended layout pattern for connecting the board to the op amp and to the supporting circuitry is shown in Figure 5 and Figure 7.
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REVISION HISTORY
9/12—Rev. A to Rev. B
Changes to User Guide Title, General Description Section, and Figure 1 ...................................................................................... 1
Changes to Figure 2 Caption ........................................................... 3
Changes to Figure 4 to Figure 7 ...................................................... 4
Added Ordering Information Section, Bill of Materials, and Table 1 .............................................................................................. 5

2/10—Rev. 0 to Rev. A
Changes to General Description and Figure 1 .............................. 1

7/09—Revision 0: Initial Version
Figure 3. Evaluation Board Schematic
ASSEMBLY DRAWING AND BOARD LAYOUT

Figure 4. Board Assembly Drawing, Component Side

Figure 5. Board Layout Pattern, Component Side

Figure 6. Board Assembly Drawing, Circuit Side

Figure 7. Board Layout Pattern, Circuit Side
## ORDERING INFORMATION

### BILL OF MATERIALS

Table 1.

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<th>Quantity</th>
<th>Reference Designator</th>
<th>Description</th>
<th>Package</th>
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<td>1</td>
<td>PCB</td>
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<td>SMA connector</td>
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NOTES

ESD Caution
ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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