FEATURES
Enables quick breadboarding/prototyping
User defined circuit configuration
Edge mounted SMA connector provisions
Easy connection to test equipment and other circuits
Two independent circuits enhance flexibility

GENERAL DESCRIPTION
The EVAL-FDA-2CPZ-24 is designed to aid in the evaluation of dual high speed differential amplifiers. The EVAL-FDA-2CPZ-24 is a bare board (that is, there are no components soldered to the evaluation board) that enables users to quickly prototype a variety of differential amplifier circuits, which minimizes risk and reduces time to market. The EVAL-FDA-2CPZ-24 evaluation board supports any of the Analog Devices, Inc., dual high speed differential amplifiers in 4 mm × 4 mm, lead frame chip scale packages (LFCSP).

Figure 1 shows the component side and circuit side of the evaluation board. Figure 2 shows the evaluation board schematic.

The 4-layer evaluation board accepts edge mounted subminiture version A (SMA) connectors on both inputs and outputs, which allows efficient and quick connection to test equipment or other circuitry.

The evaluation board ground plane, component placement, and power supply bypassing are optimized for maximum circuit flexibility and performance. The evaluation board uses a variety of surface-mount technology (SMT) component case sizes: 0402, 0508, and 7343.

Figure 3 and Figure 5 show the evaluation board assembly drawings. Figure 4 and Figure 6 show the metal layout pattern for connecting the board to the operational amplifier and to the supporting circuitry.
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REVISION HISTORY
8/2016—Rev. B to Rev. C
Changed EB-D24CP44-2Z to EVAL-FDA-2CPZ-24 .......... Throughout
Changes to Table 1 ............................................................................. 5

1/2015—Rev. A to Rev. B
Changes to General Description Section ......................................... 1
Added Ordering Information Section and Table 1 ......................... 5

2/2010—Rev. 0 to Rev. A
Changes to General Description and Figure 1 .............................. 1

7/2009—Revision 0: Initial Version
Figure 2. Dual Differential Amplifier Universal Evaluation Board Schematic
ORDERING INFORMATION

BILL OF MATERIALS

Table 1.

<table>
<thead>
<tr>
<th>Qty</th>
<th>Reference Designator</th>
<th>Description</th>
<th>Package</th>
<th>Part Number</th>
</tr>
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<tr>
<td>10</td>
<td>GND1 to GND4, TP1 to TP6</td>
<td>Test points</td>
<td>0402</td>
<td>PXXXKLCT-ND</td>
</tr>
<tr>
<td>26</td>
<td>R1 to R24, R29 to R30</td>
<td>Resistors, user defined</td>
<td>0603</td>
<td>P0.0GCT-ND</td>
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<tr>
<td>4</td>
<td>R25 to R28</td>
<td>Resistors, 0 Ω</td>
<td>7343</td>
<td>399-3765-1-ND</td>
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<td>478-7414-1-ND or 399-1095-1-ND</td>
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<td>399-3027-1-ND</td>
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<td>Capacitors, 0.1 μF</td>
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<td>J502-ND</td>
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<tr>
<td>12</td>
<td>+IN1, −IN1, +IN2, −IN2, VOCM1, VOCM2, PD1/DIS1, PD2/DIS2, OUT1, −OUT1, +OUT2, −OUT2</td>
<td>SMA connectors</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>P1 to P3</td>
<td>3-pin headers, 100 mils spacing</td>
<td>TSW-103-08-G-S</td>
<td>SAM1038-03-ND</td>
</tr>
<tr>
<td>1</td>
<td>Z1</td>
<td>Dual differential amplifier</td>
<td>24-lead LFCSP</td>
<td>ADA4927-2, ADA4930-2, ADA4937-2, ADA4938-2, ADA4939-2, ADA4940-2</td>
</tr>
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