

High Side Current Sense with Comparators and a 400mV Reference

DESCRIPTION

Demonstration circuit 2193A features the [LT[®]6119](#), a high side current sense amplifier with comparators and a 400mV reference.

The demo board circuit amplifies the voltage across an onboard current sense resistor, to provide a precision unipolar voltage output that is proportional to the sensed current. The board has two onboard comparators with latching outputs and an integrated 400mV reference that sets the threshold for the comparators. There are two versions of the board, A and B. The version determines the available comparator inputs. The DC2261A-A (LT6119-1) has an inverting and a noninverting comparator input available while the DC2261A-B (LT6119-2) has two inverting inputs

available. The demonstration board includes circuitry to guarantee that the comparator resets on power up. The current sense gain and current fault limits are set by on board resistors. The default gain is set at 10V/A and the comparators are set to trip at current thresholds of 50mA and 300mA.

The key performance specifications are listed in the table below.

Design files for this circuit board are available at <http://www.linear.com/demo/DC2261A>

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PERFORMANCE SUMMARY Specifications are at T_A = 25°C

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V _{IN}	Input Supply Range		2.7		60	V
V _{OUTA}	Measured Output Signal	I _{LOAD} = 100mA		1		V
I _{LOAD}	Output Load Current Range	Thermal Limit of R _{SENSE}			3	A
I _Q	IN Quiescent Current	V _{IN} = 12V		600		μA
I _{THR1}	Comparator1 Threshold	V _{OUTA} = 3V		300		mA
I _{THR2}	Comparator2 Threshold	V _{OUTA} = 0.5V		50		mA

OPERATING PRINCIPLES

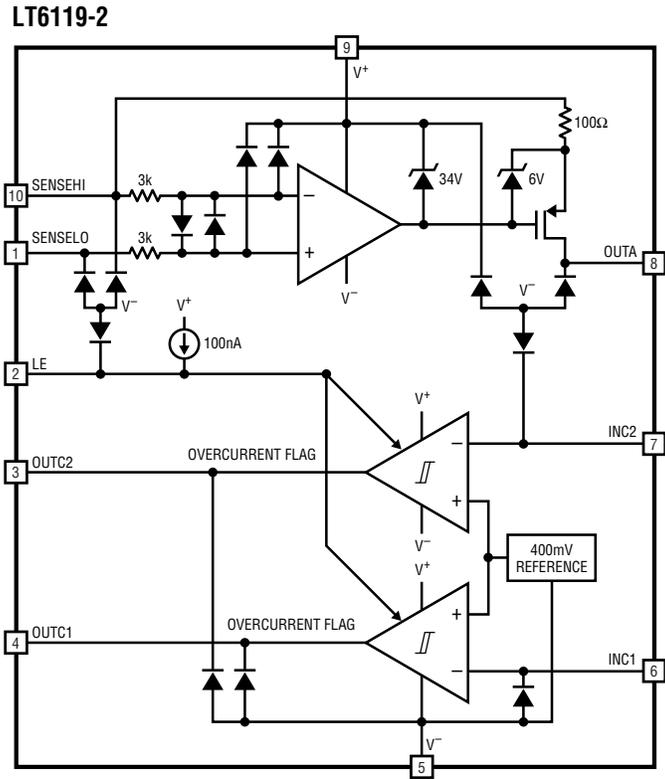
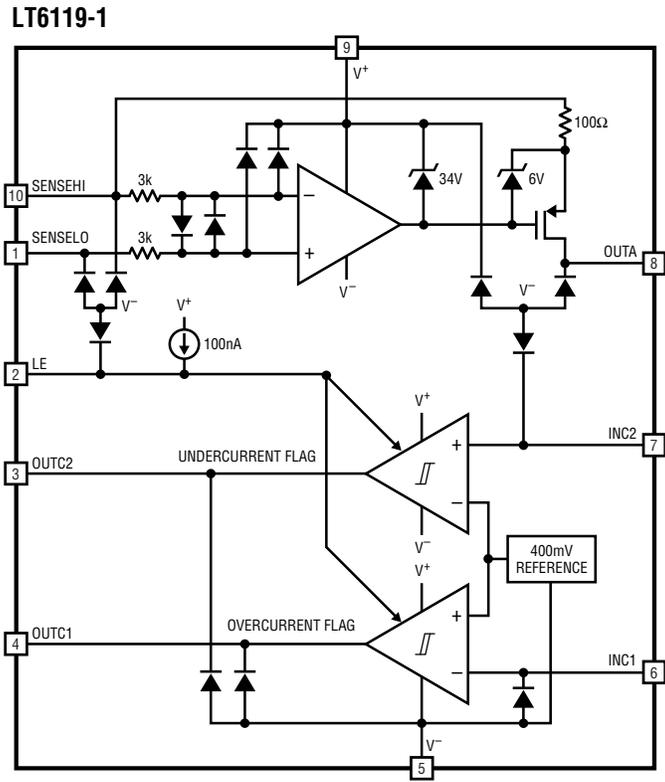
The LT6119 operates by amplifying the voltage drop across a sense resistor that is in series with the load. The sense inputs of the amplifier differentially measure the sense resistor voltage drop to control an internal variable current source that allows translation of the input voltage level to a level referenced to the GND. The circuit gain is set by the ratio of the output resistor to the input resistor. The comparator thresholds are set by the internal reference and the current trip points are set by dividing the output

resistance into a network of three resistors. The RC filter connected to the latch enable (LE) pin guarantee that the latch enable pin is held low long enough on start-up to reset the comparator output on power on.

The DC2261A is shipped with a 100mΩ sense resistor. The amplifier gain is configured for 100V/V, providing an overall circuit gain of 10V/A. The current fault thresholds are set at 50mA and 300mA.

DEMO MANUAL DC2261A

BLOCK DIAGRAM



dc2261af

QUICK START PROCEDURE

With the demonstration circuit, it is easy to set up and evaluate the performance of the LT6119. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

1. With the power off, connect the power supply positive to IN and the common to GND. With default settings the supply can range from 2.7V to 60V. If the supply does not have an accurate current readout, a DMM may be connected in series with the supply as shown in Figure 1.
2. Connect a voltmeter or oscilloscope to the OUTA with the common input connected to ground.
3. Turn on the power supply. The comparator outputs start in a known state as shown in the following table if there is no load attached.
4. Connect a load to the demo board OUT terminal (positive) and ground. The load may be a power resistor, active load instrument or a circuit of interest.
5. Measure the output voltage and confirm that it corresponds to the load current. $V_{OUTA} (V) = 10 \cdot I_{LOAD}(A)$ for the factory installed resistors.
6. If able, sweep load current from 0A to 300mA to verify the operation of the comparators. Use the reset push button to reset latched comparator outputs.

PART	OUTC1	OUTC2
DC2261A-A LT6119-1	High	Low
DC2261A-B LT6119-2	High	High

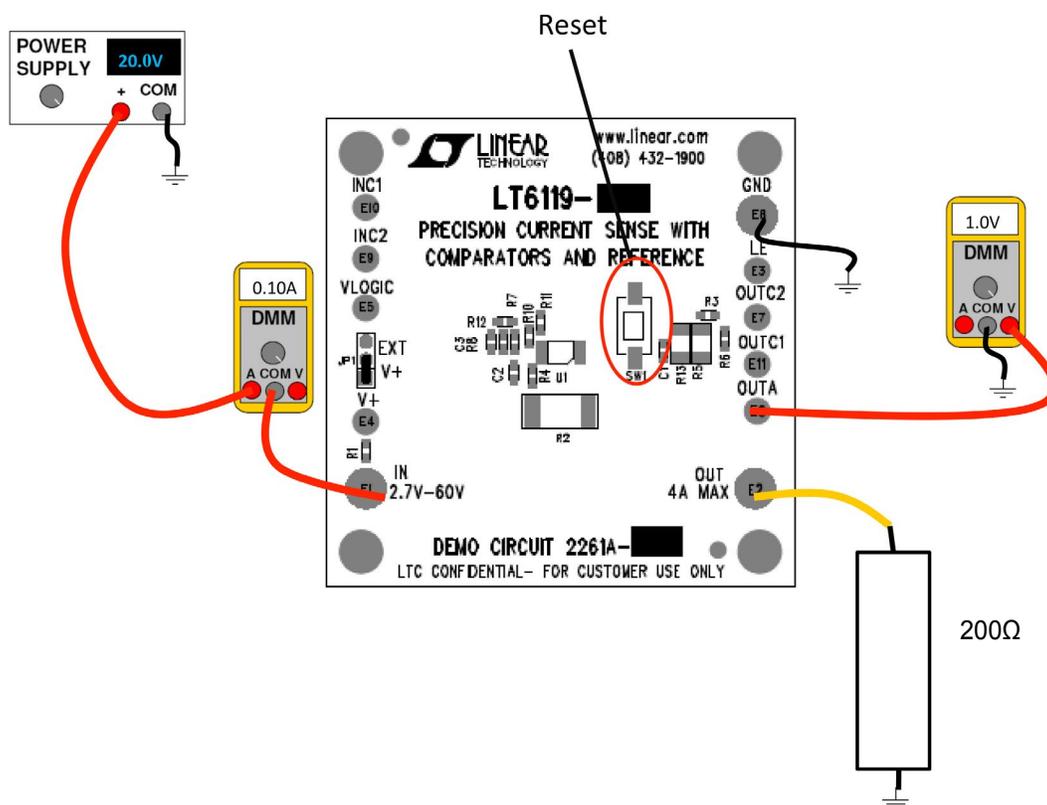


Figure 1. Test Setup

CURRENT SENSE GAIN

The gain of the LT6119 current sense amplifier is set by the ratio of R_{OUT} and R_{IN} resistors. In the demo circuit this means that the gain is set by the ratio of $(R_{OUT})/R4$, where $R_{OUT} = (R7 + R10 + R11) \parallel R8(OPT)$. The default values are $R7 = 2k$, $R10 = 6.65k$, $R11 = 1.33k$, and $R8$ is not installed. This provides a gain of $9.98k/100$. By installing the optional resistor, $R8$, the gain can be reduced. The gain error is primarily set by the precision of the resistors used:

The provided resistors are a 1% tolerance which will dominate the gain error. To reduce system gain error, the resistors can be replaced with 0.1% resistors.

Jumper Summary

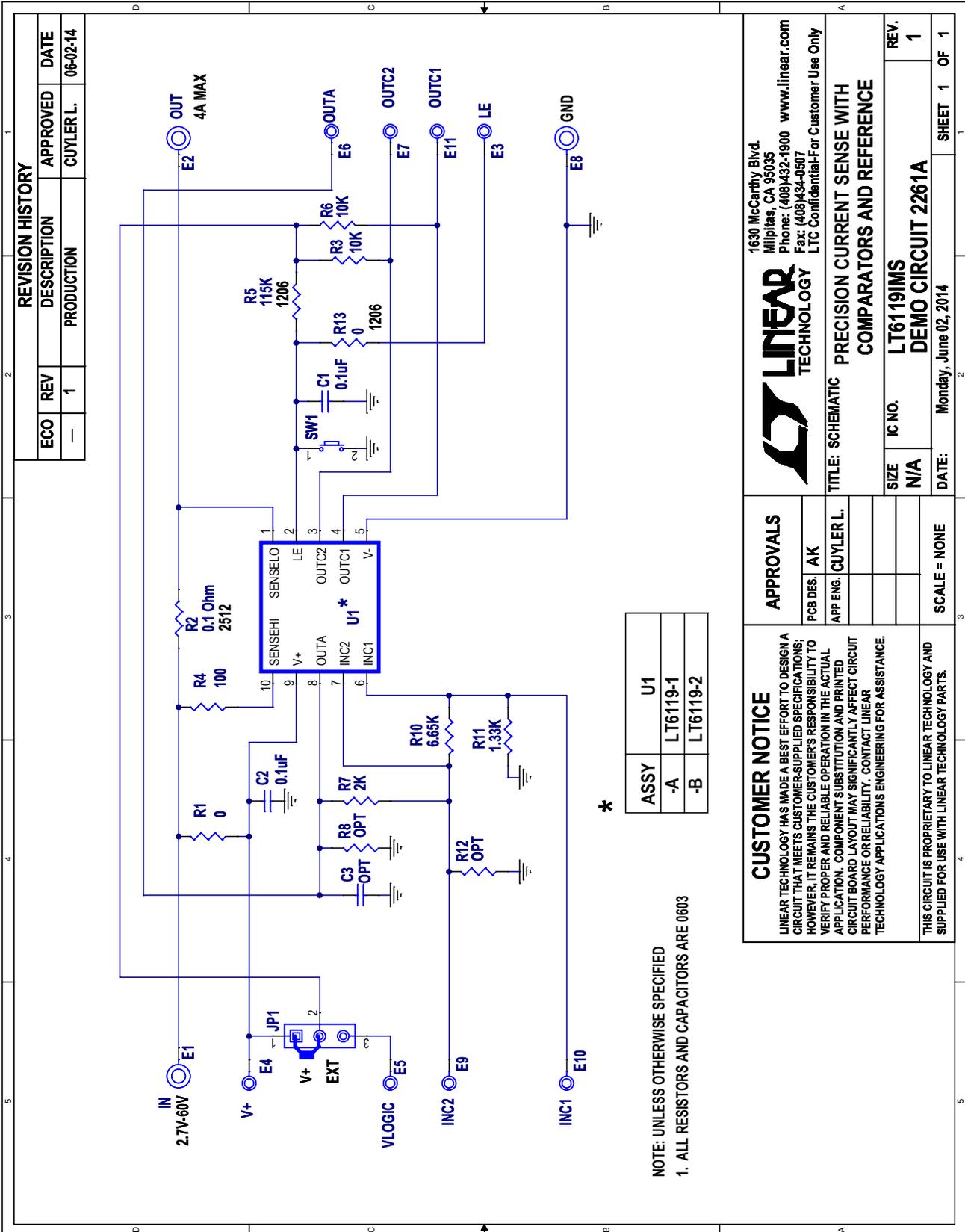
JP1: The Logic supply jumper has two settings.

- V^+ : the comparator output is tied to the V^+ supply
- EXT: the comparator output is tied to an external supply on the VLOGIC Turret

Optional Settings

1. To power the LT6119 from a supply separate from the load, remove $R1$ and connect the power supply to the V^+ turret.
2. There are a number of optional components that allow the DC2193 to be tailored to a specific application:
 - A) To free comparator inputs INC1 and INC2 for other use, remove $R7$, $R10$ and $R11$ and install $R8$. Connect LE turret as required.
 - B) To add filtering to the amplifier output, install $C3$.
 - C) If driving the LE turret from an external source, pressing the reset button, $SW1$ will short the signal. A resistor can be added at $R13$ to help isolate the external signal.

SCHEMATIC DIAGRAM



dc2261af

DEMO MANUAL DC2261A

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Mailing Address:

Linear Technology
1630 McCarthy Blvd.
Milpitas, CA 95035

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