

ring operated at a potential close to the SET pin, and tying the guard ring to the OUT pin. Guarding both sides of the circuit board is required. Bulk leakage

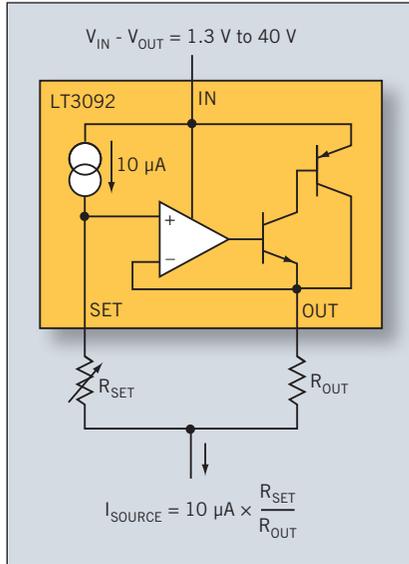


Fig. 2. The LT3092 uses two resistors (R_{OUT} and R_{SET}) to set an output current between 0.5 mA and 200 mA.

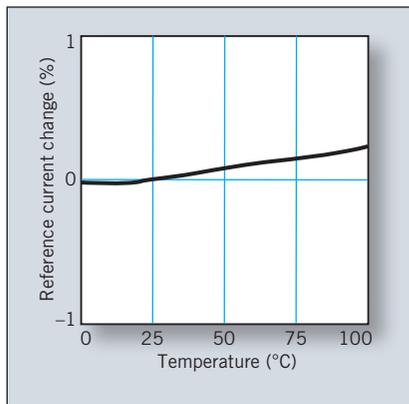


Fig. 3. Temperature change of the current source at 1-mA output current.

reduction depends on the guard ring width. A 10 nA of leakage into or out of the SET pin and its associated circuitry creates a 0.1% reference current error. Leakages of this magnitude, coupled with other sources of leakage, can cause significant offset voltage and reference current drift — especially over the possible operating temperature range.

The LT3092 does not require input or output capacitors for stability in many applications. Clean, tight PCB layouts provide a low-reactance, well-controlled operating environment for the IC without requiring capacitors for frequency compensation.

Some applications require a capacitor in parallel with R_{SET} to lower current source noise. This capacitor also provides a soft-start for the current source.

Although the LT3092 is stable without any capacitors over a variety of operating conditions, it may be necessary to add capacitors because of the input and output impedances encountered by the LT3092. These impedances may include resistive, capacitive and inductive components, and may be complex distributed networks. In addition, the current source's value will differ between applications, and its connection may be ground referenced, power supply referenced or floating in a signal line path.

If an application uses GND referred capacitors on the input or output (particularly the input), pay attention to the length of the lines powering and

returning ground from the circuit. In the case where long power supply and return lines are coupled with low ESR input capacitors, application-specific voltage spikes, oscillations and reliability concerns may be seen. This is not an issue with LT3092 stability, but rather the low ESR capacitor forming a high-Q resonant tank circuit with the inductance of the input wires. Adding series resistance with the input of the LT3092, or with the input capacitor, often solves this. Resistor values of 0.1 Ω to 1 Ω are often sufficient to dampen this resonance.

Give extra consideration to the use of ceramic capacitors. The X5R and X7R dielectrics result in more stable characteristics and are more suitable for use as the output capacitor. The X7R type has better stability across temperature, while the X5R is less expensive and is available in higher values.

Higher output current can be obtained by paralleling multiple LT3092s together. The simplest approach is to run two current sources side by side with both of their inputs and outputs tied together. This allows the sum of the current sources to deliver more output current than a single device can deliver.

Another method of paralleling devices requires fewer components and helps to share power between devices. Do this by tying the individual SET pins together and tying the individual IN pins together. Then, connect the outputs in common using small lengths of circuit board trace as ballast resistors to promote equal current sharing.

The LT3092's internal power and thermal limiting circuitry protects itself under overload conditions. For continuous normal load conditions, do not exceed the 125°C maximum junction temperature. Account for all thermal resistance sources from junction-to-ambient. Furthermore, consider all adjacent heat generating sources on the PCB within proximity of the LT3092.

The LT3092 is housed in the 8-lead TSOT-23, 3-lead SOT-223 and 8-lead 3-mm \times 3-mm DFN packages.

Table. (below) Possible relationships between R_{SET} , R_{OUT} and the output current.

R_{SET} (Ω)	R_{OUT} (Ω)	OUTPUT CURRENT (mA)
20,000	1.0	200.0
10,000	1.0	100.0
5,000	1.0	50.0
2,500	1.0	25.0
1,000	1.0	10.0
500	1.0	5.0
50	1.0	0.5