

RELIABILITY REPORT FOR

MAX969EEE

PLASTIC ENCAPSULATED DEVICES

October 15, 2008

# **MAXIM INTEGRATED PRODUCTS**

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

Approved by
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Quality Assurance
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### Conclusion

The MAX969EEE successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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### I. Device Description

A. General

The MAX965â€"MAX970 single/dual/quad micropower comparators feature rail-to-rail inputs and outputs, and fully specified single-supply operation down to +1.6V. These devices draw less than 5μA per comparator and have open-drain outputs that can be pulled beyond VCC to 6V (max) above ground. In addition, their rail-to-rail input common-mode voltage range makes these comparators suitable for ultra-low-voltage operation. A +1.6V to +5.5V single-supply operating voltage range makes the MAX965 family of comparators ideal for 2-cell battery-powered applications. The MAX965/MAX967/MAX968/MAX969 offer programmable hysteresis and an internal 1.235V ±1.5% reference. All devices are available in either space-saving 8-pin μMAX® or 16-pin QSOP packages.



## II. Manufacturing Information

A. Description/Function: Single/Dual/Quad, Micropower, Ultra-Low-Voltage, Rail-to-Rail I/O

Comparators

B. Process: B12

C. Fabrication Location: Oregon, California or Texas

D. Assembly Location: NSEB, ATP, Unisem

E. Date of Initial Production: April 26, 1997

#### III. Packaging Information

A. Package Type: 16-pin QSOP
B. Lead Frame: Copper

C. Lead Finish:

D. Die Attach:

Conductive Epoxy

E. Bondwire:

Gold (1 mil dia.)

F. Mold Material:

G. Assembly Diagram:

H. Flammability Rating:

BSSn/15Pb plate D.

Conductive Epoxy

Epoxy

Epoxy with silica filler

#05-1501-0120

Class UL94-V0

I. Classification of Moisture Sensitivity per

JEDEC standard J-STD-020-C

Level 1

J. Single Layer Theta Ja: 120°C/W
K. Single Layer Theta Jc: 37°C/W
L. Multi Layer Theta Ja: 103.7°C/W
M. Multi Layer Theta Jc: 37°C/W

## IV. Die Information

A. Dimensions: 53 X 89 mils

B. Passivation: Si<sub>3</sub>N<sub>4</sub>/SiO<sub>2</sub> (Silicon nitride/ Silicon dioxide

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: 1.2 microns (as drawn)F. Minimum Metal Spacing: 1.2 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.
 H. Isolation Dielectric: SiO<sub>2</sub>
 I. Die Separation Method: Wafer Saw



### V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm</li>D. Sampling Plan: Mil-Std-105D

### VI. Reliability Evaluation

#### A. Accelerated Life Test

The results of the 135°C biased (static) life test are pending. Using these results, the Failure Rate (3) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 320 \times 2} \text{ (Chi square value for MTTF upper limit)}$$

$$(\text{where } 4340 \times 320 \times 2)$$

$$(\text{where } 4340 = \text{Temperature Acceleration factor assuming an activation energy of } 0.8eV)$$

$$\lambda = 3.4 \times 10^{-9}$$

 $\lambda$  = 3.4 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the B12 HV Process results in a FIT Rate of 2.7 @ 25C and 17.3 @ 55C (0.8 eV, 60% UCL)

#### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

### C. E.S.D. and Latch-Up Testing

The CM33 die type has been found to have all pins able to withstand a HBM transient pulse of 2500 V per JEDEC JESD22-A114-D. Latch-Up testing has shown that this device withstands a current of 250 mA.



# **Table 1**Reliability Evaluation Test Results

# MAX969EEE

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test (	(Note 1)				
·	Ta = 135°C	DC Parameters	320	0	
	Biased	& functionality			
	Time = 192 hrs.				
Moisture Testing	(Note 2)				
85/85	Ta = 85°C	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 1000hrs.				
Mechanical Stres	ss (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
	Method 1010	•			

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data