

RELIABILITY REPORT
FOR
MAX4489AUA+
PLASTIC ENCAPSULATED DEVICES

January 26, 2011

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.
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Approved by
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Conclusion

The MAX4489AUA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX4475-MAX4478/MAX4488/MAX4489 wideband, low-noise, low-distortion operational amplifiers offer rail-to-rail outputs and single-supply operation down to 2.7V. They draw 2.2mA of quiescent supply current per amplifier while featuring ultra-low distortion (0.0002% THD+N), as well as low input voltage-noise density (4.5nV/) and low input current-noise density (0.5fA/). These features make the devices an ideal choice for applications that require low distortion and/or low noise. For power conservation, the MAX4475/MAX4488 offer a low-power shutdown mode that reduces supply current to 0.01 μ A and places the amplifiers' outputs into a high-impedance state. These amplifiers have outputs which swing rail-to-rail and their input common-mode voltage range includes ground. The MAX4475-MAX4478 are unity-gain stable with a gain-bandwidth product of 10MHz. The MAX4488/4489 are internally compensated for gains of +5V/V or greater with a gain-bandwidth product of 42MHz. The single MAX4475/MAX4476/MAX4488 are available in space-saving, 6-pin SOT23 and TDFN packages.

II. Manufacturing Information

A. Description/Function:	SOT23, Low-Noise, Low-Distortion, Wide-Band, Rail-to-Rail Op Amps
B. Process:	B12
C. Number of Device Transistors:	
D. Fabrication Location:	Oregon
E. Assembly Location:	Malaysia, Philippines, Thailand
F. Date of Initial Production:	July 28, 2001

III. Packaging Information

A. Package Type:	8-pin uMAX
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive
E. Bondwire:	Au (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-2501-0158
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	221°C/W
K. Single Layer Theta Jc:	41.9°C/W
L. Multi Layer Theta Ja:	206.3°C/W
M. Multi Layer Theta Jc:	41.9°C/W

IV. Die Information

A. Dimensions:	62 X 87 mils
B. Passivation:	Si ₃ N ₄ /SiO ₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect:	Al/0.5%Cu with Ti/TiN Barrier
D. Backside Metallization:	None
E. Minimum Metal Width:	1.2 microns (as drawn)
F. Minimum Metal Spacing:	1.2 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw

V. Quality Assurance Information

- A. Quality Assurance Contacts: Don Lipps (Manager, Reliability Engineering)
Bryan Preeshl (Vice President of QA)
- B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.
0.1% For all Visual Defects.
- C. Observed Outgoing Defect Rate: < 50 ppm
- D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{1000 \times 4340 \times 239 \times 2} \quad (\text{Chi square value for MTTF upper limit})$$

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 0.88 \times 10^{-9}$$

$\lambda = 0.88$ F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at <http://www.maxim-ic.com/qa/reliability/monitor>. Cumulative monitor data for the B12 Process results in a FIT Rate of 0.06 @ 25C and 1.06 @ 55C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing (ESD lot N291CA004A D/C 0308, Latch-up lot S291DA004A D/C 0415)

The OX49-1 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2500V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250mA.

Table 1
Reliability Evaluation Test Results

MAX4489AUA+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS
Static Life Test (Note 1)	Ta = 135°C	DC Parameters	79	0	N290CA015B, D/C 0640
	Biased	& functionality	80	0	N290CA035B, D/C 0710
	Time = 1000 hrs.		80	0	N290CA047B, D/C 0718

Note 1: Life Test Data may represent plastic DIP qualification lots.