

RELIABILITY REPORT  
FOR  
MAX4227ESA+  
PLASTIC ENCAPSULATED DEVICES

December 3, 2009

**MAXIM INTEGRATED PRODUCTS**

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## Conclusion

The MAX4227ESA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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### I. Device Description

#### A. General

The MAX4223-MAX4228 current-feedback amplifiers combine ultra-high-speed performance, low distortion, and excellent video specifications with low-power operation. The MAX4223/MAX4224/MAX4226/MAX4228 have a shutdown feature that reduces power-supply current to 350 $\mu$ A and places the outputs into a high-impedance state. These devices operate with dual supplies ranging from  $\pm 2.85$ V to  $\pm 5.5$ V and provide a typical output drive current of 80mA. The MAX4223/MAX4225/MAX4226 are optimized for a closed-loop gain of +1 (0dB) or more and have a -3dB bandwidth of 1GHz, while the MAX4224/MAX4227/MAX4228 are compensated for a closed-loop gain of +2 (6dB) or more, and have a -3dB bandwidth of 600MHz (1.2GHz gain-bandwidth product). The MAX4223-MAX4228 are ideal for professional video applications, with differential gain and phase errors of 0.01% and 0.02°, 0.1dB gain flatness of 300MHz, and a 1100V/ $\mu$ s slew rate. Total harmonic distortion (THD) of -60dBc (10MHz) and an 8ns settling time to 0.1% suit these devices for driving high-speed analog-to-digital inputs or for data-communications applications. The low-power shutdown mode on the MAX4223/MAX4224/MAX4226/MAX4228 makes them suitable for portable and battery-powered applications. Their high output impedance in shutdown mode is excellent for multiplexing applications. The single MAX4223/MAX4224 are available in space-saving 6-pin SOT23 packages. All devices are available in the extended -40°C to +85°C temperature range.

## II. Manufacturing Information

A. Description/Function:	1GHz, Low-Power, SOT23, Current Feedback Amplifiers with Shutdown
B. Process:	CB2
C. Number of Device Transistors:	
D. Fabrication Location:	Oregon
E. Assembly Location:	Malaysia, Philippines, Thailand
F. Date of Initial Production:	July 11, 1997

## III. Packaging Information

A. Package Type:	8-pin SOIC (N)
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive
E. Bondwire:	Au (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-3001-0052
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	170°C/W
K. Single Layer Theta Jc:	40°C/W
L. Multi Layer Theta Ja:	136°C/W
M. Multi Layer Theta Jc:	38°C/W

## IV. Die Information

A. Dimensions:	50 X 59 mils
B. Passivation:	Si <sub>3</sub> N <sub>4</sub> (Silicon nitride)
C. Interconnect:	Au
D. Backside Metallization:	None
E. Minimum Metal Width:	2 microns (as drawn)
F. Minimum Metal Spacing:	2 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO <sub>2</sub>
I. Die Separation Method:	Wafer Saw

## V. Quality Assurance Information

- A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)  
Bryan Preeshl (Managing Director of QA)
- B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.  
0.1% For all Visual Defects.
- C. Observed Outgoing Defect Rate: < 50 ppm
- D. Sampling Plan: Mil-Std-105D

## VI. Reliability Evaluation

### A. Accelerated Life Test

The results of the 150°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( $\lambda$ ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 80 \times 2} \quad (\text{Chi square value for MTTF upper limit})$$

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 13.4 \times 10^{-9}$$

$\lambda = 13.4$  F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at <http://www.maxim-ic.com/qa/reliability/monitor>. Cumulative monitor data for the CB2 Process results in a FIT Rate of 0.14 @ 25C and 2.48 @ 55C (0.8 eV, 60% UCL)

### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

### C. E.S.D. and Latch-Up Testing

The OP30-1 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2000 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250 mA.

**Table 1**  
Reliability Evaluation Test Results

**MAX4227ESA+**

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES
<b>Static Life Test</b> (Note 1)				
	Ta = 150°C Biased Time = 192 hrs.	DC Parameters & functionality	80	0
<b>Moisture Testing</b> (Note 2)				
HAST	Ta = 130°C RH = 85% Biased Time = 96hrs.	DC Parameters & functionality	77	0
<b>Mechanical Stress</b> (Note 2)				
Temperature Cycle	-65°C/150°C 1000 Cycles Method 1010	DC Parameters & functionality	77	0

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data