

RELIABILITY REPORT
FOR
MAX14676AEWO+
WAFER LEVEL PRODUCTS

September 16, 2015

MAXIM INTEGRATED

160 RIO ROBLES
SAN JOSE, CA 95134

Approved by
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Quality Assurance
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Conclusion

The MAX14676AEWO+ successfully meets the quality and reliability standards required of all Maxim Integrated products. In addition, Maxim Integrated's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim Integrated's quality and reliability standards.

Table of Contents

I.Device Description	IV.Die Information
II.Manufacturing Information	V.Quality Assurance Information
III.Packaging Information	VI.Reliability Evaluation
.....Attachments	

I. Device Description

A. General

The MAX14676/MAX14676A are battery-charge-management solutions ideal for low-power wearable applications. These devices include a linear battery-charger with a Smart Power Selector, ModelGauge fuel gauge, and several power-optimized peripherals. They feature an ultra-low-power buck regulator with a quiescent current of 900nA (typical) and 74% efficiency with 101mA output. The battery charger features Smart Power Selector operation, allowing operation with dead battery. It limits input current based on a register setting. If the charger power source is unable to supply the entire system load, the smart power control circuit will supplement the system load with current from the battery. The MAX14676/MAX14676A embed a Maxim proprietary ModelGauge fuel gauge to provide an accurate estimate of the available capacity for rechargeable lithium batteries. The MAX14676/MAX14676A include a synchronous high-efficiency step-down converter. The device features a fixed-frequency PWM mode for tighter regulation, and a burst mode for increased efficiency during light-load operation. The MAX14676/MAX14676A have a boost regulator and three programmable current sinks that can be used to drive a variety of LED configurations. The boost converter is controlled independently from the current sinks, and they can be also used separately. The MAX14676/MAX14676A feature a power switch controller that allows the device to be turned on and off by a pushbutton. This controller also provides a delayed reset signal and voltage sequencing. These devices are available in a 42-bump, 0.5mm pitch, 3.497mm x 3.118mm wafer-level package (WLP).

II. Manufacturing Information

A. Description/Function:	Wearable Charge Management Solution
B. Process:	S18
C. Number of Device Transistors:	184056
D. Fabrication Location:	California
E. Assembly Location:	Texas
F. Date of Initial Production:	September 16, 2014

III. Packaging Information

A. Package Type:	42 bmp WLP
B. Lead Frame:	N/A
C. Lead Finish:	N/A
D. Die Attach:	None
E. Bondwire:	N/A
F. Mold Material:	
G. Assembly Diagram:	#05-9000-5389
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	1
J. Single Layer Theta Ja:	N/A
K. Single Layer Theta Jc:	N/A
L. Multi Layer Theta Ja:	36°C/W
M. Multi Layer Theta Jc:	N/A

IV. Die Information

A. Dimensions:	124.0157X138.9763 mils
B. Passivation:	Si ₃ N ₄ /SiO ₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect:	Al with Ti/TiN Barrier
D. Backside Metallization:	None
E. Minimum Metal Width:	0.18um
F. Minimum Metal Spacing:	0.18um
G. Bondpad Dimensions:	
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw

V. Quality Assurance Information

- A. Quality Assurance Contacts: Don Lipps (Manager, Reliability Engineering)
Bryan Preeshl (Vice President of QA)
- B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.
0.1% for all Visual Defects.
- C. Observed Outgoing Defect Rate: < 50 ppm
- D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 48 \times 2} \quad (\text{Chi square value for MTTF upper limit})$$

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 22.9 \times 10^{-9}$$

$$\lambda = 22.9 \text{ F.I.T. (60\% confidence level @ 25°C)}$$

The following failure rate represents data collected from Maxim Integrated's reliability monitor program. Maxim Integrated performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at <http://www.maximintegrated.com/qa/reliability/monitor>. Cumulative monitor data for the S18 Process results in a FIT Rate of 0.05 @ 25C and 0.93 @ 55C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing (lot TAOW5Q002E, D/C 1345)

The AL69-0 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1000V per JEDEC JESD22-A114. Latch-Up testing has shown that this device withstands a current of +/-100mA and overvoltage per JEDEC JESD78.

Table 1
Reliability Evaluation Test Results

MAX14676AEWO+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS
Static Life Test (Note 1)	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	48	0	TAOW5Q002E, D/C 1345

Note 1: Life Test Data may represent plastic DIP qualification lots.