

NOTES:

1. Assembly Options:


1.a) w/o the C0/UART short (NoStuff: R26, R27)

1.b) w/ DP1 for pin 11 (Stuff: R13)


1.c) w/o OSC_32K_XOUT for pin 11 (NoStuff: R15)

1.d) w/o TCXO (NoStuff: U4, Y1-4)


2. Associated Documents



BOM
700-0201 Rev3



ASY DWG
705-0183 Rev1



PCB FAB
600-0183 Rev1

Revision History:

Rev	Description	ECO	Author
01	Initial release Based on 700-0183 rev2 using LTC5800IWR-WHMA	1180	CN
02	Update U1 p/n (documentation only, not a functional change)	1214	CN
03	Change 32kHz XTAL Change 20MHz XTAL	1394	RMP CN



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CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND IS SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN:

CHECKED:

APPROVED:

ENGINEER:

DESIGNER:

dust networks™

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TITLE:

LTP5900IPC-WHMA

PCA SCHEMATIC, ETERNA 22-PIN WH MOTE, BEAR

SIZE **A**

DWG NO. **710-0201**

REV **03**

DATE: **Wednesday, July 29, 2015**

SHEET **1** OF **2**



Y1, Y2, Y3, Y4 and U4 are alternate options. Footprint may overlap; valid installation options are: Y1 only, Y2 & U4, Y3 & U4 or Y4 & U4. R23 and R24 may be used instead of U4 (bypass).

DUST NETWORKS



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E. ENGINEER.

DATE:	Tuesday, July 28, 2015	SHEET 2 OF 2
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