Synchronous Boost Converters Provide High Voltage without the Heat

by Greg Dittmer

Introduction
The LTC3813 and LTC3814-5 reduce the size of high voltage, high power boost converters by incorporating heat-saving features that eliminate the need for large components and heat sinks. In particular, two features significantly reduce heat losses over other high power boost solutions:

- Synchronous control eliminates the high power loss in the diode at high output currents
- Strong internal gate drivers reduce switching losses at high output voltages.

The LTC3813 can regulate output voltages up to 100V, while the LTC3814-5 is suitable for applications up to 60V. They both use a constant off-time peak current mode control architecture. Current mode control provides tight cycle-by-cycle monitoring of inductor current and constant off-time allows high conversion ratios such as 7V input to 100V output at 250kHz.

Advantage of Synchronous Control in High Power Boost Converters
As load current increases, synchronous boost converters have a significant advantage over non-synchronous boost converters due to the low power dissipation of the synchronous MOSFET compared to that of the boost diode in a non-synchronous converter. For example, an output load of 5A dissipates 5A • 0.5V = 2.5W in the diode in a non-synchronous converter. This high power dissipation requires a large package (e.g. D²PAK) and a heat sink, which adds complexity, cost and area to the power supply. In contrast, a synchronous converter using a typical 10mΩ MOSFET would dissipate only (5A)² • 0.01Ω = 0.25W. Thus the...
synchronous MOSFET requires only a small SO8-size package and no heat sink to carry the same current.

Without heat sinking, the maximum load current of a non-synchronous boost converter is limited by the power dissipation of the boost diode. Assuming a thermal resistance of $50^\circ C/W$ on the PC board where the boost diode is mounted, the DC forward current derating curves of a typical 5A Schottky diode show that at a $50^\circ C$ ambient temperature, the maximum current the diode can carry is about 3A.

**Feature-Rich Controllers**

Besides synchronous conversion, the LTC3813 and LTC3814-5 provide many additional features for a high performance boost converter. No $R_{\text{SENSE}}$ current sensing utilizes the voltage drop across the bottom MOSFET to eliminate the need for a sense resistor—saving cost and simplifying board layout. For applications that require more accurate current limit, the LTC3813 can accommodate a sense resistor to achieve higher accuracy.

The off-time is programmable with an external resistor and, with an additional resistive divider from $V_{\text{IN}}$ to the $V_{\text{OFF}}$, can be compensated for changes in input voltage to keep the frequency relatively constant over a wide supply range. Off-times as low as 100ns can be chosen to provide high $V_{\text{OUT}}/V_{\text{IN}}$ step-up ratios. At low duty cycles, the step up ratio is limited by the 350ns minimum on time of the bottom MOSFET.

A high bandwidth error amplifier provides fast line and load transient response and a precise 0.8V, ±0.5% reference (0°C to 85°C) provides a very accurate output voltage. An internal undervoltage lockout comparator monitors the driver supply voltage and shuts down the drivers if the supply voltage is below a threshold that is safe for the power MOSFETs (6.2V for the LTC3813 and 4.2V for the LTC3814-5). The LTC3813 also provides a pin for undervoltage lockout on the input supply that is programmable with a resistive divider. Finally, the LTC3813 also has a phase-locked loop for external clock synchronization in noise sensitive applications.

A power good pin, accurate cycle-by-cycle inductor current limit, and overvoltage protection are additional fault protection features. Programmable soft-start ensures that the output capacitor ramps up in a controlled manner at start-up with no overshoot.

The LTC3814-5 provides a simplified feature set in a smaller more convenient package (thermally enhanced 16-lead TSSOP). The LTC3814-5 has a maximum output voltage of 60V and offers all the features of the LTC3813 except for input supply UVLO and external clock synchronization.

**Strong Gate Drivers for High Efficiency**

Because switching losses are proportional to the square of the output voltage, these losses can dominate in high output voltage applications with inadequate gate drive. The LTC3813 and 3814-5 have strong 1Ω gate drivers that minimize transition losses, even when multiple MOSFETs are used for high current applications. Dual N-channel synchronous drives combined with strong drivers result in very high power conversion efficiencies (see Figures 3 and 7). The LTC3813 uses a high voltage floating driver to drive the synchronous MOSFET at output voltages up to 100V (60V for the LTC3814-5).

The LTC3813 is optimized for driving 100V MOSFETs, which are typically rated at a $V_{\text{GS}}$ of 6V or higher. As a result, the LTC3813 has an internal under-voltage lockout that keeps the drivers off until the driver supply is greater than 6.2V, with 500mV of hysteresis. The LTC3814-5 is optimized for driving logic level MOSFETs, which are rated at a $V_{\text{GS}}$ of 4.5V and this version has an internal undervoltage lockout threshold of 4.2V with 500mV of hysteresis.

**IC/Driver Supply Regulator**

The LTC3813’s internal control circuitry and top and bottom MOSFET drivers operate from a supply voltage in the range of 6.2V to 14V (4.2V to 14V for the LTC3814-5). If the input supply voltage or another available supply falls within this voltage range it can be used to supply IC/driver power (see Figure 1a). If a supply in this range is not available, a single low current external MOSFET and resistor can be added to easily generate a regulated 10V (5.5V for the LTC3814-5) IC/driver supply using the internal linear regulator circuitry (Figure 1b). Using an external pass element has the advantage of reducing power dissipation on the IC and it also allows the transistor to be chosen with the
appropiate BV_{DSS} and power rating for the application—a small SOT23 package will often suffice.

Figure 1c shows a solution for applications that require the boost converter to continue operating when the input voltage has fallen below the undervoltage threshold of the IC. The cost is slightly lower efficiency. In this circuit, the regulator is connected to the output instead of the input. Diode D1 supplies power to the IC until the output voltage is high enough to generate the chip supply from the output. When the output is in regulation, the undervoltage threshold of the IC. The SOT23. A resistive divider is connected from \( \text{V}_{\text{IN}} \) to \( \text{V}_{\text{OUT}} \) to disable the INTVCC regulator.

A 403k\( \Omega \) resistor is connected from \( \text{V}_{\text{OUT}} \) to the \text{IOFF} \) pin to set the frequency to 250kHz. Connecting the resistor to the output (as opposed to a constant supply voltage) has the advantage of keeping the frequency constant during output start-up. Connecting the resistive divider from \( \text{V}_{\text{IN}} \) to the \text{V}_{\text{OFF}} \) pin sets input supply range for constant frequency operation from 5V to 12V. The \text{V}_{\text{RING}} \) pin is connected to \( \text{V}_{\text{IN}} \) to set the max sense voltage to 200mV. This sets the nominal peak inductor current limit to 200mV/0.012 = 20A using the Si7848DP MOSFET and, after accounting for parameter variations and inductor ripple amplitude, provides a maximum load of 2A at \( \text{V}_{\text{IN}} \) = 5V and 4A at \( \text{V}_{\text{IN}} \) = 12V. Figures 4 and 5 illustrate the outstanding load transient and overcurrent performance of the power supply.

### 12V–40V to 50V, 250W DC/DC Converter

The circuit shown in Figure 6 generates a 50V output voltage from a 12V–40V input using the LTC3813. Since the maximum input voltage is greater than 14V, the LTC3813 produces a regulated 10V from the input supply using a ZXMN10A07F MOSFET in a SOT23. A resistive divider is connected from \( \text{V}_{\text{IN}} \) to the UVIN pin to set the undervoltage lockout threshold to 10V on the input supply. This ensures that the boost converter doesn’t hang at start-up when the powered by a current limited source supply when continued on page 37.
synchronization, user adjustable switching frequency and a SHARE pin for paralleling modules. The LTM8022 also employs Burst Mode operation, drawing only 50µA quiescent current at no load while maintaining only 30mV of output voltage ripple. Like the LTM8020, the quiescent current when shut down is less than 1µA. The schematic is very simple, with examples of 3.3V and 8V output designs shown in Figures 3 and 4, respectively.

...Or, Even More Power...

The LTM8023 is the big brother of the LTM8022, capable of producing up to 2A of output current. The LTM8023 has the same input, output voltage range, and control features as the LTM8022. It also features Burst Mode operation and low quiescent current. The LTM8022 and LTM8023 share the same footprint and pin pattern, so even if you start a design with the LTM8022 but later find that you need more current, you can simply drop in the LTM8023. In most cases, the design will use identical passive components as the LTM8022, as seen in the 3.3V example in Figure 5.

**Conclusion**

The LTM8020, LTM8022 and LTM8023 µModule regulators make power supply development fast and easy. Their broad input and output voltage ranges, load capabilities and small size (see Table 1) make them readily fit into a wide variety of applications.

<table>
<thead>
<tr>
<th>Part Number</th>
<th>V&lt;sub&gt;IN&lt;/sub&gt; Range</th>
<th>Max Load</th>
<th>V&lt;sub&gt;OUT&lt;/sub&gt; Range</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTM8020EV</td>
<td>4V to 36V</td>
<td>200mA</td>
<td>1.25V to 5V</td>
<td>6.25 × 6.25 × 2.32mm</td>
</tr>
<tr>
<td>LTM8022EV</td>
<td>3.6V to 36V</td>
<td>1A</td>
<td>0.8V to 10V</td>
<td>11.25 × 9 × 2.82mm</td>
</tr>
<tr>
<td>LMT8023EV</td>
<td>3.6V to 36V</td>
<td>2A</td>
<td>0.8V to 10V</td>
<td>11.25 × 9 × 2.82mm</td>
</tr>
</tbody>
</table>

LTC3562, continued from page 35

Mode operation to maximize power efficiency at light loads. Under no-load conditions, the regulators can also be programmed into LDO mode, which provides the lowest quiescent current (all four regulators in LDO mode only draw a combined 80µA for the entire chip).

To save even more power, the LTC3562 can be programmed to reduce the regulators’ output voltages in Burst Mode operation or forced Burst Mode operation during light load conditions. Since power dissipation is directly proportional to the supply voltage multiplied by the load current, dropping the supply voltage effectively reduces the circuit’s total power dissipation. If the output load is resistive in nature, reducing the supply voltages has an even greater effect, since power dissipation in the load is proportional to the supply voltage squared.

**Conclusion**

The LTC3562 is a highly flexible I<sup>2</sup>C quad step-down converter composed of two 600mA and two 400mA buck regulators in a 3mm × 3mm QFN package. The output voltages of the regulators can be switched on the fly using servo control or I<sup>2</sup>C control. Each regulator can also be switched on the fly into four possible high efficiency or low-noise operating modes. This is a perfect device for high performance applications that require constant control of the power supply. It can also be used to simplify design, build and test cycles, since output voltages can easily be changed without changing components.

LTC3813 and LTC3814-5, continued from page 21

the output is drawing full load. Its efficiency is shown in Figure 7.

**Conclusion**

The LTC3813 and LTC3814-5’s synchronous architecture and high voltage capability make them ideally suited for high voltage high power boost converters. They decrease complexity by eliminating the requirement for a large diode package and heat sink to dissipate its high power loss. Programmable frequency and current limit, wide output voltage range, and ability to drive logic-level or higher threshold MOSFETs provide maximum flexibility in using them for a variety of boost applications. Other features such as such as strong gate drivers to minimize transition losses, an accurate voltage reference, accurate cycle-by-cycle current limit, and an on-chip bias supply controller make the LTC3813 and LTC3814-5 the obvious choice for high performance, high power boost converters.