

Figure 2. Two LTC4370s can be cascaded to enable current sharing of three supplies.

$$\Delta I = |I_1 - I_2|$$

Using the worst-case errors, above, the error is:

$$\Delta I \leq \left(\frac{2\text{mV}}{R_{\text{SENSE}}} + 0.01 \cdot I_{\text{LOAD}} \right) [A]$$

For the circuit of Figure 2, where ideal load sharing means the load is distributed into $\frac{1}{3}I_{\text{LOAD}}$ and $\frac{2}{3}I_{\text{LOAD}}$, it is easier to estimate the worst-case imbalance via an expression of the maximum and minimum current of each supply:

$$I_{\text{MAX}} = \left(0.672 \cdot I_{\text{LOAD}} + \frac{2\text{mV}}{3.01 \cdot R_{\text{SENSE}}} \right) [A]$$

$$I_{\text{MIN}} = \left(0.328 \cdot I_{\text{LOAD}} + \frac{2\text{mV}}{3.01 \cdot R_{\text{SENSE}}} \right) [A]$$

CONCLUSION

By cascading the shared output of one LTC4370 with another LTC4370, three or more supplies can be efficiently controlled to provide equal current to the load. With errors on the order of the sense resistor tolerance, the voltage drop is minimal. ■

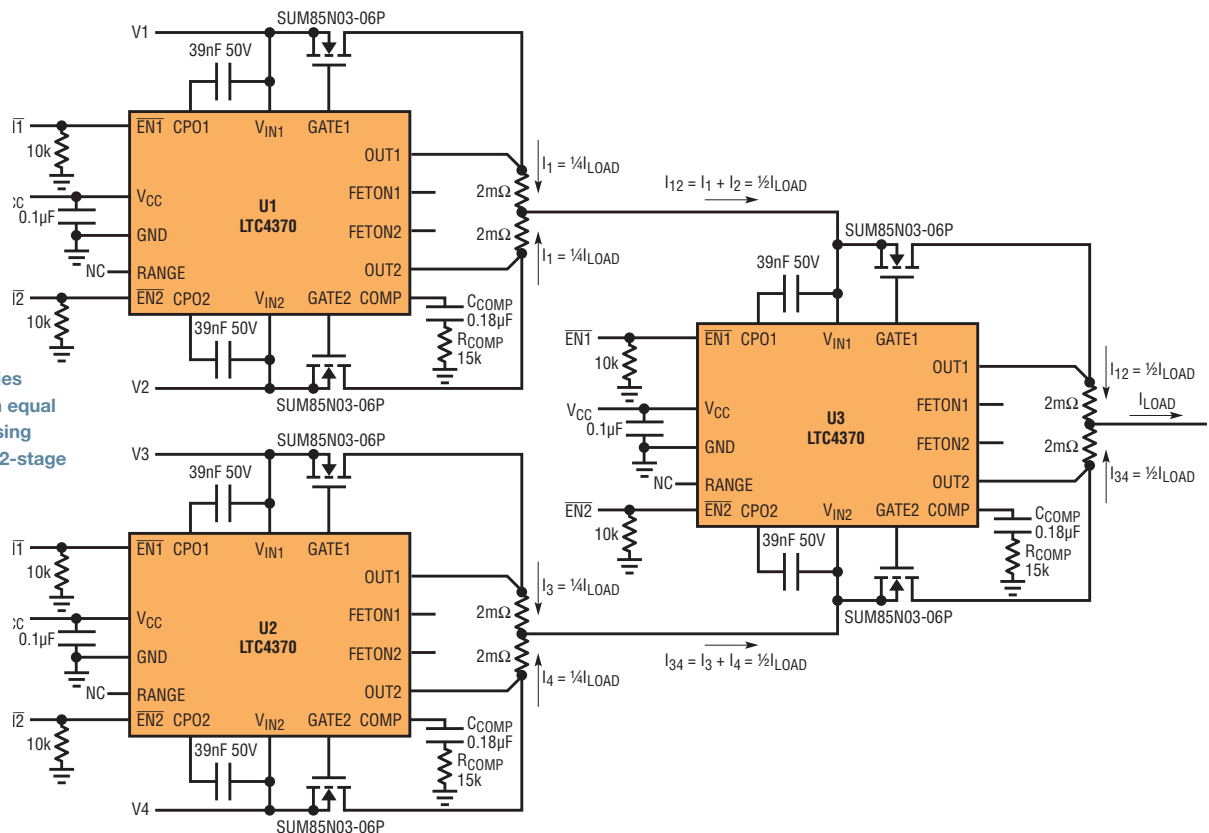


Figure 3. Four supplies can each support an equal share of a load by using three LTC4370s in a 2-stage cascade.