Minimize Standby Current in Automotive DDR Supplies

David Gilbert

When you turn on a laptop or a smart phone, you expect to wait for it to boot up, but you are less patient when you turn on your car. With a car, consumers expect immediate access to computer electronics, including navigation and infotainment systems, and automobile manufacturers strive to meet this desire with design strategies that shorten start-up time. One such strategy is to keep dynamic memory (RAM) active at all times, even during the ignition-off state.

The DDR3 memory used in automobiles operates on a 1.5V rail with peak load currents over 2A—preferably produced by a high efficiency DC/DC converter to minimize heat dissipation. In these applications, light load efficiency is just as important to preserve battery life when the automobile is not running. DDR memory can consume 1mA–10mA from the 1.5V rail in standby, but drawing 10mA from the battery is unacceptable when the car is parked for long periods.

This constraint rules out the use of a linear regulator, where input and output current are equal. On the other hand, a switching step-down (buck) regulator draws less input current than load current in proportion to the step-down ratio:

\[ I_{IN} = \frac{1}{\eta} \cdot I_{OUT} \cdot \frac{V_{OUT}}{V_{IN}} \]

where \( \eta \) is the efficiency factor (0 to 1).

Figure 1 shows that the LT®8610AB synchronous step-down regulator achieves ~83% efficiency at a 1mA load. For a battery voltage of 12V and load current of 1mA at 1.5V, calculated input current is only 1.5mA.

(continued on page 4)
The LT8610A and LT8610AB have a low component count, low minimum input voltage, low quiescent current and high efficiency across a wide load range. These features make them the preferred solution for providing standby power to DDR memory in automotive applications.

**DIRECT DC/DC CONVERSION FROM CAR BATTERY TO 1.5V DDR MEMORY**

LT8610A and LT8610AB are monolithic, synchronous step-down regulators designed specifically for automotive systems. They deliver 3.5A while consuming only 2.5μA quiescent current. Building a circuit around them is easy. No additional semiconductors are required, they work with inexpensive ceramic capacitors, and the MSOP package has leads that are easy to solder and inspect. With a typical minimum on-time of 30ns (45ns guaranteed maximum), one can design compact, high switching-frequency buck regulators with large step-down ratios. Figure 2 shows an application that delivers 3.5A at 1.5V. The operating frequency is 475kHz to optimize efficiency and remain below the AM radio band.

Both parts feature excellent fault tolerance to automotive environments. A maximum input of 42V handles load dumps. A robust switch design and high speed current comparator protect the device during output shorts. The minimum input is worst-case 3.4V, the maximum duty cycle is above 99% and the dropout voltage is typically 200mV at 1A, all of which keep the output in regulation through cold-crank. The typical minimum input voltage is plotted in Figure 3.

**SAVE THE BATTERY WITH LOW RIPPLE Burst Mode OPERATION AND MINIMAL QUIESCENT CURRENT**

The LT8610A and LT8610AB are designed to minimize output voltage ripple over the entire load range. At light loads, they maintain efficiency by reducing their operating frequency and entering Burst Mode® operation. Fast transient response is maintained even at very low loads. This feature combined with the very low quiescent current of 2.5μA means that, even at a few μA of load, the LT8610A and LT8610AB are more efficient than a linear regulator with zero quiescent current. For systems where low frequency operation must be avoided, Burst Mode operation can be turned off by applying either a logic high signal or clock signal to the SYNC pin.

The difference between the LT8610A and LT8610AB is that the LT8610AB features higher efficiency at light loads. This is achieved by using an increased Burst Mode current limit, allowing more energy to be delivered during each switch cycle and lowering the switching frequency for a given load. Because a fixed amount of energy is required to...
The LT8610A and LT8610AB are designed to minimize output voltage ripple over the entire load range. At light loads, they maintain efficiency by reducing their operating frequency and entering Burst Mode operation. Fast transient response is maintained even at very low loads.

Figure 4. An increased Burst Mode current limit on the LT8610AB results in substantial efficiency gains at light load compared to the LT8610A.

Figure 5. Output voltage ripple versus number of 1210 size 47μF output capacitors for two inductor values, at 10mA load. (a) Ripple for the 475kHz application in Figure 2. (b) Ripple for the 2MHz application in Figure 6.

switch the MOSFET on and off, a lower switching frequency reduces gate-charge losses and increases efficiency.

Figure 4 shows the efficiency difference between the LT8610A and LT8610AB. For loads between 1mA and 100mA, the LT8610AB improves efficiency by more than 10% compared to the LT8610A. The trade-off to the increased Burst Mode current limit is that more energy is delivered in each switch cycle, so more output capacitance is required to keep output voltage ripple low. Figure 5 compares the output ripple for the LT8610A and LT8610AB as a function of the output capacitance for two inductor values, at 10mA load.

In addition to the current limit, the inductor choice affects the efficiency and switching frequency in Burst Mode operation. This is because for a fixed current limit, a larger inductor value can store more energy than a smaller one. If high efficiency at light loads is paramount, then the inductor value can be increased beyond the starting value recommended in the data sheet.

GO FASTER FOR A SMALLER SOLUTION

For most automotive systems, 9V to 16V is the typical input voltage, so application circuits are usually optimized for this range. The 475kHz application in Figure 2 operates at the designed frequency over the entire input range of 3.5V to 42V. However, if we restrict the normal operating voltage to 16V (42V transient), the operating frequency can be increased and the value and size of the inductor reduced. With a worst-case minimum on-time of 45ns, the LT8610A and LT8610AB can be programmed to 2MHz as shown in Figure 6.
An important feature is that this internal regulator can draw current from either the $V_{IN}$ pin or the BIAS pin. If a voltage of 3.1V or higher is tied to the BIAS pin, gate drive current is drawn from BIAS. If the BIAS voltage is lower than $V_{IN}$, the internal linear regulator will run more efficiently using the lower voltage supply, thus increasing overall efficiency.

Note that when the input voltage goes above 16V, the output remains in regulation although the switching frequency decreases to maintain safe operation. The 2MHz solution is identical to the circuit in Figure 2, except for the $R_T$ resistor changed to 18.2kΩ and the inductor value and size reduced in order to save space. Figure 7 shows efficiency versus load for two inductor choices.

### BIAS PIN OPTIMIZES EFFICIENCY

The LT8610A and LT8610AB use two internal nMOSFETs specifically optimized for automotive applications. In particular, the gate drive circuitry requires less than 3V to fully enhance the FETs. To generate the gate drive supply, the LT8610A/AB includes an internal linear voltage regulator, the output of which is the INTV$_{CC}$ pin (do not load INTV$_{CC}$ with external circuitry).

An important feature is that this internal regulator can draw current from either the $V_{IN}$ pin or the BIAS pin. If the BIAS pin is left open, then gate drive current is drawn from $V_{IN}$. However, if a voltage of 3.1V or higher is tied to the BIAS pin, gate drive current is drawn from BIAS. If the BIAS voltage is lower than $V_{IN}$, the internal linear regulator will run more efficiently using the lower voltage supply, thus increasing overall efficiency.

The efficiency data in Figures 1, 4 and 7 was recorded with the BIAS pin open. After all, if the 1.5V output is the only rail alive, then there is likely no good place to tie the BIAS pin. However, if there is a 3.3V or 5V supply, tie it to the BIAS pin, even if the supply is not available in standby or ignition-off conditions. Figure 8 shows the efficiency with and without a 3.3V supply connected to BIAS. In calculating the total efficiency, we have included the power drawn from the 3.3V rail and assumed that it was generated with 85% efficiency.

Note that the benefit to externally powering BIAS is greater at higher operating frequencies because the gate drive current is higher. The LT8610A also benefits...
An important consideration for automotive applications is the behavior of the power supply during cold crank and idle-stop transients, when the voltage from the 12V battery may drop below 4V. The LT8610AB operates up to 99% duty cycle, providing output regulation at the lowest possible input voltage.

more from external bias compared to the LT8610AB—the AB’s increased Burst Mode current limit results in a lower operating frequency for a given load.

### NOT JUST FOR MEMORY

The LT8610AB is an excellent regulator for other automotive supplies, including 3.3V and 5V supplies, with efficiency above 90%, as shown in Figure 9.

An important consideration for automotive applications is the behavior of the power supply during cold crank and idle-stop transients, when the voltage from the 12V battery may drop below 4V. The LT8610AB operates up to 99% duty cycle, providing output regulation at the lowest possible input voltage. Figure 10(a) shows the dropout voltage. This is the difference between $V_{IN}$ and $V_{OUT}$ as the input voltage decreases towards the intended output regulation voltage. The LT8610AB also has excellent start-up and dropout behavior, resulting in predictable and reliable output voltage as a function of input voltage. Figure 10(b) shows the output voltage as the input supply is ramped from zero to 12V and back to zero.

### CONCLUSION

The LT8610A and LT8610AB have a low component count, low minimum input voltage, low quiescent current and high efficiency across a wide load range. These features make them the preferred solutions for providing standby power to DDR memory in automotive applications. Table 1 summarizes the performance of the LT8610 family.

Visit [www.linear.com/LT8610](http://www.linear.com/LT8610) for data sheets, demo boards and other applications information.

### Table 1. Comparison of features for the LT8610 family of monolithic, synchronous buck converters

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>LT8610</th>
<th>LT8610A</th>
<th>LT8610AB</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max Load Current (A)</td>
<td></td>
<td>2.5</td>
<td>3.5</td>
<td>3.5</td>
</tr>
<tr>
<td>Minimum On-Time (ns) (Typ)</td>
<td></td>
<td>50</td>
<td>30</td>
<td>30</td>
</tr>
<tr>
<td>Efficiency (%)</td>
<td>$V_{IN} = 12V$, $V_{OUT} = 1.5V$, $I_{LOAD} = 10mA$, $f_{SW} = 475kHz$, $L = 3.3\mu H$</td>
<td>73.9</td>
<td>73.9</td>
<td>85.5</td>
</tr>
<tr>
<td>Output Voltage Ripple (mV$_{P-P}$)</td>
<td>$I_{LOAD} = 10mA$, $C_{OUT} = 47\mu F$, $L = 3.3\mu H$</td>
<td>8.4</td>
<td>8.4</td>
<td>52.5</td>
</tr>
</tbody>
</table>

Figure 9. Efficiency for 3.3V and 5V outputs is above 90%, reducing total power dissipation and keeping temperature under control.

Figure 10. The LT8610AB operates to 99% duty cycle, providing smooth start-up and low dropout voltage.