Introduction

Circuits that require 5V remain popular despite the fact that modern systems commonly supply a 3.3V power bus, not 5V. The tiny LTC1700 is optimized to deliver 5V from the 3.3V bus at very high efficiency, though it can also efficiently boost other voltages. The small MSOP package and 530kHz operation promote small surface mount circuits requiring minimal board space, perfect for the latest portable devices. By taking advantage of the synchronous rectifier driver, the LTC1700 provides up to 95% efficiency. To keep light load efficiency high in portable applications, the LTC1700 draws only 180µA in sleep mode. The LTC1700 features a start-up voltage as low as 0.9V, adding to its versatility.

The LTC1700 uses a constant frequency, current mode PWM control scheme. Its NoRSENSE™ feature means the current is sensed at the main MOSFET, eliminating the need for a sense resistor. This saves cost, space and improves efficiency at heavy loads. For noise-sensitive applications, Burst Mode operation can be disabled when the SYNC/MODE pin is pulled low or driven by an external clock. The LTC1700 can be synchronized to an external clock ranging from 400kHz to 750kHz.

3.3V Input, 5V/3A Output Boost Regulator

Figure 1 shows a 3.3V input to 5V output boost regulator which can supply up to 3A load current. Figure 2 shows that the efficiency is greater than 90% for a load current range of 200mA to 3A and stays above 80% all the way down to a 3mA load.

C2 is a tantalum capacitor providing bulk capacitance to compensate for possible long wire connections to the input supply. In applications where the regulator’s input is continuous on page 35.
to one \(R_1 = R_2 = 604\, \Omega\) and \(V_{\text{OUT}} = V_2 - V_1\) the input referred differential voltage noise density is \(9\, \text{nV/}\sqrt{\text{Hz}}\) and differential input signal-to-noise ratio is 80.9dB with 0.1V\text{RMS} input signal in a 4MHz noise bandwidth. The input AC common mode rejection depends on the matching of resistors \(R_1\) and \(R_3\) and the LT1567 inverter gain tolerance (common mode rejection is at least 40dB up to 1MHz with one percent resistors and two percent inverter typical gain tolerance). If the differential input is DC coupled, then \(V_{\text{REF}}\) must be set equal to input common mode voltage \(V_{\text{INCM}}\) (if \(V_{\text{REF}}\) is greater than \(V_{\text{INCM}}\) then a peak voltage on Pin 7 may exceed the output voltage swing limit). The DC voltage at the amplifier’s output \(V_{\text{OUT}}\) (Pin 1) is \(V_{\text{REF}}\).

**Conclusion**

With one LT1567 and two or three resistors, it is easy to design low noise, differential circuits for signals up to 5MHz. The LT1567 can also be used to make low noise second and third order lowpass filters and second order bandpass filters with differential outputs. See www.linear.com for a spreadsheet-based design tool for just this purpose.

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connected very close to a low impedance supply, this capacitor is not needed.

### 2-Cell Input, 3.3V/1A Output Regulator

In digital cameras and other battery-powered devices, the LTC1700 makes for a high efficiency boost regulator in a small package. Figure 3 shows a 2-alkaline cell to 3.3V output circuit. This circuit can supply 1A maximum output current. Figure 4 shows the efficiency at different battery voltages. Efficiency of this circuit peaks at 93%. If a lower \(R_{\text{DS(ON)}}\) MOSFET (such as Si6466) is used for \(M_1\), the maximum output current can be increased to 1.4A with about a 2% reduction in efficiency due to the increase in gate capacitance. MOSFETs with lower than 2.5V gate threshold voltages are recommended. The LTC1700 is also an ideal device for single cell Li-Ion battery to 5V applications.

**Conclusion**

The LTC1700 boost controller brings high efficiency and small size to low voltage applications. Its features are ideally suited to both battery-powered and line-powered applications.