

Waivers - Customer approval of stackup includes approval of:

1- SE impedance on inner and outer layers with 25 ohms will have +/-5 ohms tolerance.

1- Dielectrics thickness have been adjusted to meet impedance and overall board thickness.

Notes / Comments:

1- Please include approved stackup with final data set.

Stackup based on customer provided stackup.(no data or drawing)

			Est. Thick.	Tolerance	L Y R #	Single Ended Model						L Y R #	Differential Model											
						Org. I/W	A/W I/W	Fin. I/W	Ref. Plane	Calc. Imp.	Org. I/W		Org. space	A/W I/W	A/W space	Fin. I/W	Fin. space	Ref. Plane	Calc. Imp.					
1	S	3/8 oz + Plating	3/8			1	12		11.5	2	50	1	10	15			9.9	15.1	2	100				
					1	36		35.5	2	25		1	12	5			12	5	2	75				
		1501(49)	=>									1	7.6	10.4			7.6	10.4	2	100				
2	P	Core	=>																					
3	P	1078(64)	=>																					
4	P	Core	=>																					
5	P	2x2116(54)	=>																					
6	S	Core	=>			6	28		28	5,7	25	6	8.5	15			8.9	14.6	5,7	100				
					6	9		9.6	5,7	50														
7	P	1078(64)	=>																					
8	P	Core	=>																					
9	P	1501(49)	=>																					
					10	36		35.5	9	25		10	7.6	10.4			7.6	10.4	9	100				
					10	12						10	12	5			12	5	9	75				
10	S	3/8 oz + Plating	3/8			10	12		11.5	9	50	10	10	15			9.9	15.1	9	100				
Thickness After plating				62.05	not including solder mask						Units			Mils										
Target Thickness				62+/-10%	over All						Impedance Tolerance			(SE) +/- 10%				(DIFF) +/- 10%						