
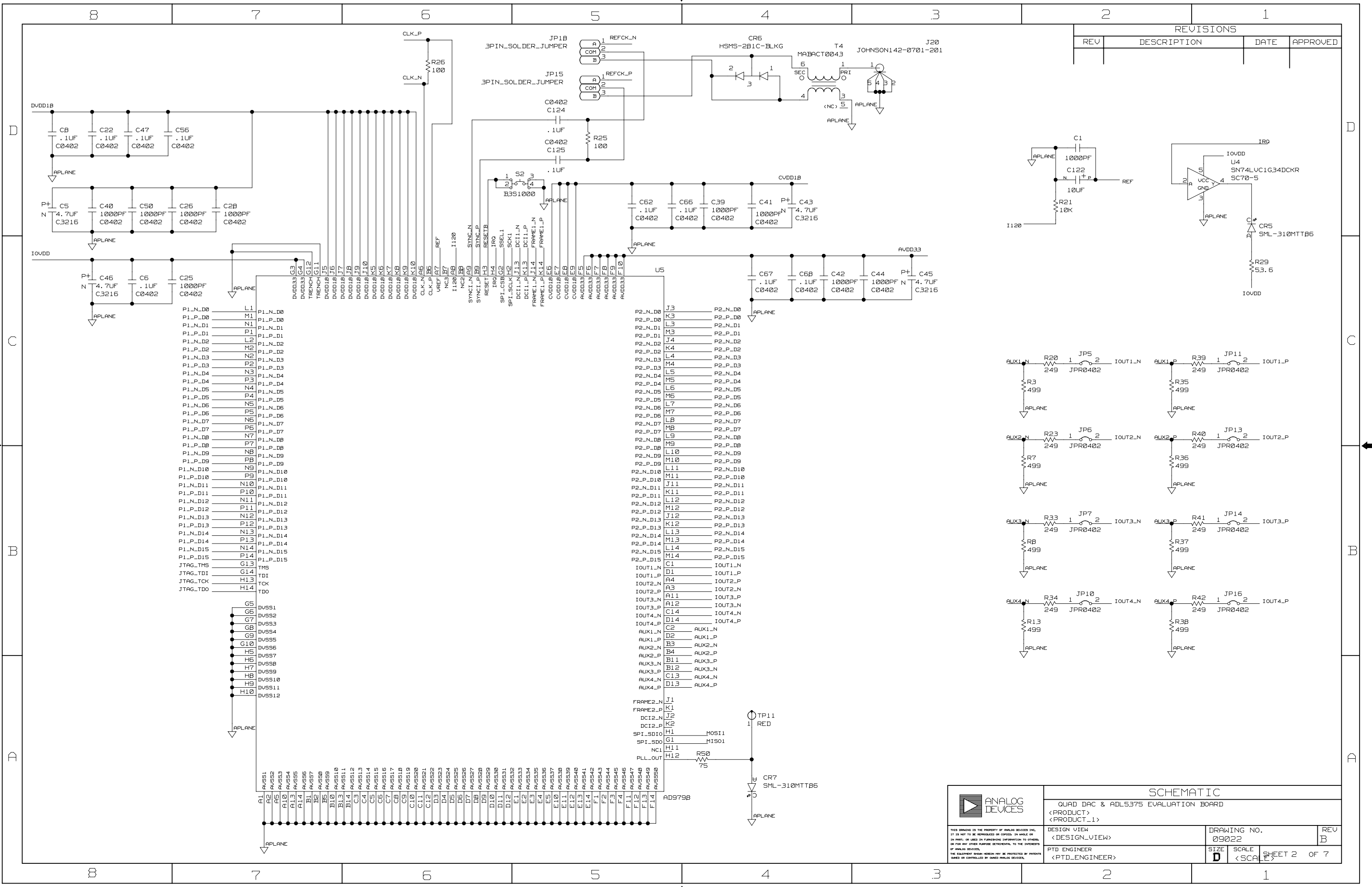


8		7		6		5		4		3		2		1		
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								REV	DESCRIPTION	DATE	APPROVED					
RELAY CONTROL CHART					JP#	ON	OFF	A				7-09	K. J.			
					1			B				3-10	K. J.			
					2											
					3											
CONTROL			CODE	DEVICE	FUNCTION	CONNECTOR	4									
							5									
							* SEE ASSEMBLY INSTRUCTIONS									
					MAY 10, 2010											
					TEMPLATE ENGINEER P. ALONGI		DATE 7-09	SCHEMATIC								
					HARDWARE SERVICES -											
					HARDWARE SYSTEMS -			QUAD DAC & ADL5375 EVALUATION BOARD <PRODUCT> <PRODUCT_1> <DRAWING_TITLE1> <DRAWING_TITLE2> <DRAWING_TITLE3> <DRAWING_TITLE4> <DRAWING_TITLES>								
					TEST ENGINEER -											
					COMPONENT ENGINEER -											
					TEST PROCESS -											
					HARDWARE RELEASE -											
					DESIGNER -		7-09	MASTER PROJECT TEMPLATE <DRAWING_TITLE5>		TESTER TEMPLATE		DRAWING NO.		REV.		
					PTD ENGINEER <PTD_ENGINEER>		7-09	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES				09022		B		
					CHECKER -			TOLERANCES DECIMALS FRACTIONS ANGLES X.XX +-0.010 +-1/32 +-2 X.XXX +-0.005				SIZE SCALE CODE ID NO. D <SCALE>		SHEET 1 OF 7		
8		7		6		5		4		3		2		1		
P.O. SPEC.		BK/BD SPEC.		SOCKET OEM		OEM PART#		HANDLER								




REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

P1_N_D0	L1	P1_N_D0
P1_P_D0	M1	P1_P_D0
P1_N_D1	N1	P1_N_D1
P1_P_D1	P1	P1_P_D1
P1_N_D2	L2	P1_N_D2
P1_P_D2	M2	P1_P_D2
P1_N_D3	N2	P1_N_D3
P1_P_D3	P2	P1_P_D3
P1_N_D4	N3	P1_N_D4
P1_P_D4	P3	P1_P_D4
P1_N_D5	N4	P1_N_D5
P1_P_D5	P4	P1_P_D5
P1_N_D6	N5	P1_N_D6
P1_P_D6	P5	P1_P_D6
P1_N_D7	N6	P1_N_D7
P1_P_D7	P6	P1_P_D7
P1_N_D8	N7	P1_N_D8
P1_P_D8	P7	P1_P_D8
P1_N_D9	N8	P1_N_D9
P1_P_D9	P8	P1_P_D9
P1_N_D10	N9	P1_N_D10
P1_P_D10	P9	P1_P_D10
P1_N_D11	N10	P1_N_D11
P1_P_D11	P10	P1_P_D11
P1_N_D12	N11	P1_N_D12
P1_P_D12	P11	P1_P_D12
P1_N_D13	N12	P1_N_D13
P1_P_D13	P12	P1_P_D13
P1_N_D14	N13	P1_N_D14
P1_P_D14	P13	P1_P_D14
P1_N_D15	N14	P1_N_D15
P1_P_D15	P14	P1_P_D15
JTAG_TMS	G13	TMS
JTAG_TDI	G14	TDI
JTAG_TCK	H13	TCK
JTAG_TDO	H14	TDO

P2_N_D0	J3	P2_N_D0
P2_P_D0	K3	P2_P_D0
P2_N_D1	L3	P2_N_D1
P2_P_D1	M3	P2_P_D1
P2_N_D2	J4	P2_N_D2
P2_P_D2	K4	P2_P_D2
P2_N_D3	L4	P2_N_D3
P2_P_D3	M4	P2_P_D3
P2_N_D4	L5	P2_N_D4
P2_P_D4	M5	P2_P_D4
P2_N_D5	L6	P2_N_D5
P2_P_D5	M6	P2_P_D5
P2_N_D6	L7	P2_N_D6
P2_P_D6	M7	P2_P_D6
P2_N_D7	L8	P2_N_D7
P2_P_D7	M8	P2_P_D7
P2_N_D8	L9	P2_N_D8
P2_P_D8	M9	P2_P_D8
P2_N_D9	L10	P2_N_D9
P2_P_D9	M10	P2_P_D9
P2_N_D10	L11	P2_N_D10
P2_P_D10	M11	P2_P_D10
P2_N_D11	J11	P2_N_D11
P2_P_D11	K11	P2_P_D11
P2_N_D12	L12	P2_N_D12
P2_P_D12	M12	P2_P_D12
P2_N_D13	J12	P2_N_D13
P2_P_D13	K12	P2_P_D13
P2_N_D14	L13	P2_N_D14
P2_P_D14	M13	P2_P_D14
P2_N_D15	L14	P2_N_D15
P2_P_D15	M14	P2_P_D15
IOUT1_N	C1	IOUT1_N
IOUT1_P	D1	IOUT1_P
IOUT2_N	A4	IOUT2_N
IOUT2_P	A3	IOUT2_P
IOUT3_N	A11	IOUT3_N
IOUT3_P	A12	IOUT3_P
IOUT4_N	C14	IOUT4_N
IOUT4_P	D14	IOUT4_P
AUX1_N	C2	AUX1_N
AUX1_P	D2	AUX1_P
AUX2_N	B3	AUX2_N
AUX2_P	B4	AUX2_P
AUX3_N	B11	AUX3_N
AUX3_P	B12	AUX3_P
AUX4_N	C13	AUX4_N
AUX4_P	D13	AUX4_P

FRAME2_N	J1
FRAME2_P	K1
DCI2_N	J2
DCI2_P	K2
SPI_SDIO	H1
SPI_SDO	G1
NC1	H11
PLL_OUT	H12



ANALOG
DEVICES

SCHEMATIC

QUAD DAC & ADL5375 EVALUATION BOARD

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<PRODUCT_1>

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
<PTD_ENGINEER>

DRAWING NO.
09022

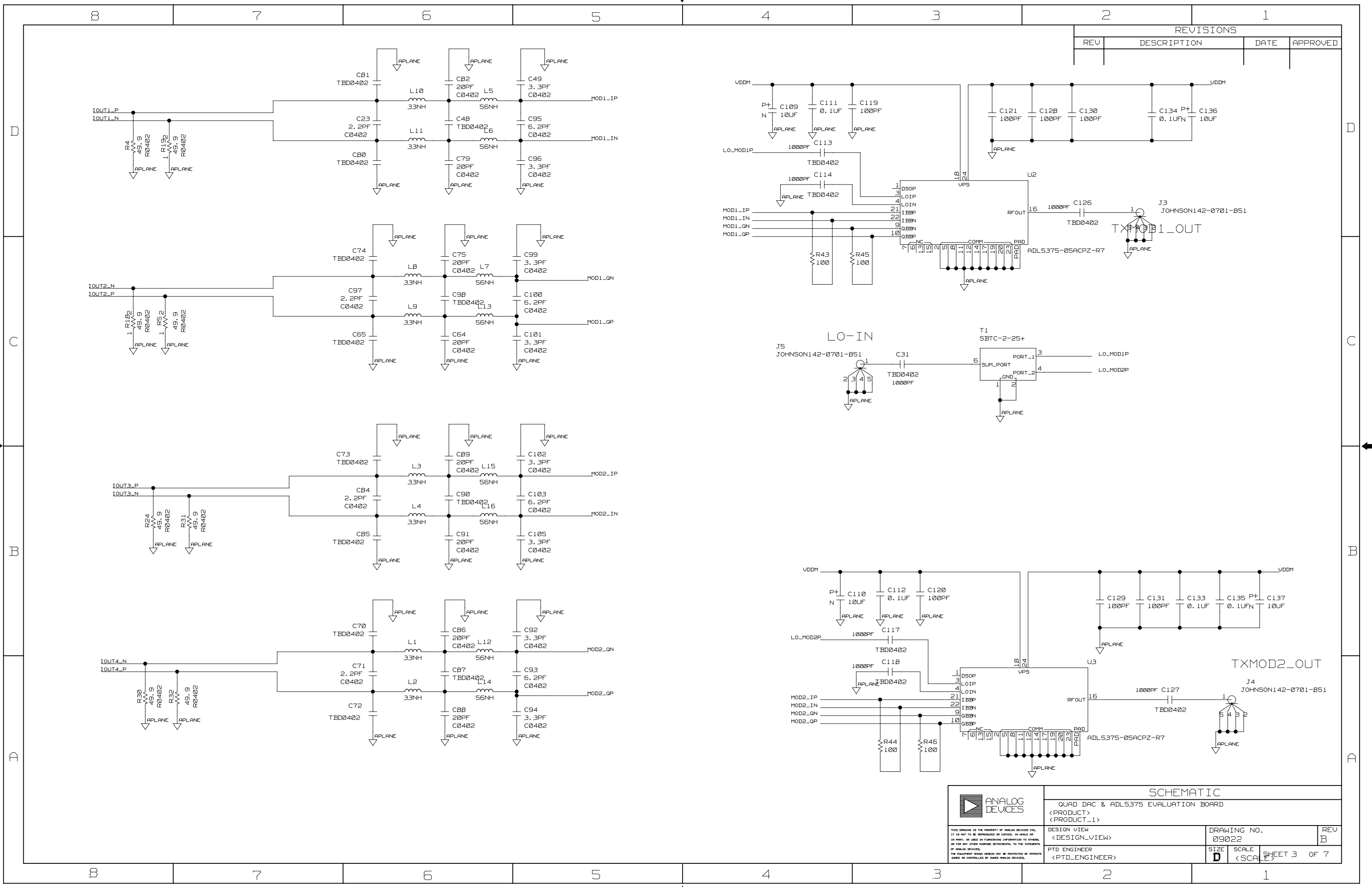
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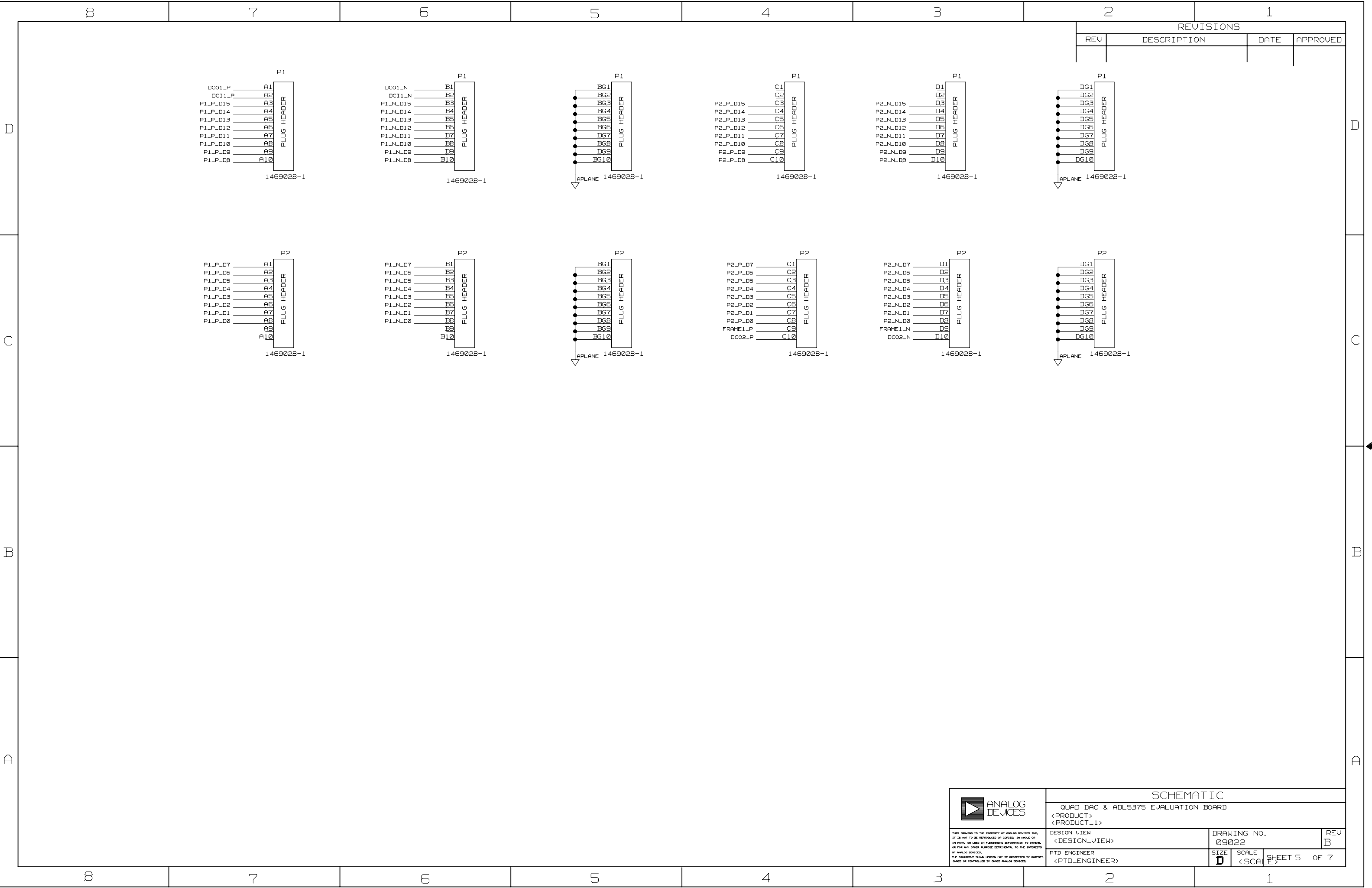
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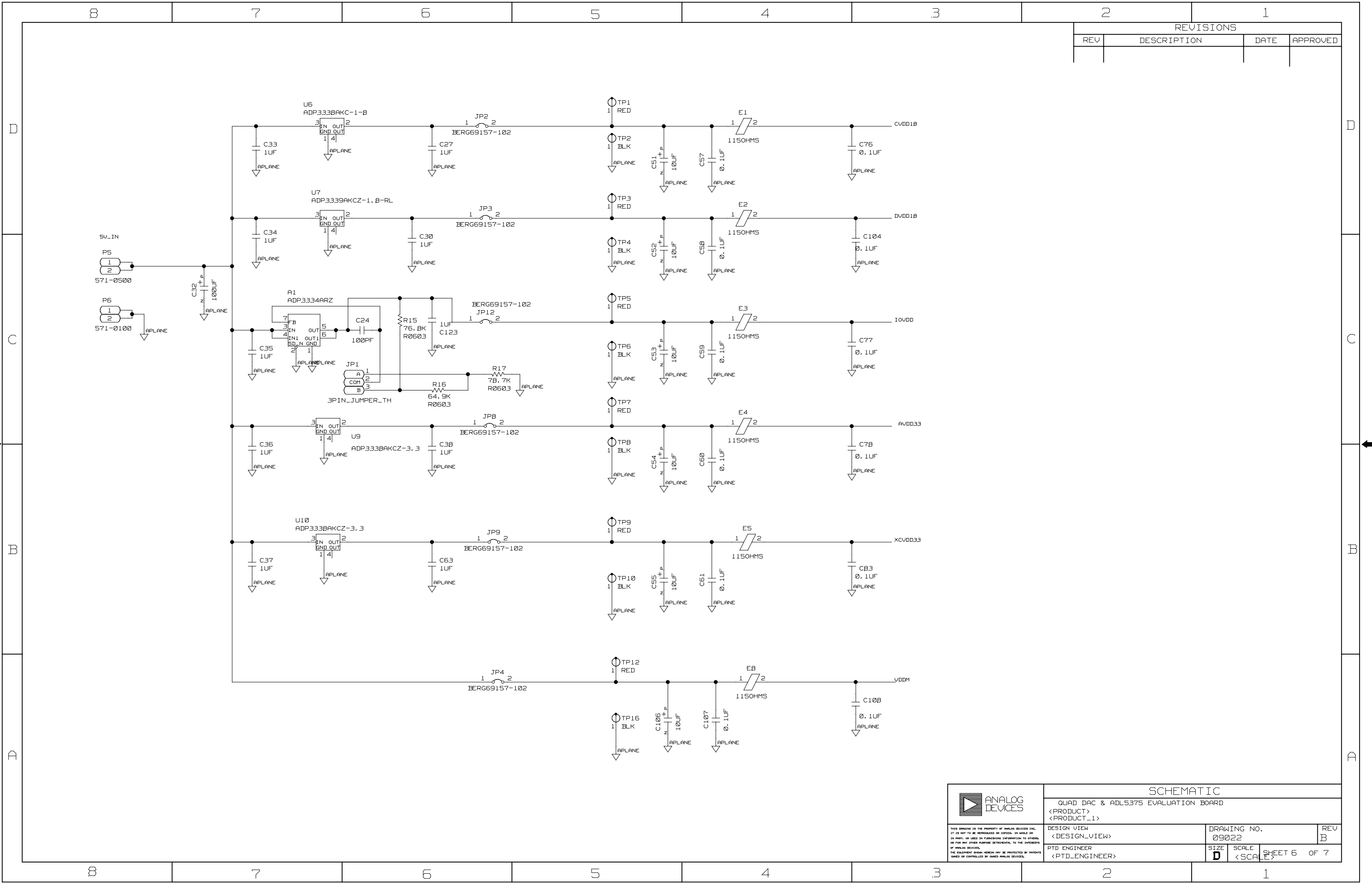
REV
B


SHEET 2 OF 7

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ANALOG
DEVICES

SCHEMATIC

QUAD DAC & ADL5375 EVALUATION BOARD

<PRODUCT>
<PRODUCT_1>

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
<PTD_ENGINEER>

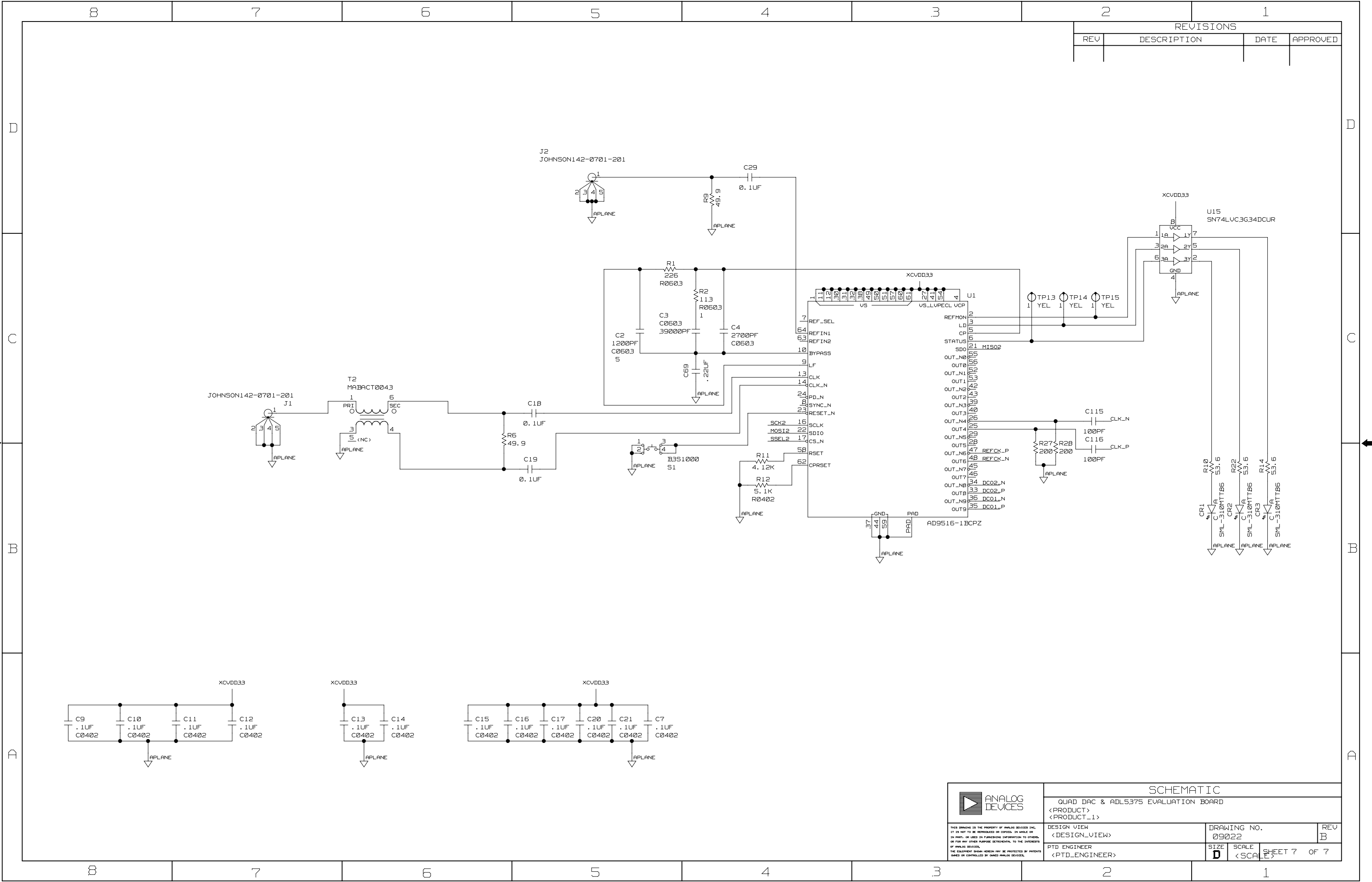
DRAWING NO.
09022


REV
B

SIZE
D

SCALE
<SCALE>

SHEET 6 OF 7



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	QUAD DAC & ADL5375 EVALUATION BOARD		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 09022	REV B
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>