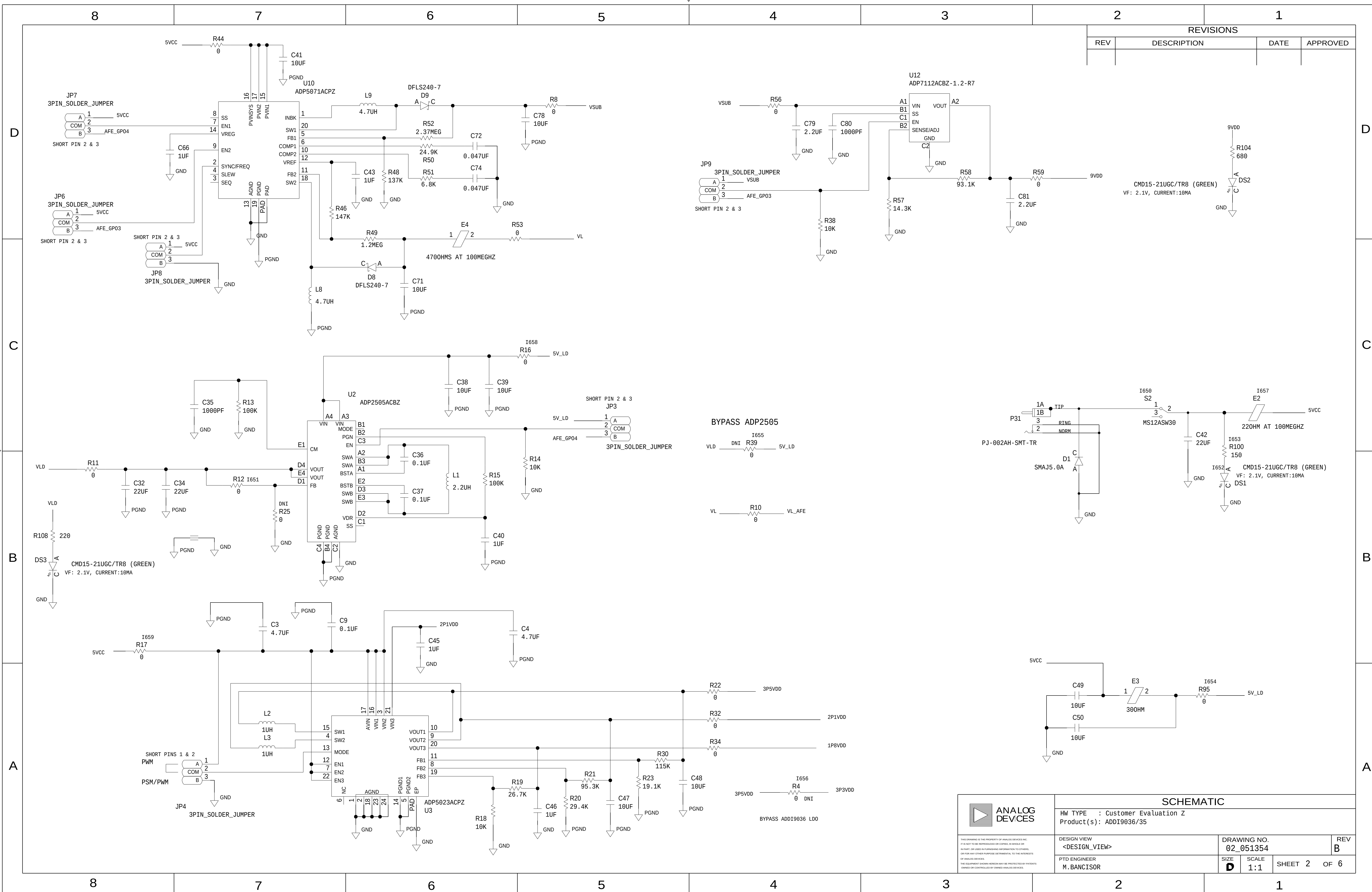
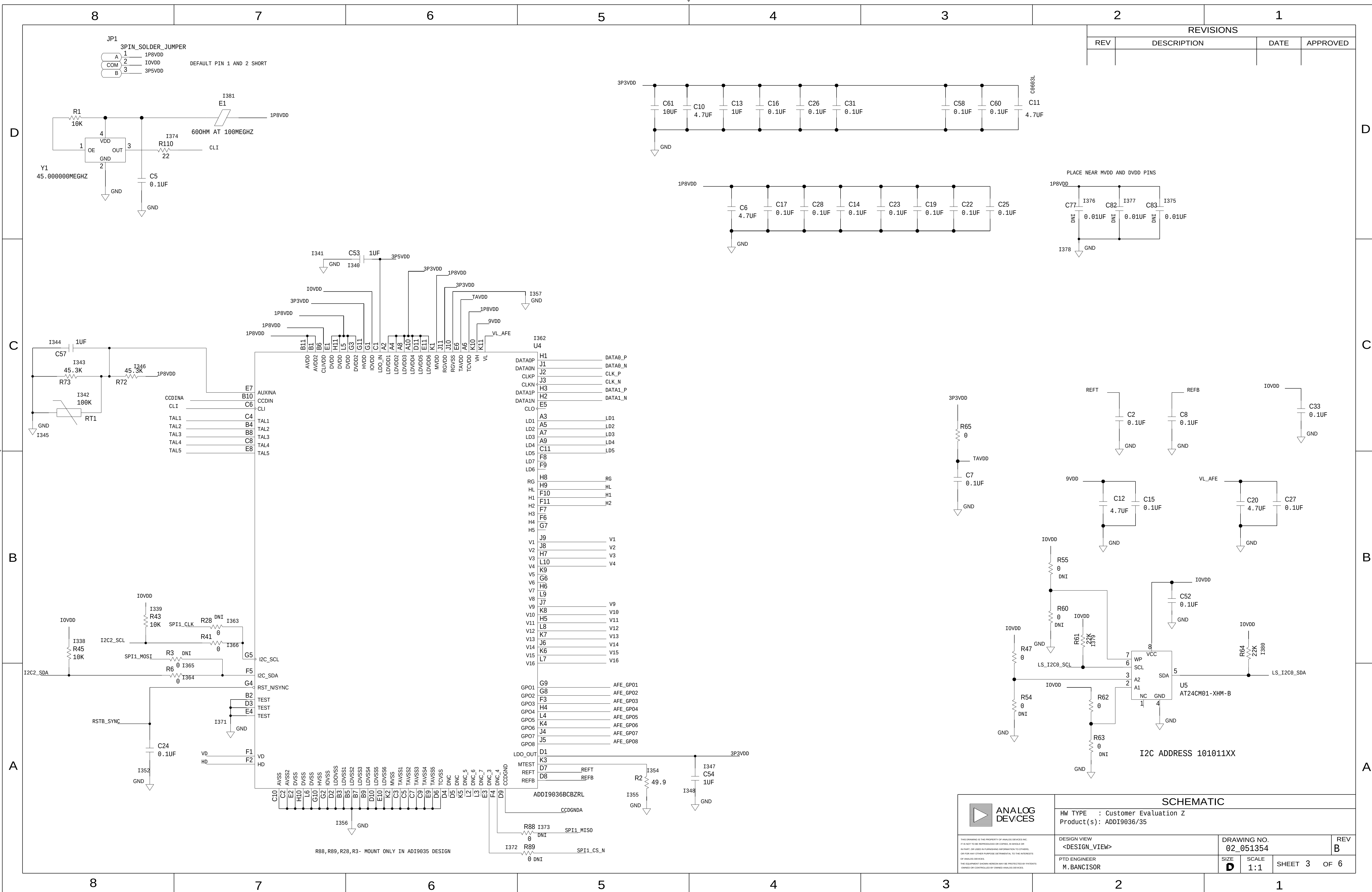
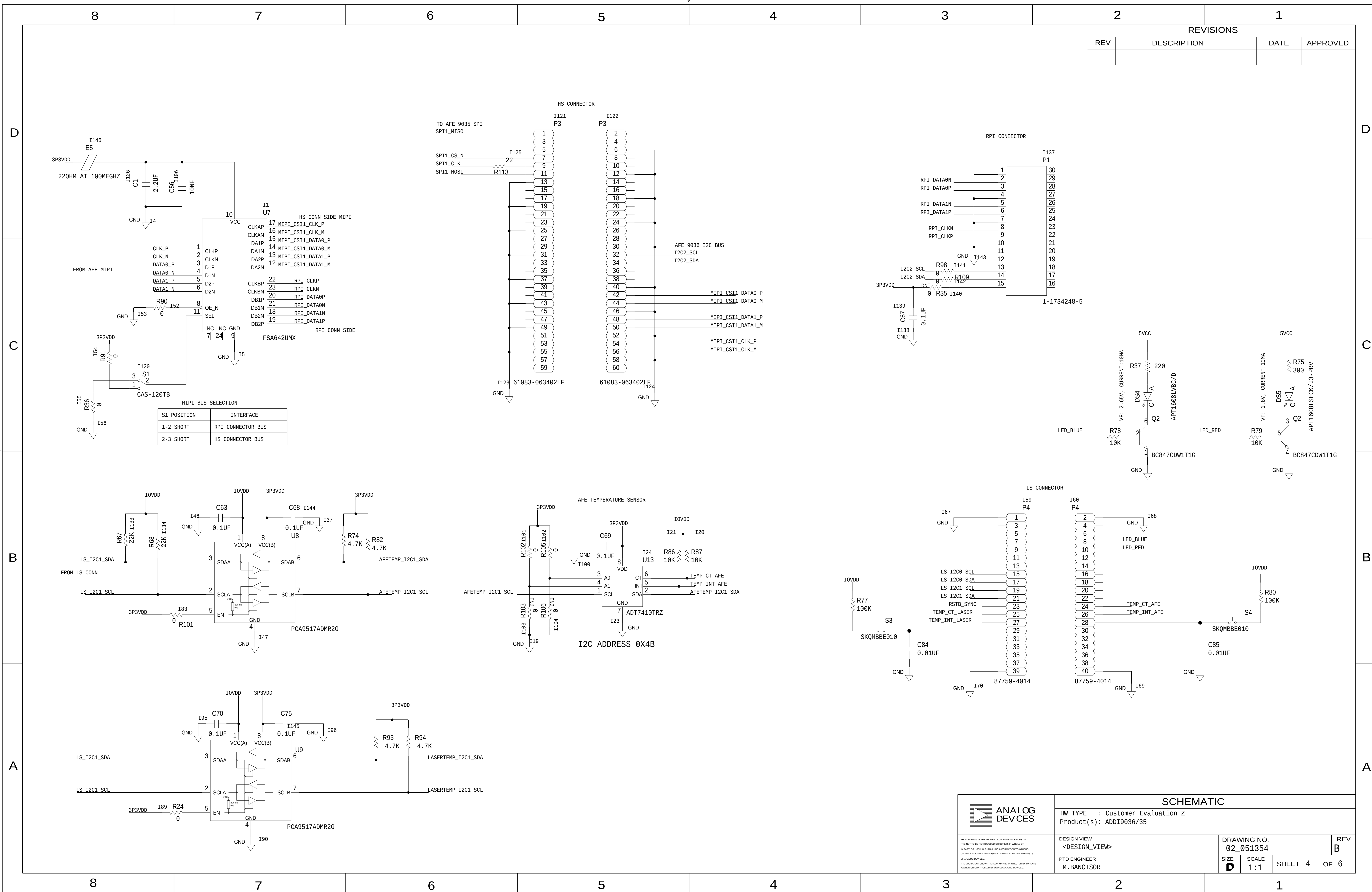


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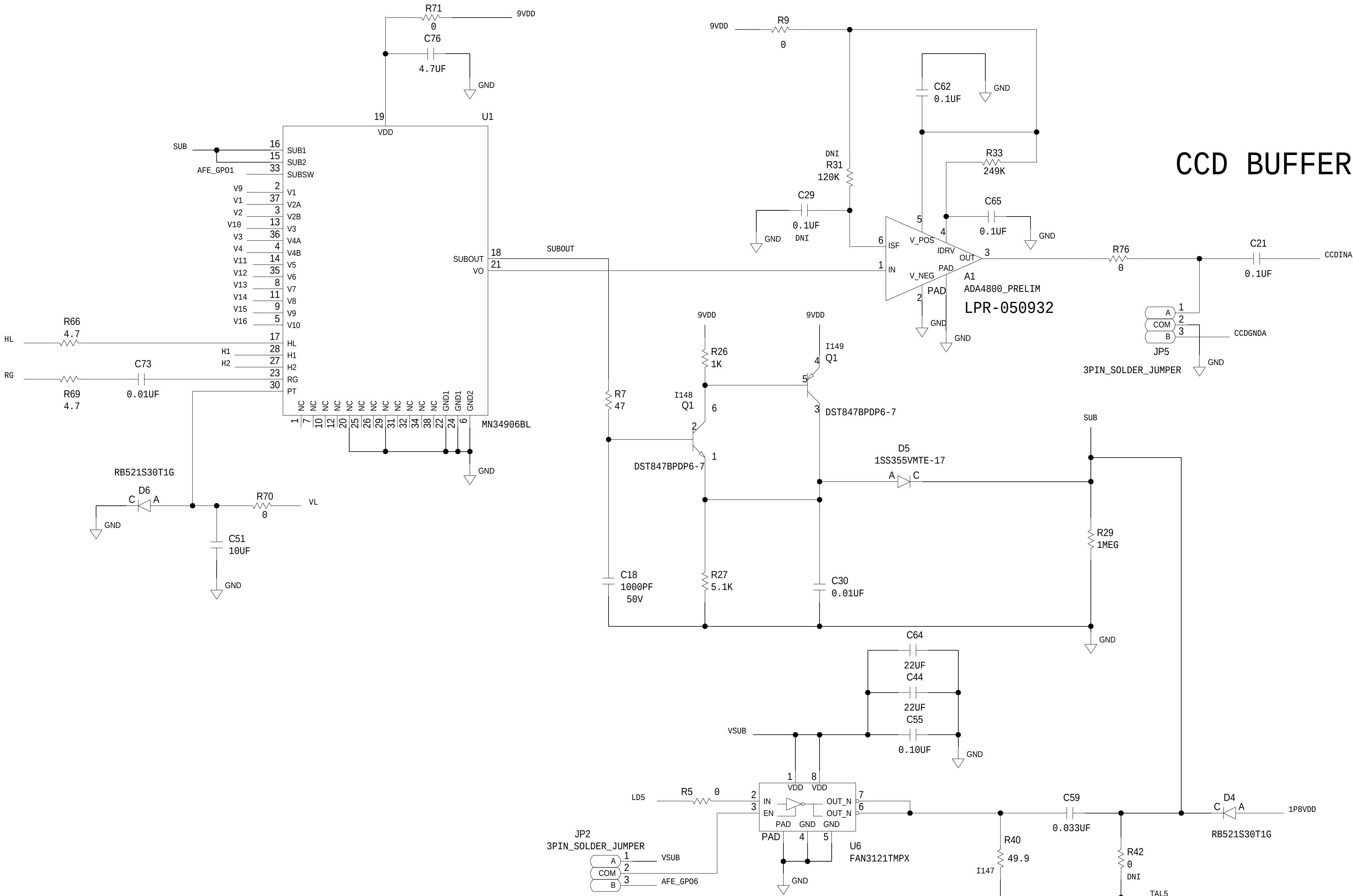






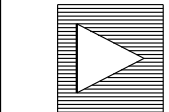
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

NOTE: SEPARATE VO AND RG BY GND PATTERN



CCD BUFFER CIRCUIT

SUB CLOCK DRIVER CIRCUIT



ANALOG
DEVICES

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SCHEMATIC

HW TYPE : Customer Evaluation Z
Product(s): ADDI9036/35

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
M. BANCISOR

DRAWING NO.
02_051354

SIZE
D

SCALE
1:1

SHEET 6 OF 6

REV
B