### Change Log

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Description</th>
<th>Author</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.1</td>
<td>2012/07/16</td>
<td>Initial Release</td>
<td>MCP</td>
</tr>
<tr>
<td>0.1</td>
<td>2014/01/20</td>
<td>Public Release. Removed unnecessary notes from the schematic.</td>
<td>MCP</td>
</tr>
</tbody>
</table>
SDRAM should be as close to the BF524 as possible.
Silkscreen: JTAG

JTAG header should be as close to BF524 as possible

I2C Aardvark Connector

BF_UART1TX_T 5
BF_UART1RX_T 5
BF_TMS 4
BF_TCK 4
BF_TRSTn 4
BF_TDI 4
BF_TDO 4
BF_EMUn 4

V3P3 3,4,5,6,7,8,11,12,13,14,15,16,17,18,19,20

BF_SPI_SEL1 5
BF_SPI_SCK 5,13
BF_SPI_MOSI 5,13
BF_SPI_MISO 5,13
BF_SDA_T 5,13
BF_SCL_T 5,13

Title Size Document Number Rev

Date: Sheet of

Advantiv™ EVAL-ADV7625-SMZ Video Evaluation Board

BF524 FLASH / JTAG / RS232

Monday, January 20, 2014
ADV7625 I2C ADDRESS:
ALSB Low : IO Map address is 0xB0 (default)
ALSB High : IO Map address is 0xB2
JTAG header should be as close to SHARC as possible.