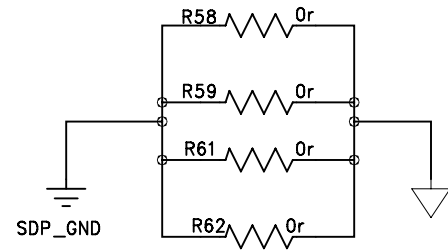


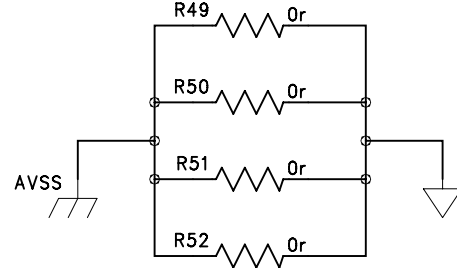
AD7176-2 24-Bit 250KSPS SD ADC

GPIO Ref to 5V Analog
Provision for voltage divider
for low voltages

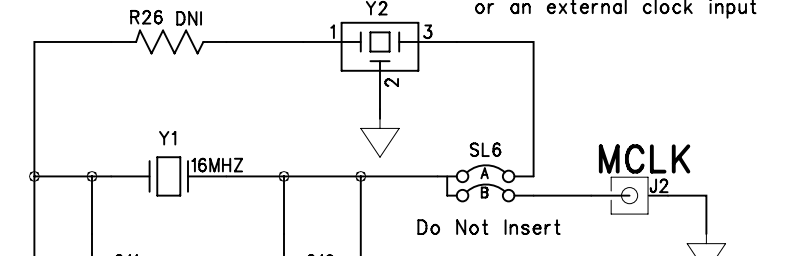
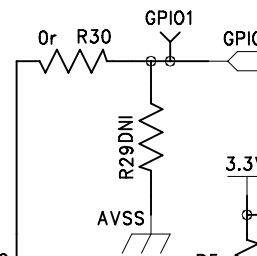
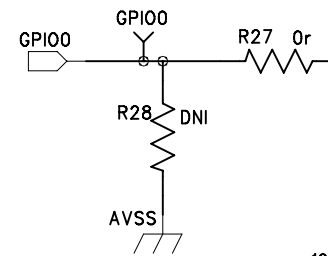
Connector:
ADC internal clock output
or an external clock input



Connect SDP_GND and AGND
for ADP7104 return currents

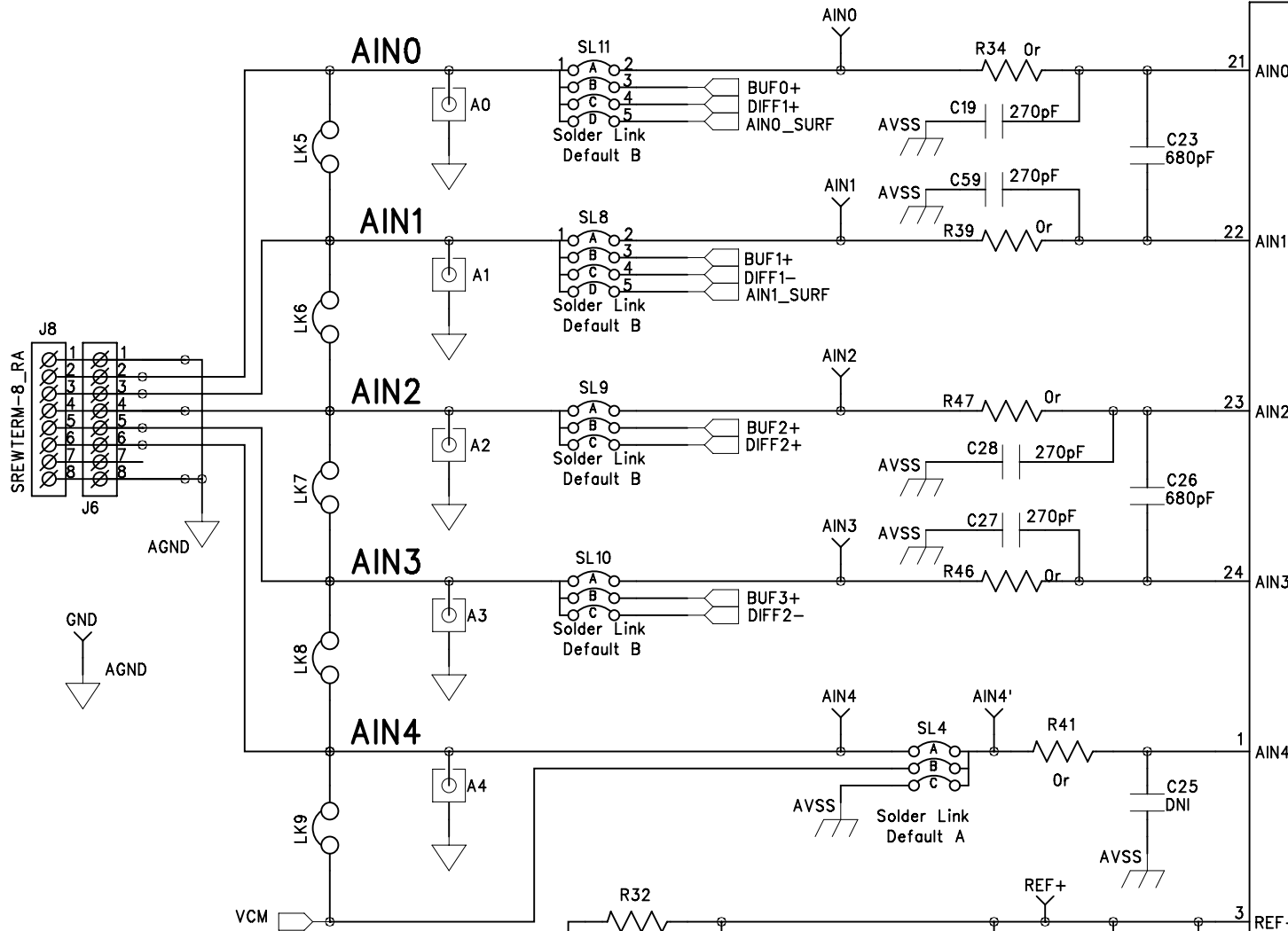


Connect AVSS and AGND for 5V supply
Disconnect for +/- 2.5V operation



Do Not Insert

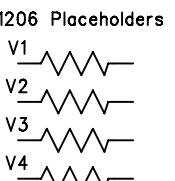
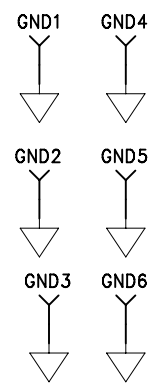
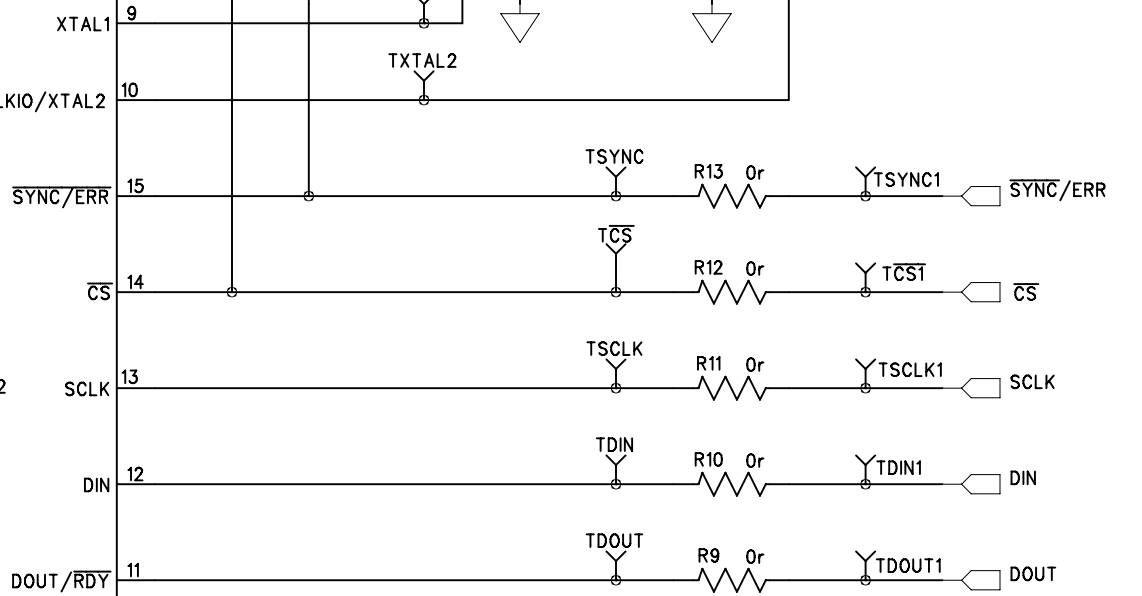
MCLK



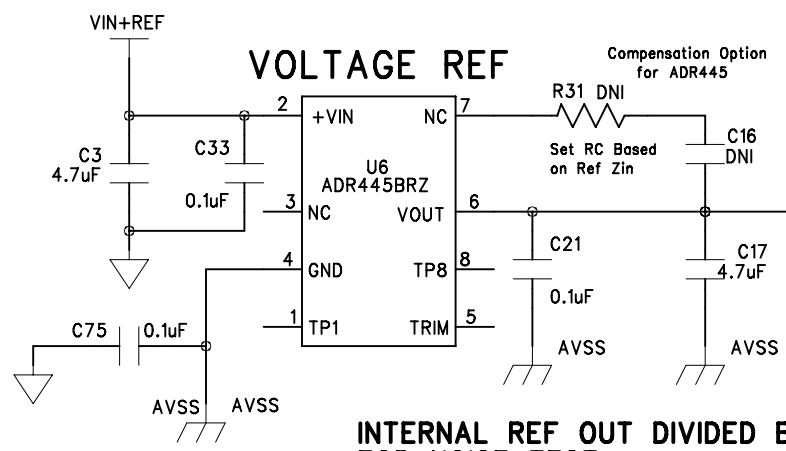
ADC

U5
AD7176-2

DIGITAL INTERFACE

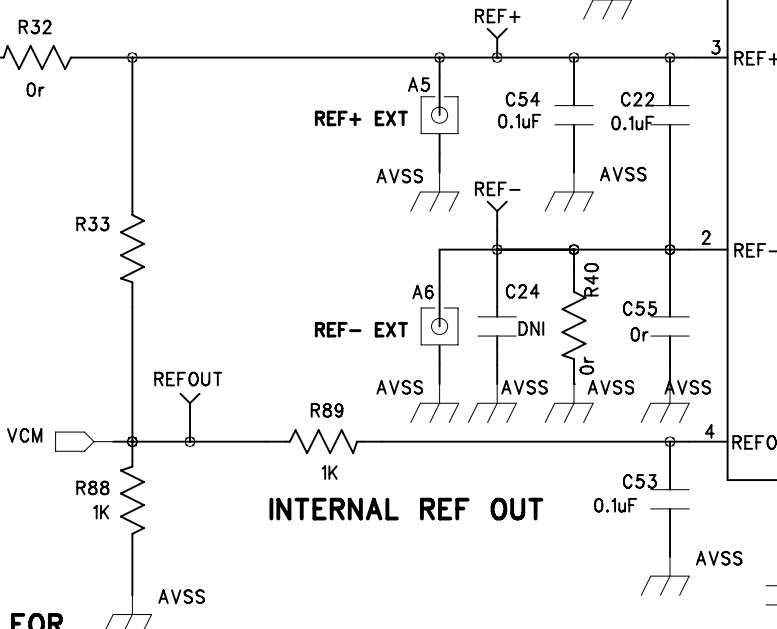


Set SL1 to A.
Setting AVDD2 = 3.3V is not allowed
when AVDD1=2.5V & AVSS = -2.5V.

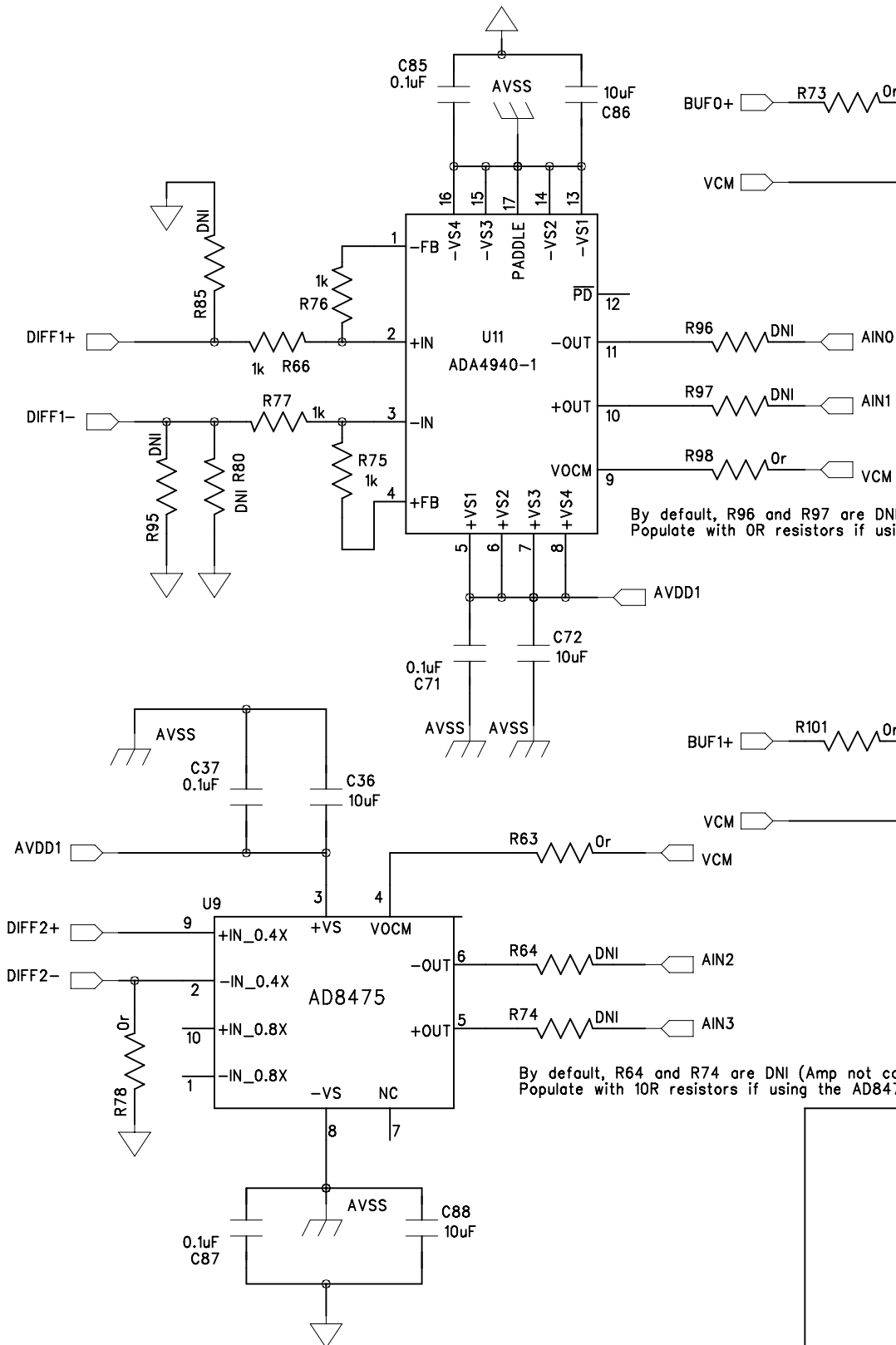


INTERNAL REF OUT DIVIDED BY 2
FOR NOISE TEST
SET R89 TO 0R AND REMOVE R88 FOR
ANY OTHER ANALOG INPUT SIGNALS

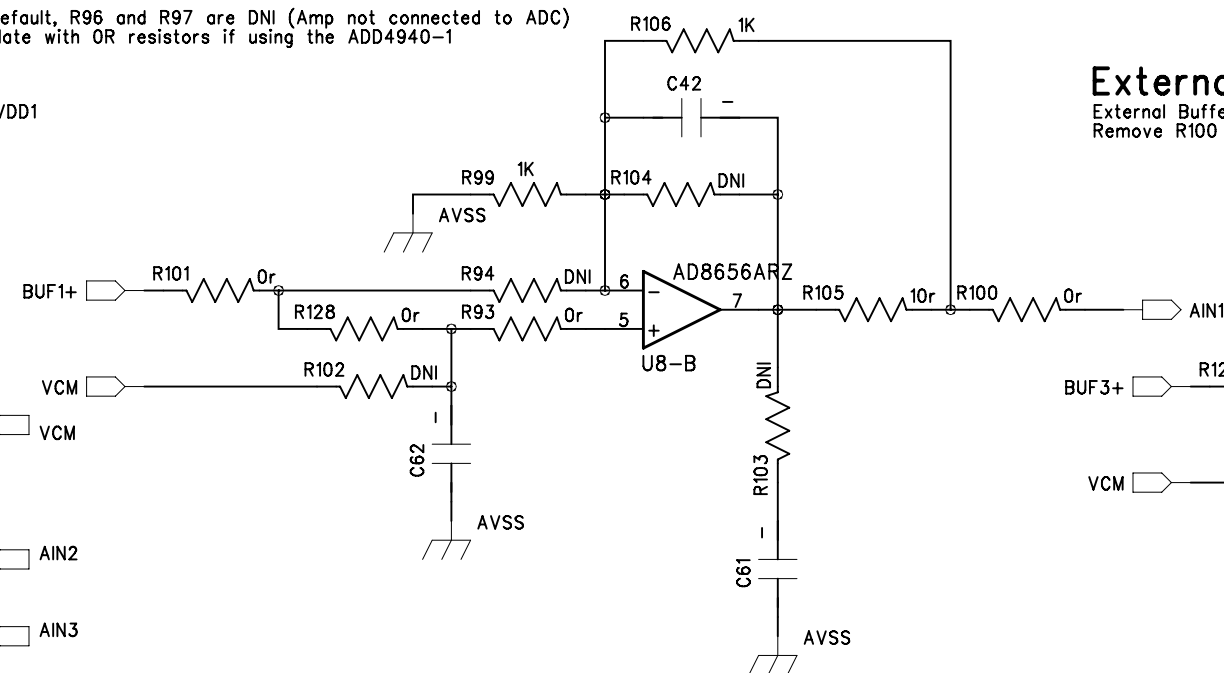
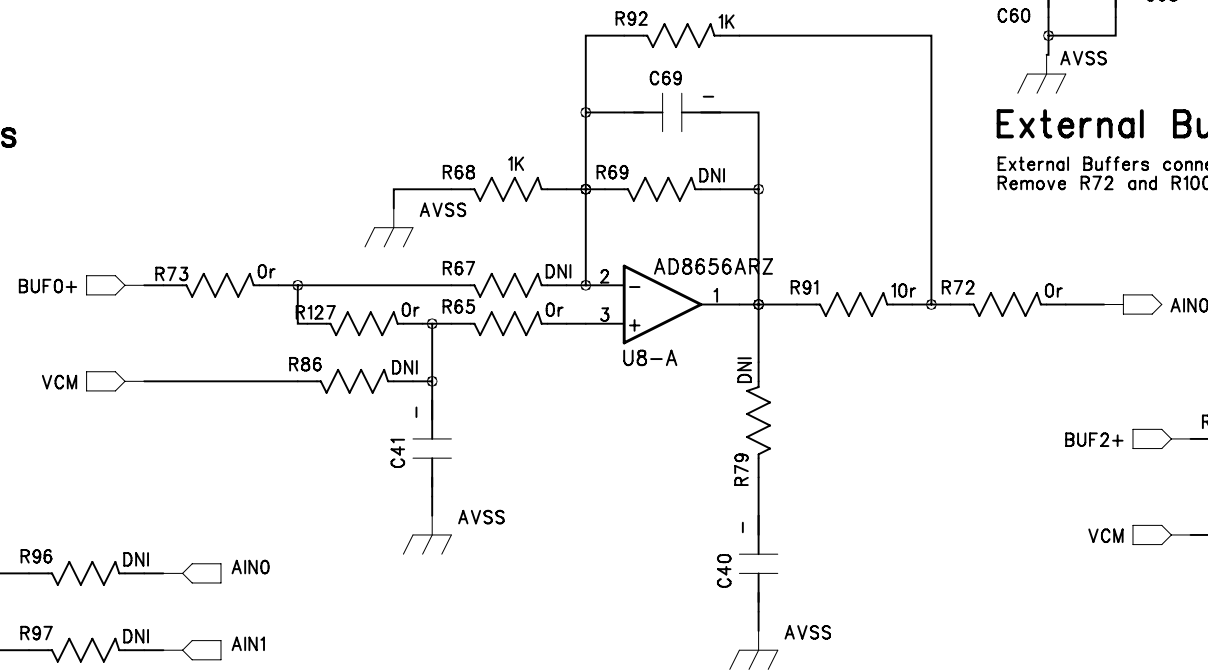
INTERNAL REF OUT



Single Ended to Diff ADC Driver Options

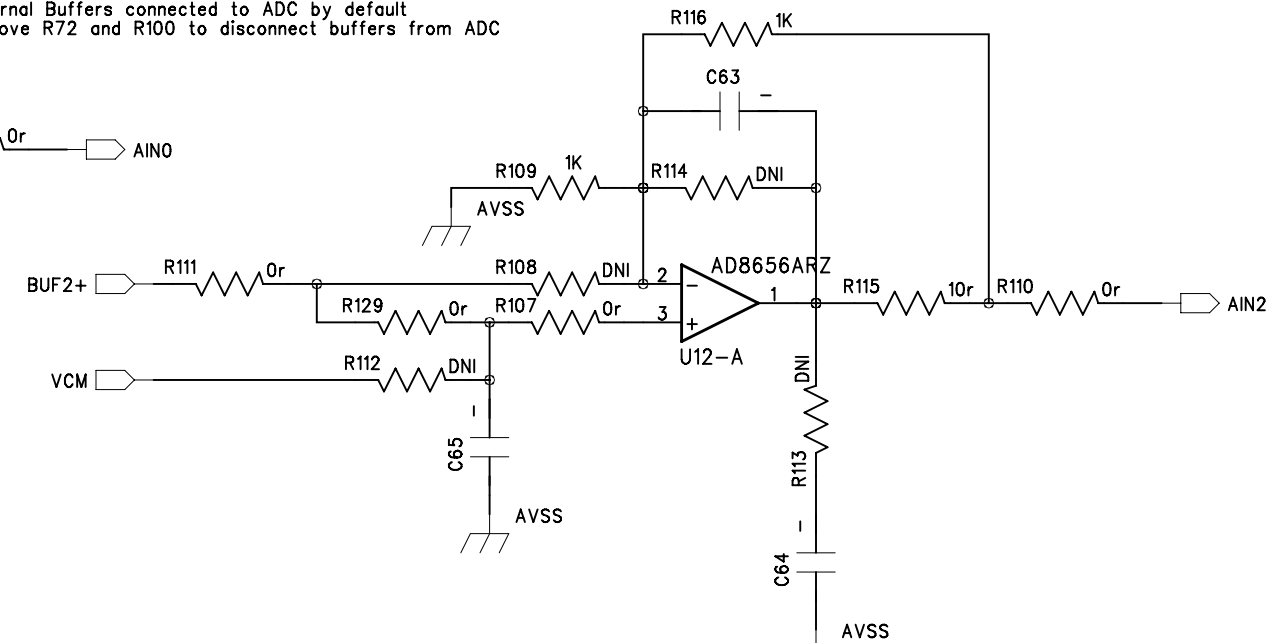
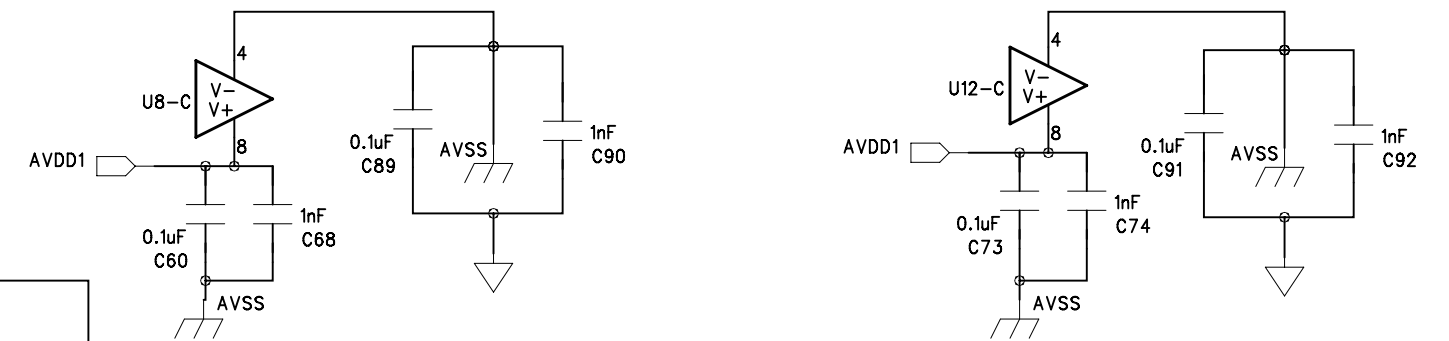


Attenuating Single Ended to Differential Driver



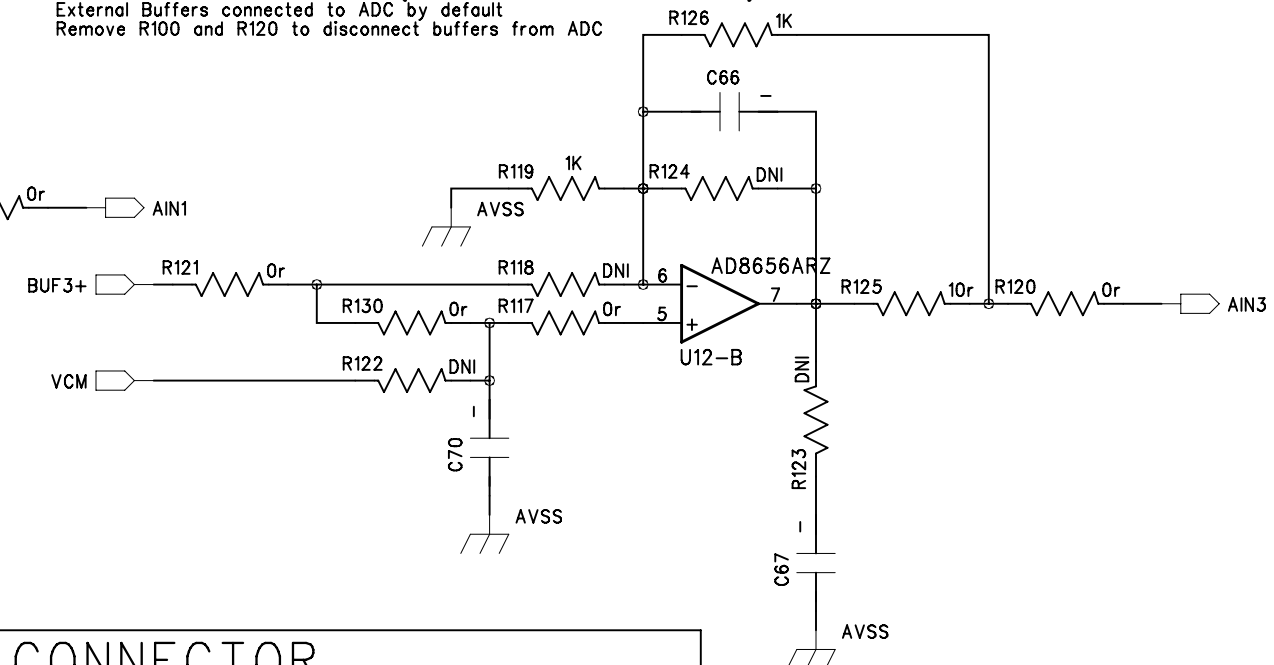
External Buffers (Default Gain = 2)

External Buffers connected to ADC by default
Remove R72 and R100 to disconnect buffers from ADC

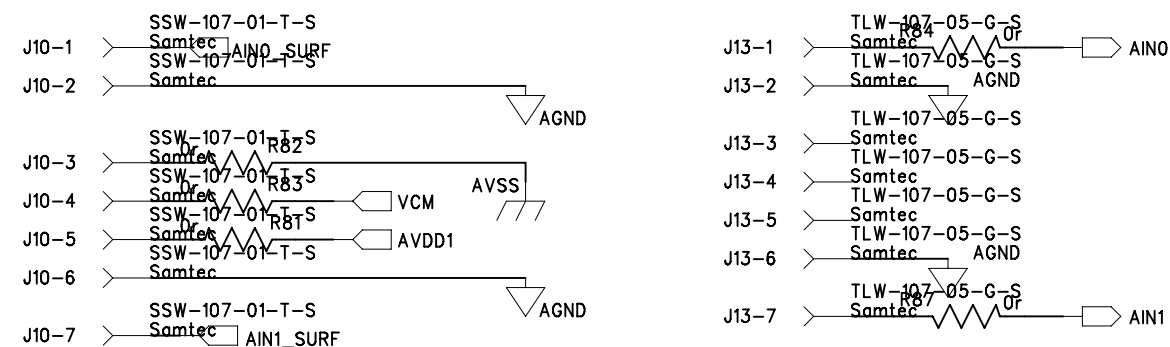


External Buffers (Default Gain = 2)

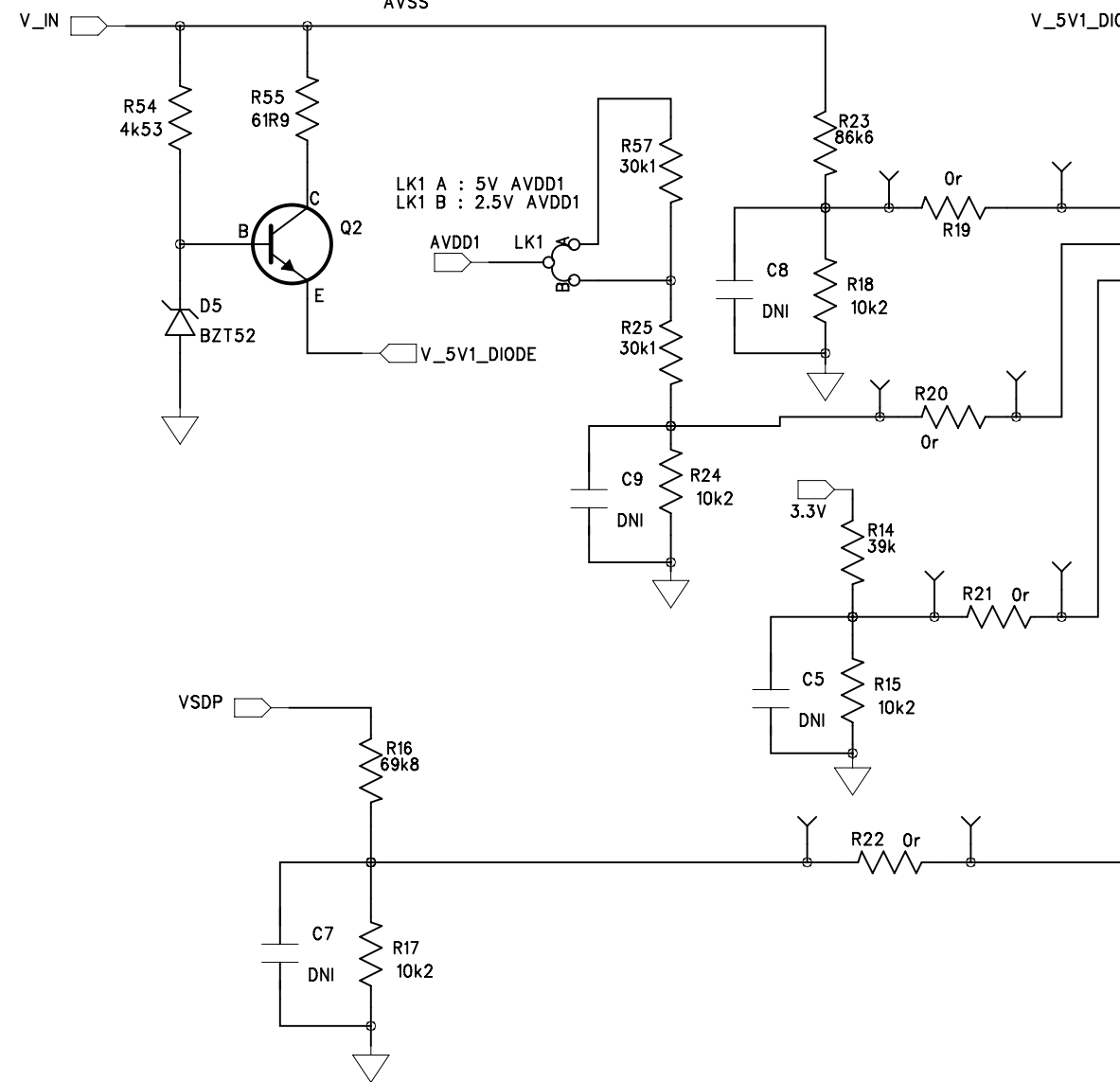
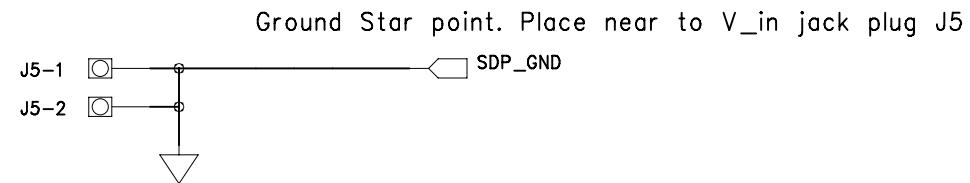
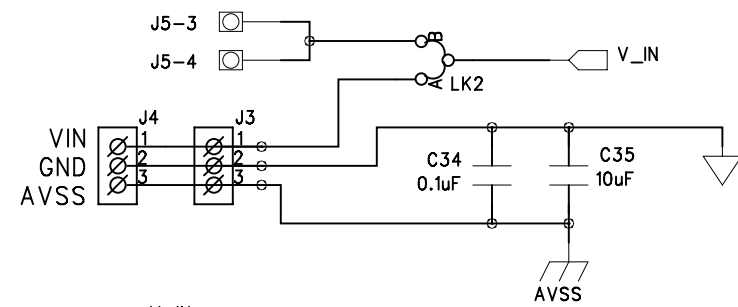
External Buffers connected to ADC by default
Remove R100 and R120 to disconnect buffers from ADC



OPTIONAL HEADER CONNECTOR

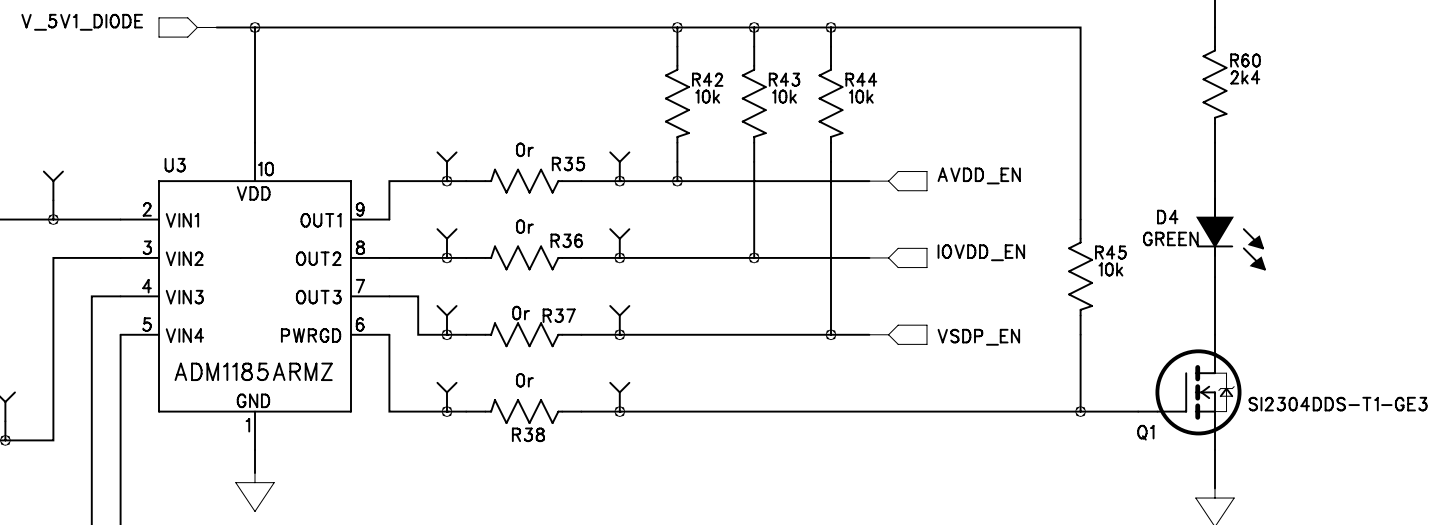


POWER SUPPLY

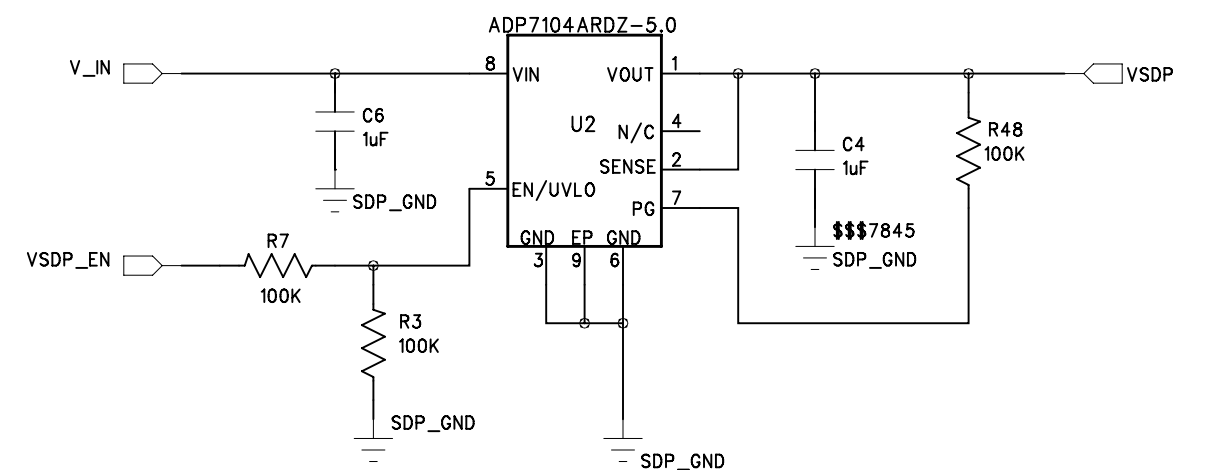


C160-C163 placed only if specific delays needed

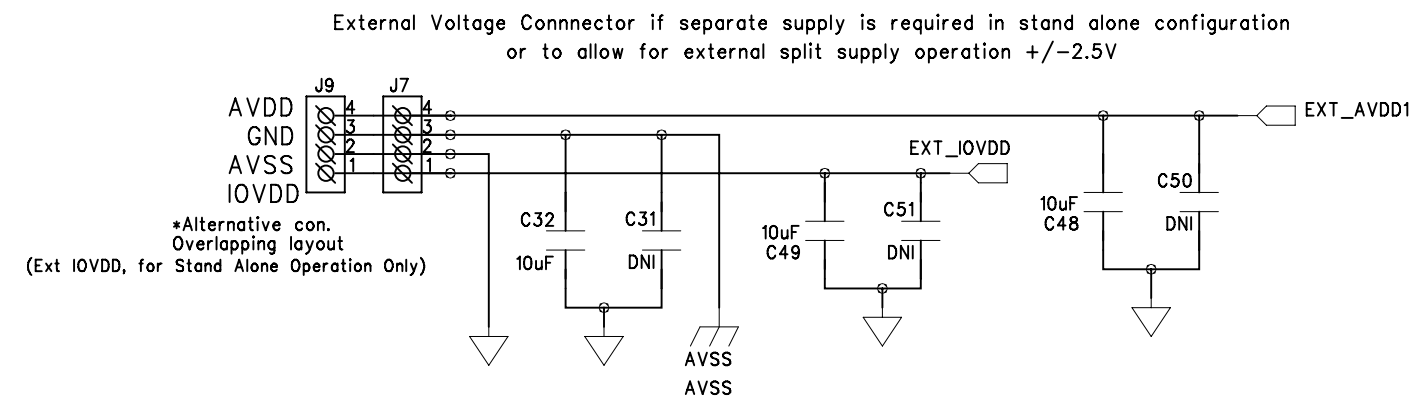
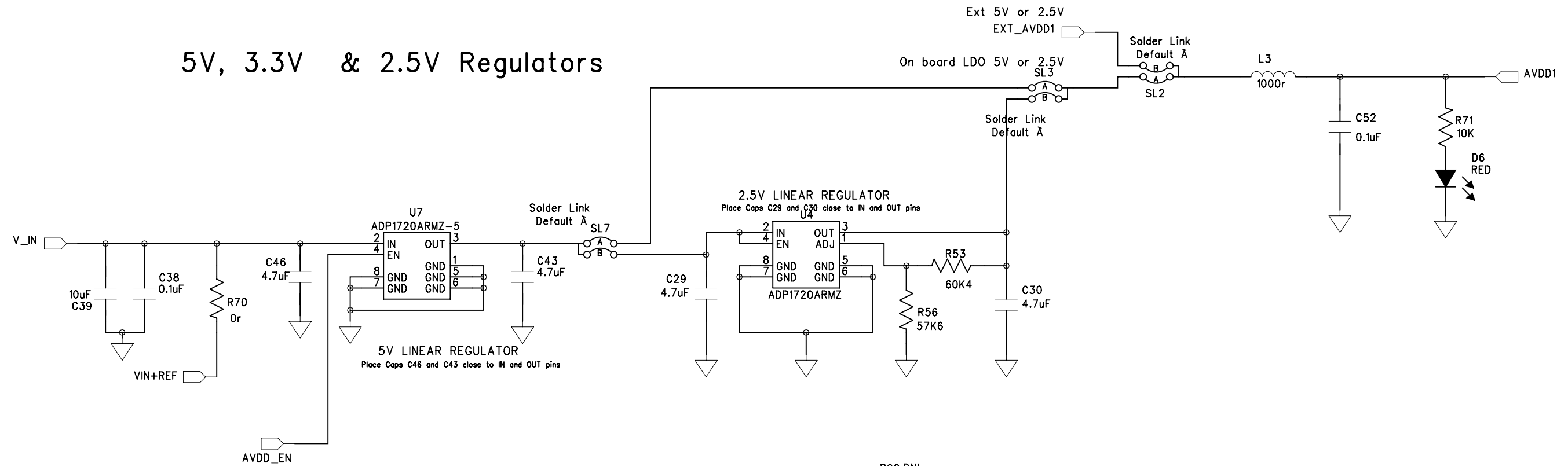
POWER SEQUENCE CONTROL



Power Supply for SDP Board 5V LDO to Power SDP



5V, 3.3V & 2.5V Regulators



SDP CONNECTOR EEPROM-SW/USB ID

VIO: USE to set IO voltage max draw 20mA

VIN: Use this pin to power the SDP requires 4-7V 200mA

BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board

