

6

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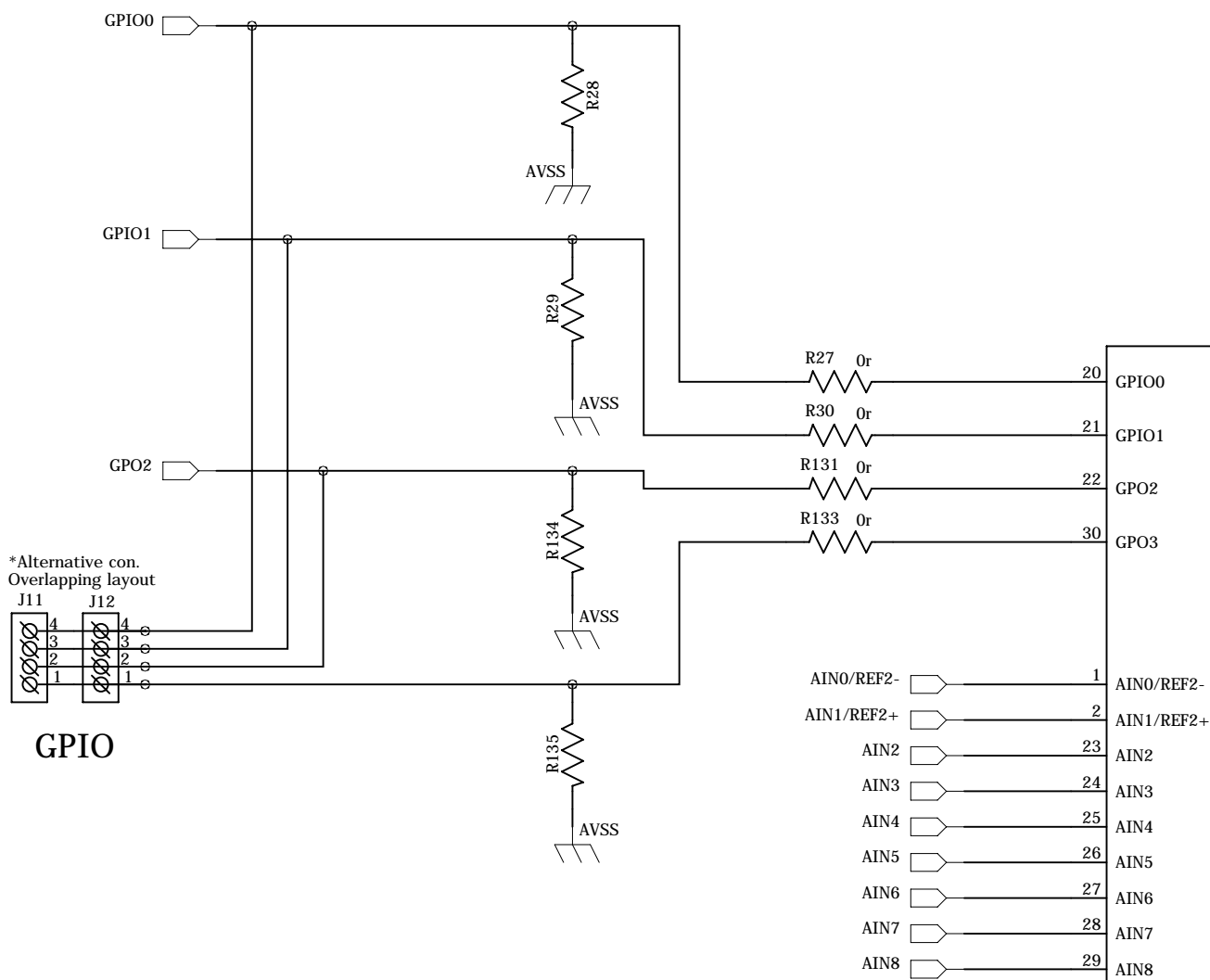
2

1

AD7172-4 24-Bit 31.25KSPS SD ADC

GPIO Ref to AVDD1 Analog
Provision for voltage divider
for low voltages

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



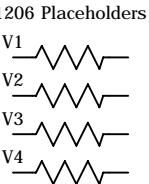
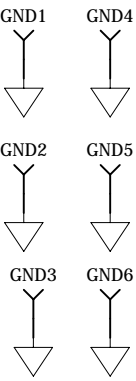
GPIO

ADC

DIGITAL INTERFACE

Pin 6 = No Connect

PMOD



COMPANY: Analog Devices International			
TITLE: EVAL-AD7172-4SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A2	02-039649	A
SCALE: N/A		SHEET: 1 of 6	

DRAWN: Pat Sheahan	DATED: 7-Oct-2014
CHECKED: Rob Kiely	DATED: 3-Sept 2014
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

6

5

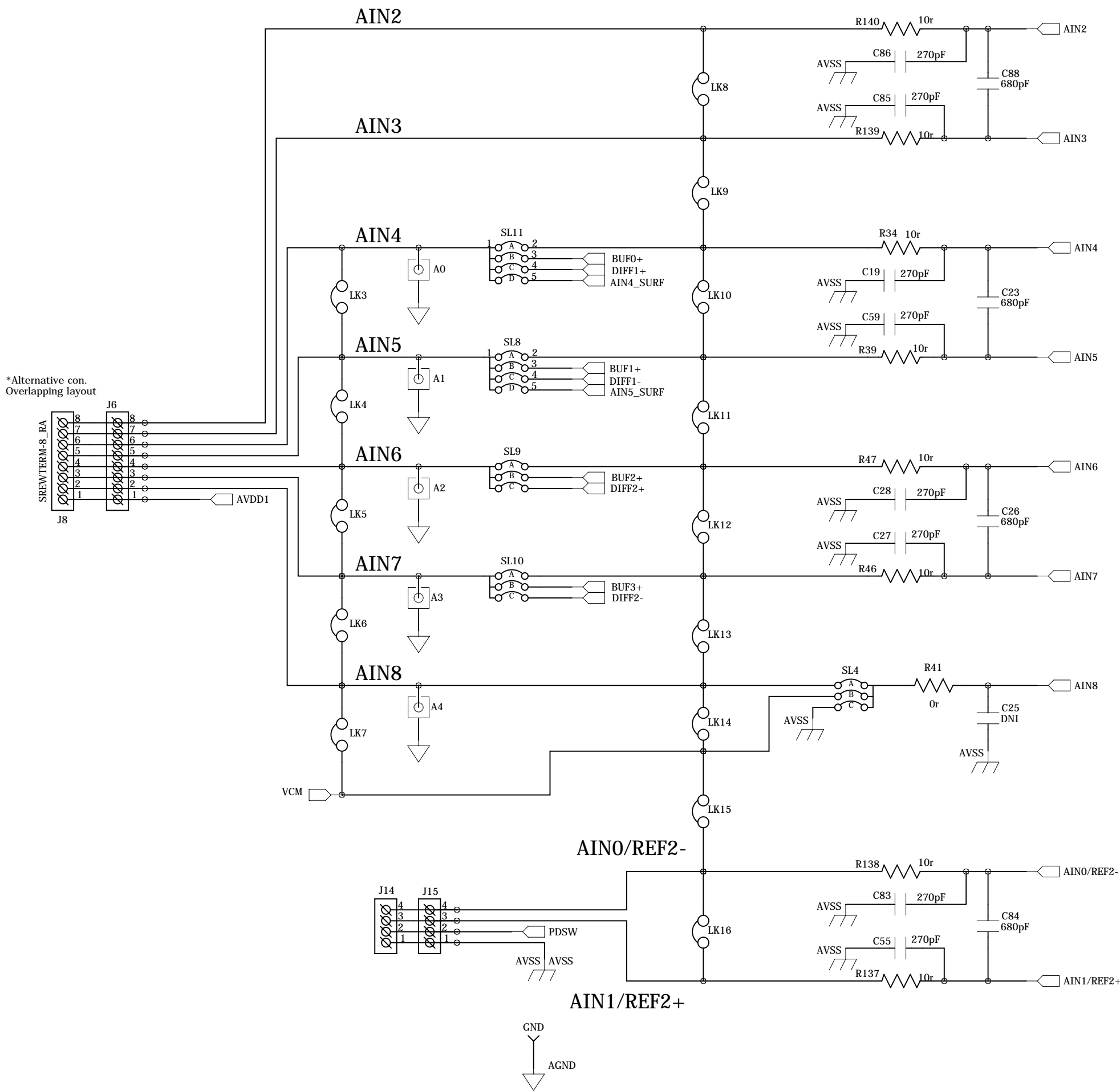
4

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2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Analog Devices International			
TITLE: EVAL-AD7172-4SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A2	02-039649	A
SCALE: N/A			SHEET: 2 OF 6

DRAWN: Pat Sheahan	DATED: 7-Oct-2014
CHECKED: Rob Kiely	DATED: 7-Oct-2014
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

6

5

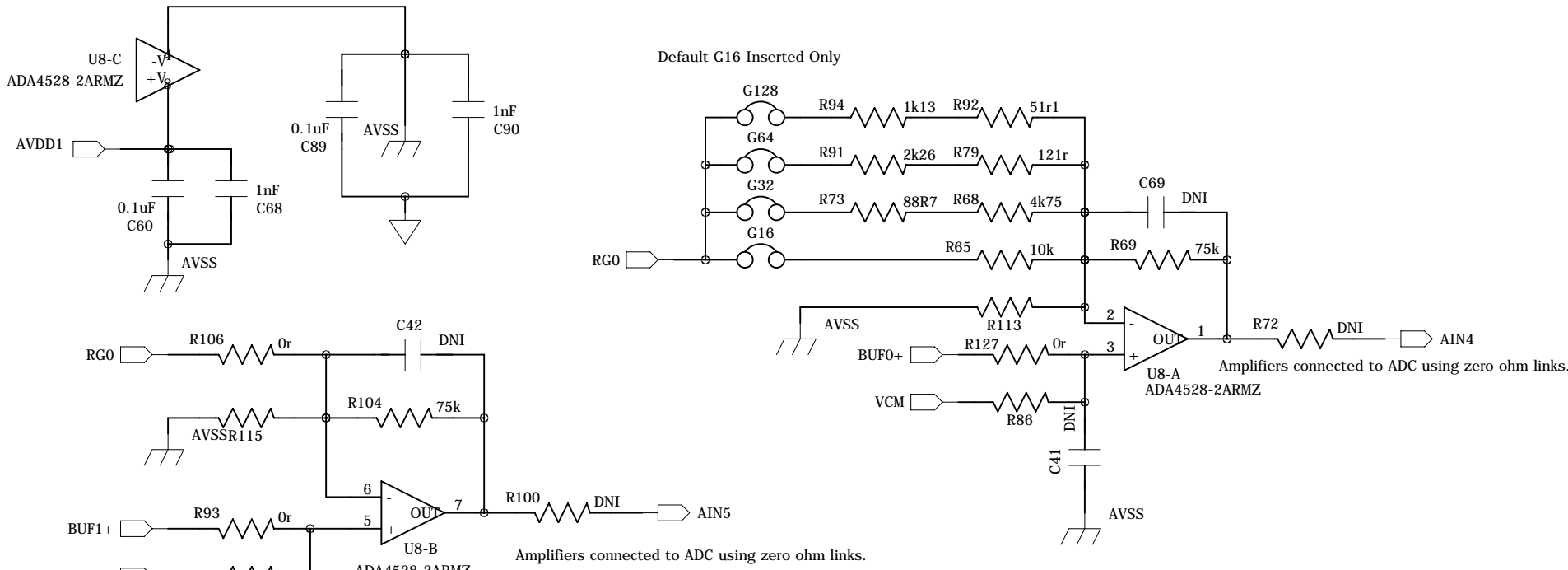
4

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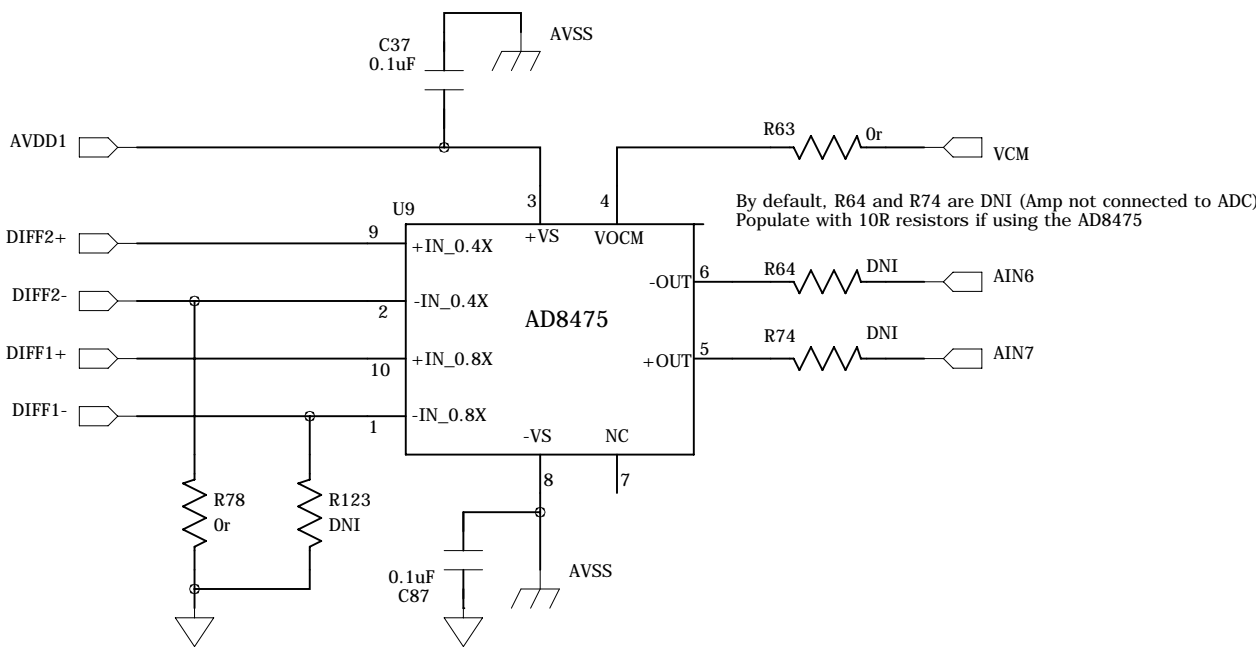
1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



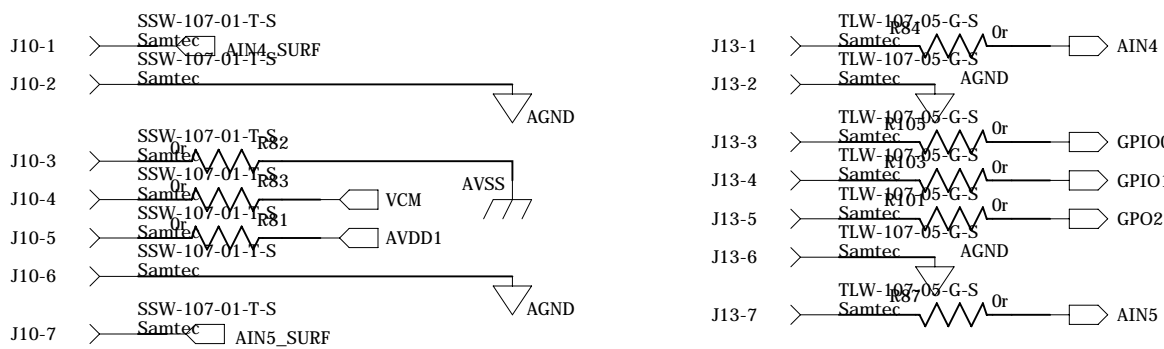
Selectable Gain InAmp 1st Stage

Reconfigurable Input Stage



Attenuating Single Ended to Differential Driver

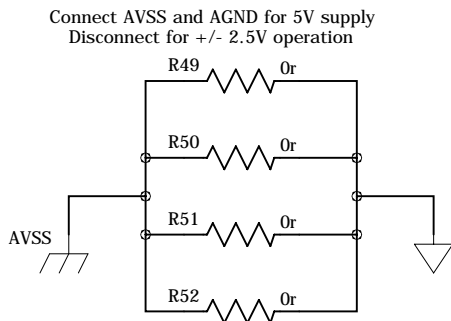
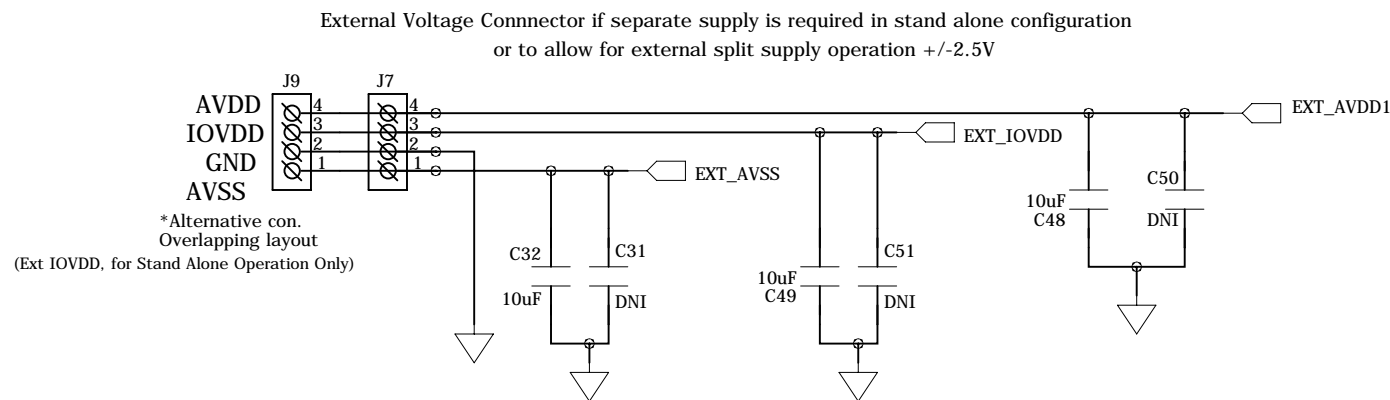
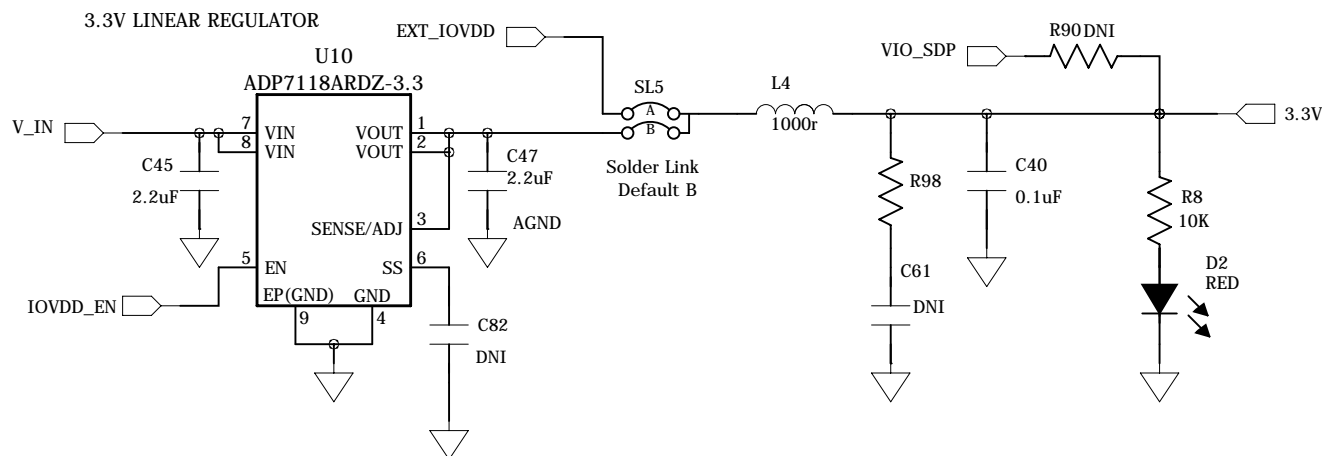
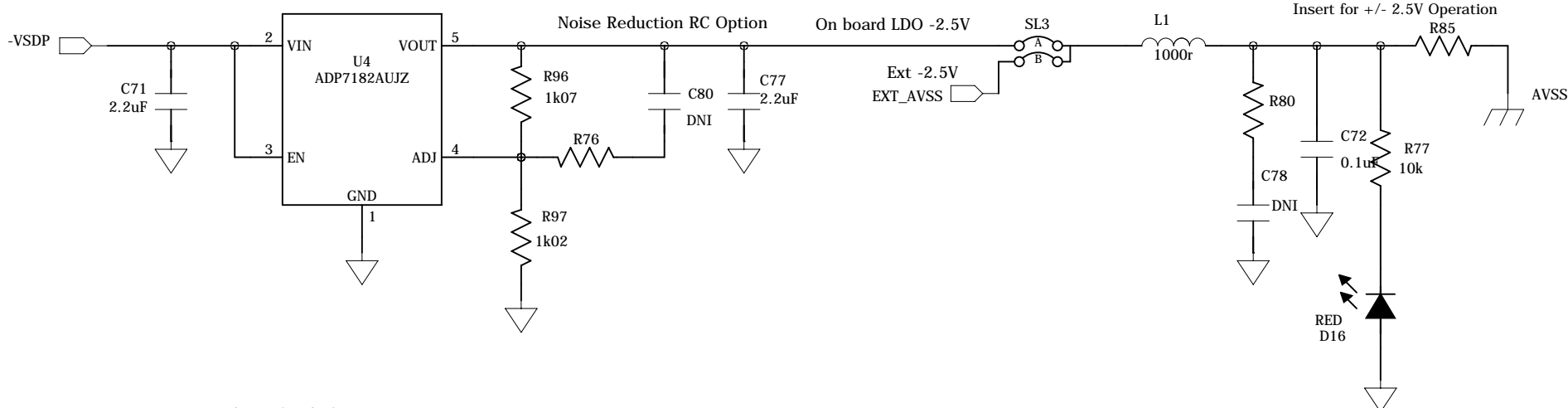
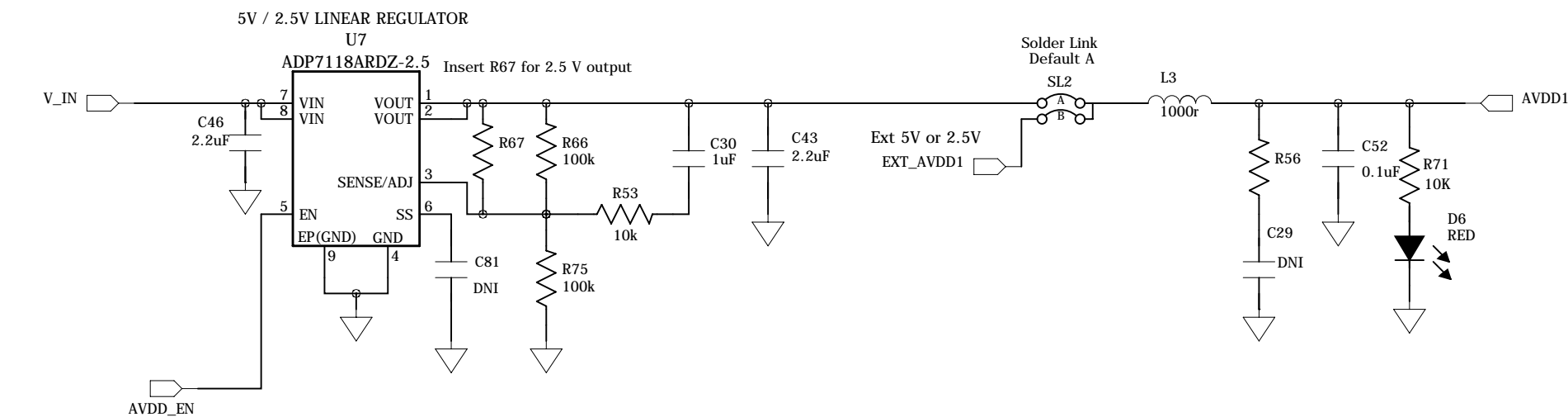
OPTIONAL HEADER CONNECTOR



COMPANY: Analog Devices International				
TITLE: EVAL-AD7172-4SDZ				
CHECKED: Rob Kiely	DATED: 3-Sept 2014	CODE: <Code>	SIZE: A2	DRAWING NO: 02-039649
QUALITY CONTROL: <QC By>	DATED: <QC Date>			REV: A
RELEASED: <Released By>	DATED: <Release Date>	SCALE: N/A		SHEET: 30F 6

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

5V, 3.3V & +/- 2.5V Regulators



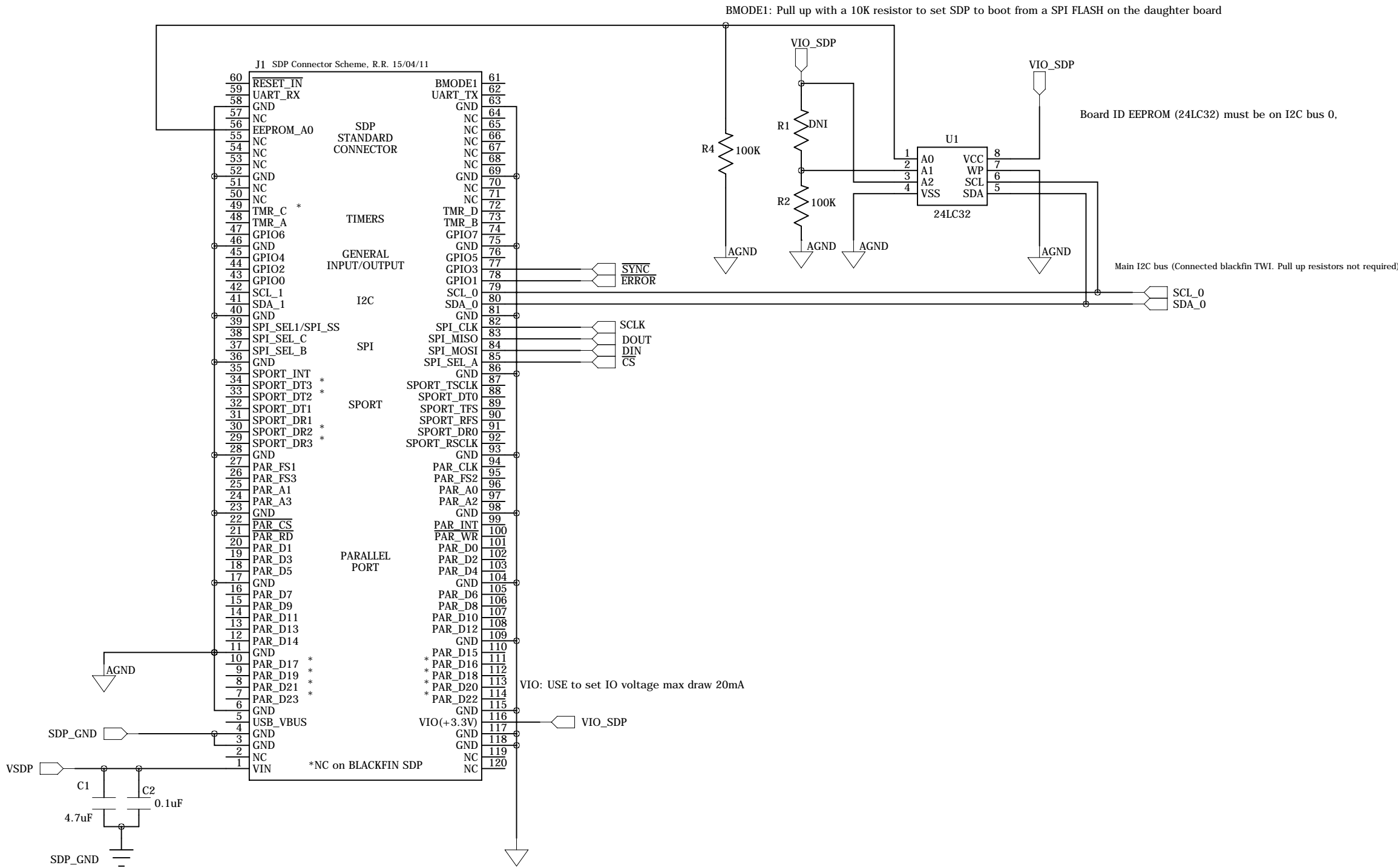
COMPANY: Analog Devices International				
TITLE: EVAL-AD7172-4SDZ				
CODE:	SIZE: A2	DRAWING NO: 02-039649		REV: A
SCALE: N/A		SHEET: 5 of 6		

DRAWN: Pat Sheahan	DATED: 7-Oct-2014
CHECKED: Rob Kiely	DATED: 3-Sept 2014
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

SDP CONNECTOR EEPROM-SW/USB ID

VIO: USE to set IO voltage max draw 20mA
VIN: Use this pin to power the SDP requires 4-7V 200mA
BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board



VIN: Use this pin to power the SDP requires 5V 200mA
I2C bus 1 is common across both connectors on SDP - Pull up resistors required (connected to blackfin GPIO - use I2C_0 first)

COMPANY: Analog Devices International			
TITLE: EVAL-AD7172-4SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A2	02-039649	A
SCALE: N/A			SHEET: 6 of 6

DRAWN: Pat Sheahan	DATED: 7-Oct-2014
CHECKED: Rob Kiely	DATED: 3-Sept 2014
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>