ADSP–CM419F EZ–KIT
SCHEMATIC
CUSTOMER DESIGN SHOULD CONNECT C12 DIRECTLY TO VDD_EXT

Place on bottom side of PCB directly under U1

1.2V @ 500mA

FAULT

32Mb SPI Flash

Power pins, SPI flash

ADSP-CM419F EZ-KIT

Title

ANALOG DEVICES

Date 08/02/16

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Rev 1.0A
ACNDO_ISO components need 1mm isolation from GND
LCD connector for SPI0

LCD connector for SPI1

ADDRESS: 1010 XXX, XXX is BLOCK SELECT

TWI address 1010 000x
where x is the R/W bit. Read – 1, Write – 0

ADDRESS PINS NOT USED, CAN FLOAT

20 x 2 Character Display