

87654321

REV A

DESCRIPTION

DATE

APPROVED

AD9257 (THOR OCTAL) ENGINEERING EVAL BOARD

12 BIT 40/80 MSPS - 14 BIT 40/65 MSPS

SINGLE LANE SERIAL LVDS

ANALOG
DEVICES

ENGINEER EVALUATION BOARD

AD9257

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DESIGN VIEW

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PTD ENGINEER

D.ITO

DRAWING NO.

9257EE01A

REV

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SIZE

D

SCALE

NONE

SHEET

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OF

10

87654321

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C

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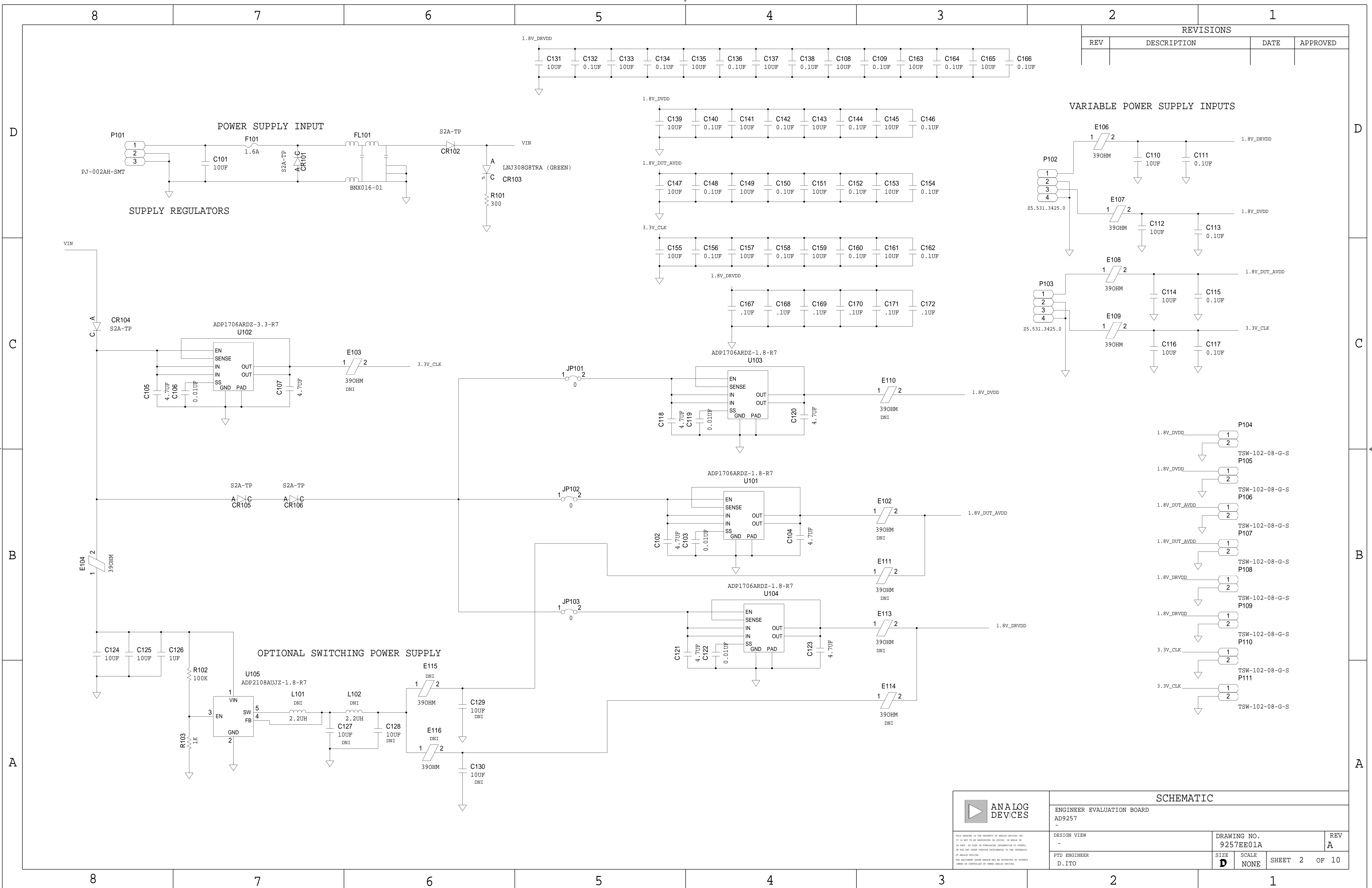
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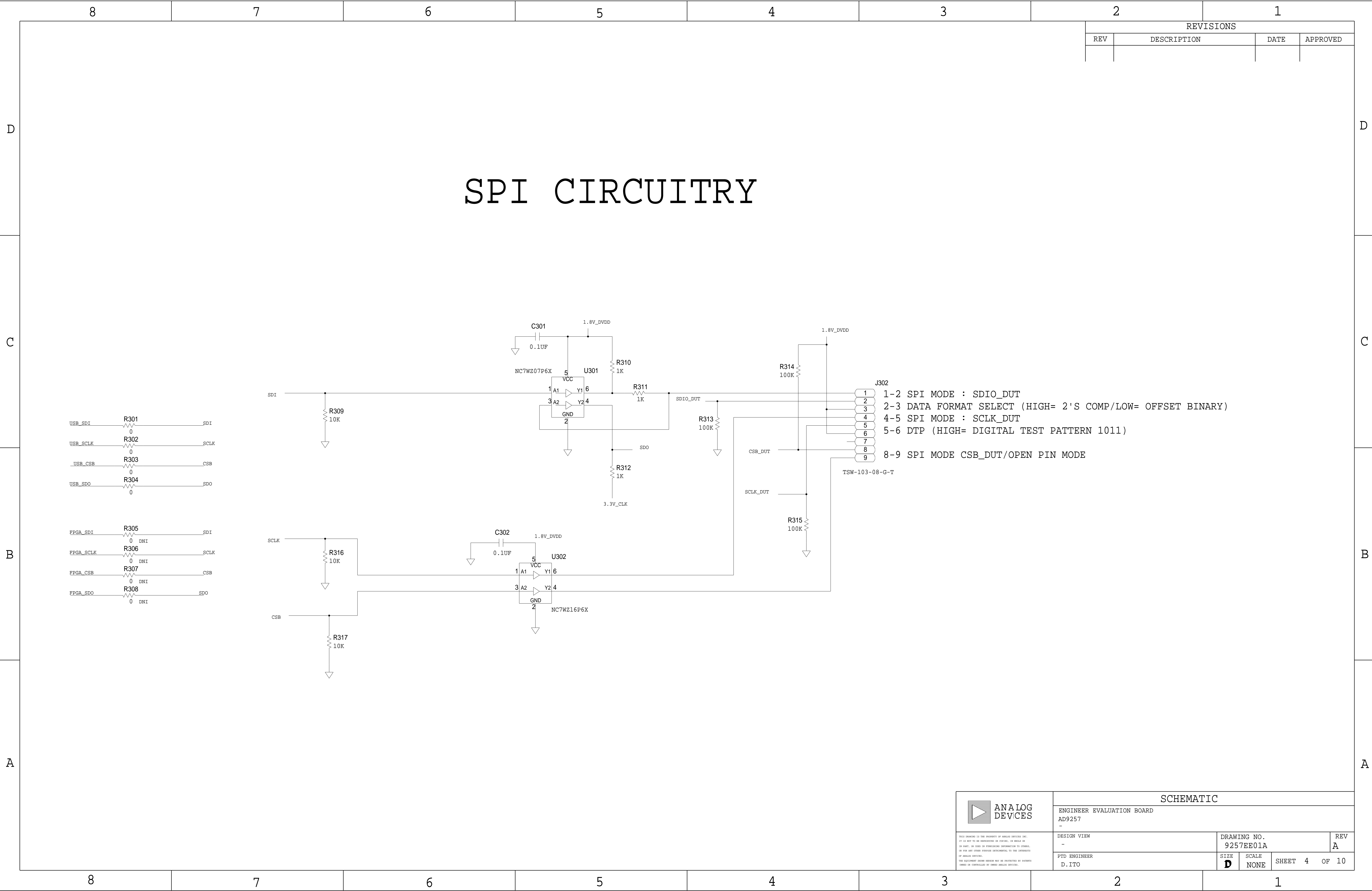
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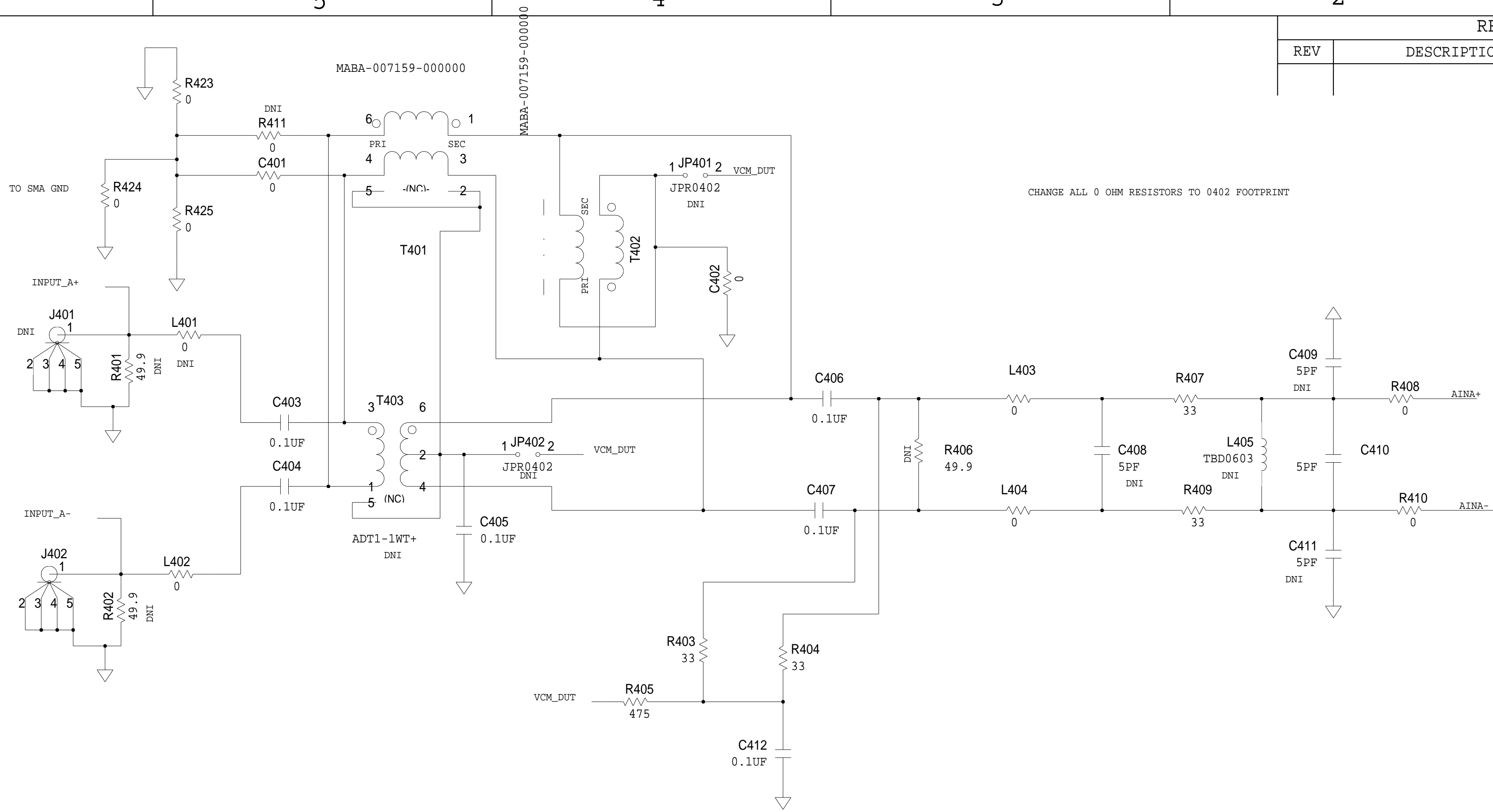
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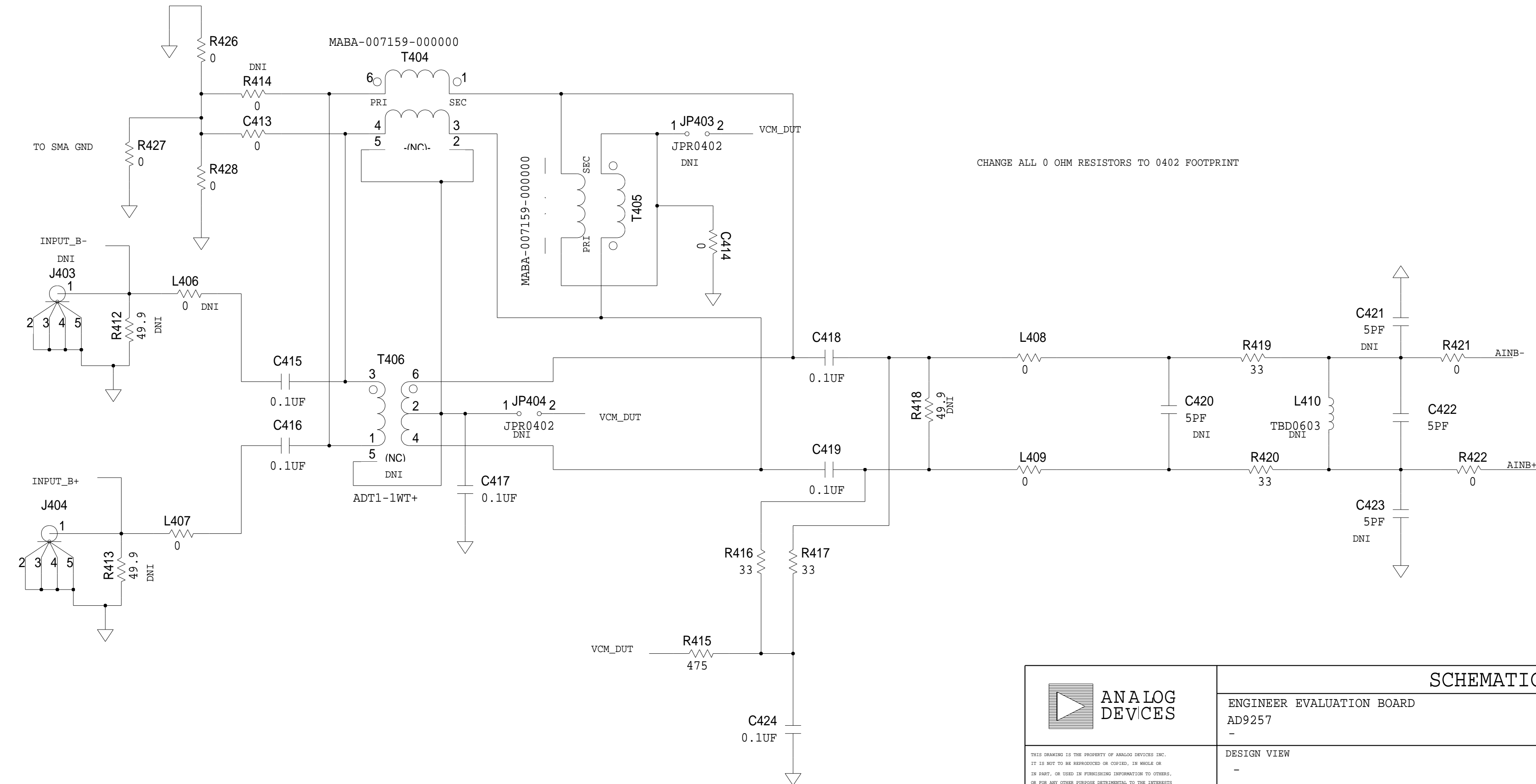


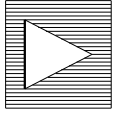


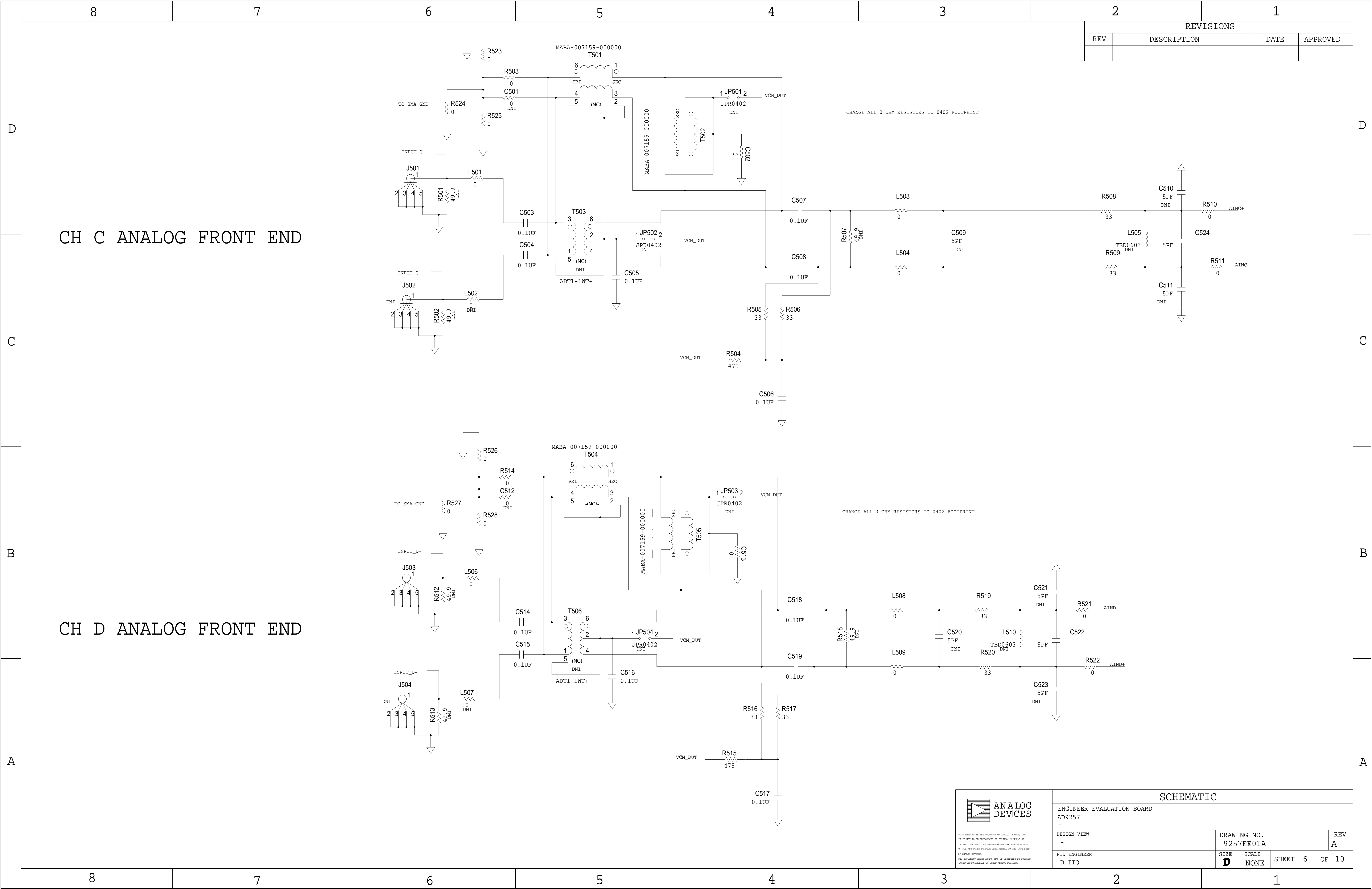
CH A ANALOG FRONT END

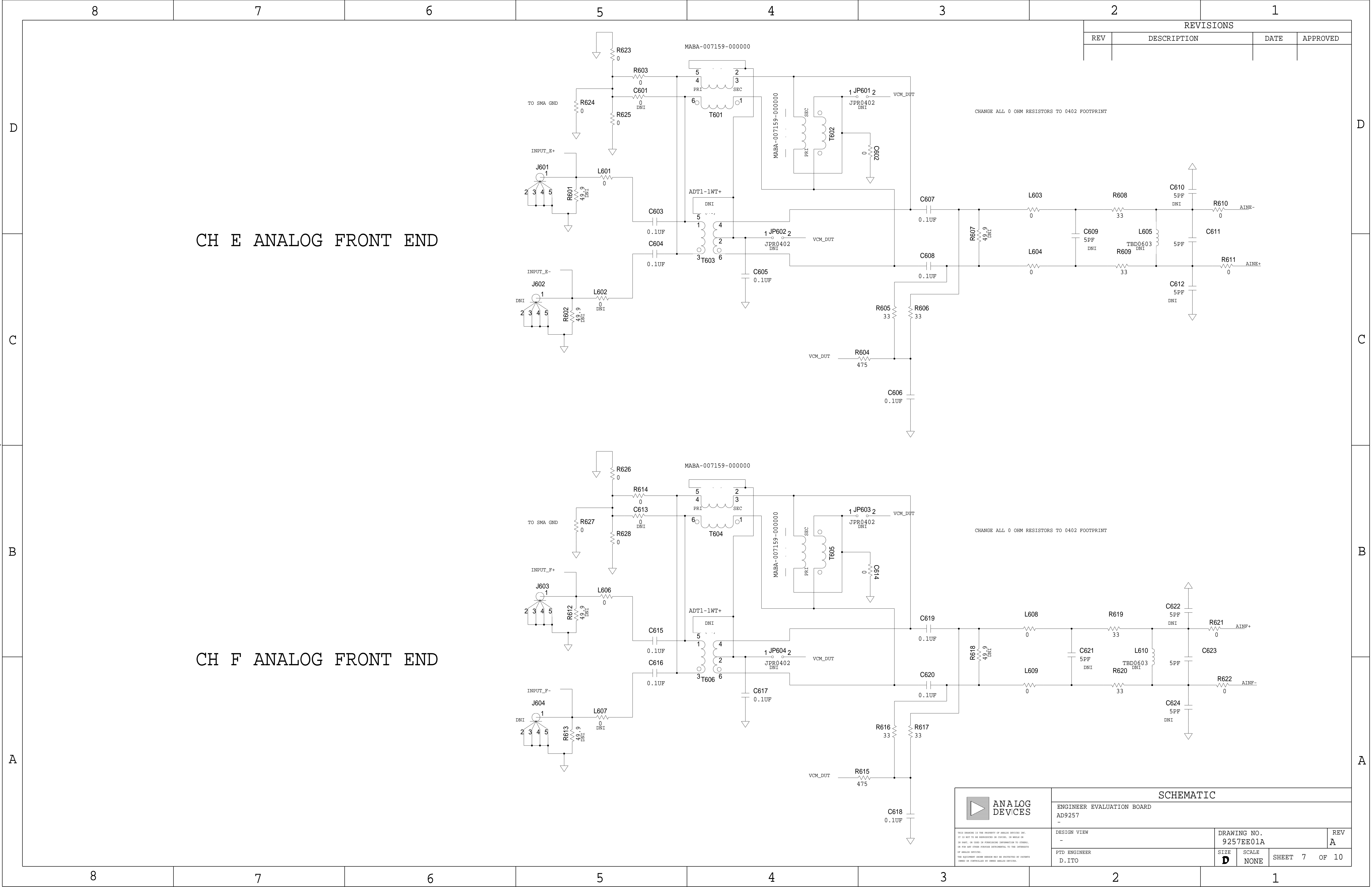


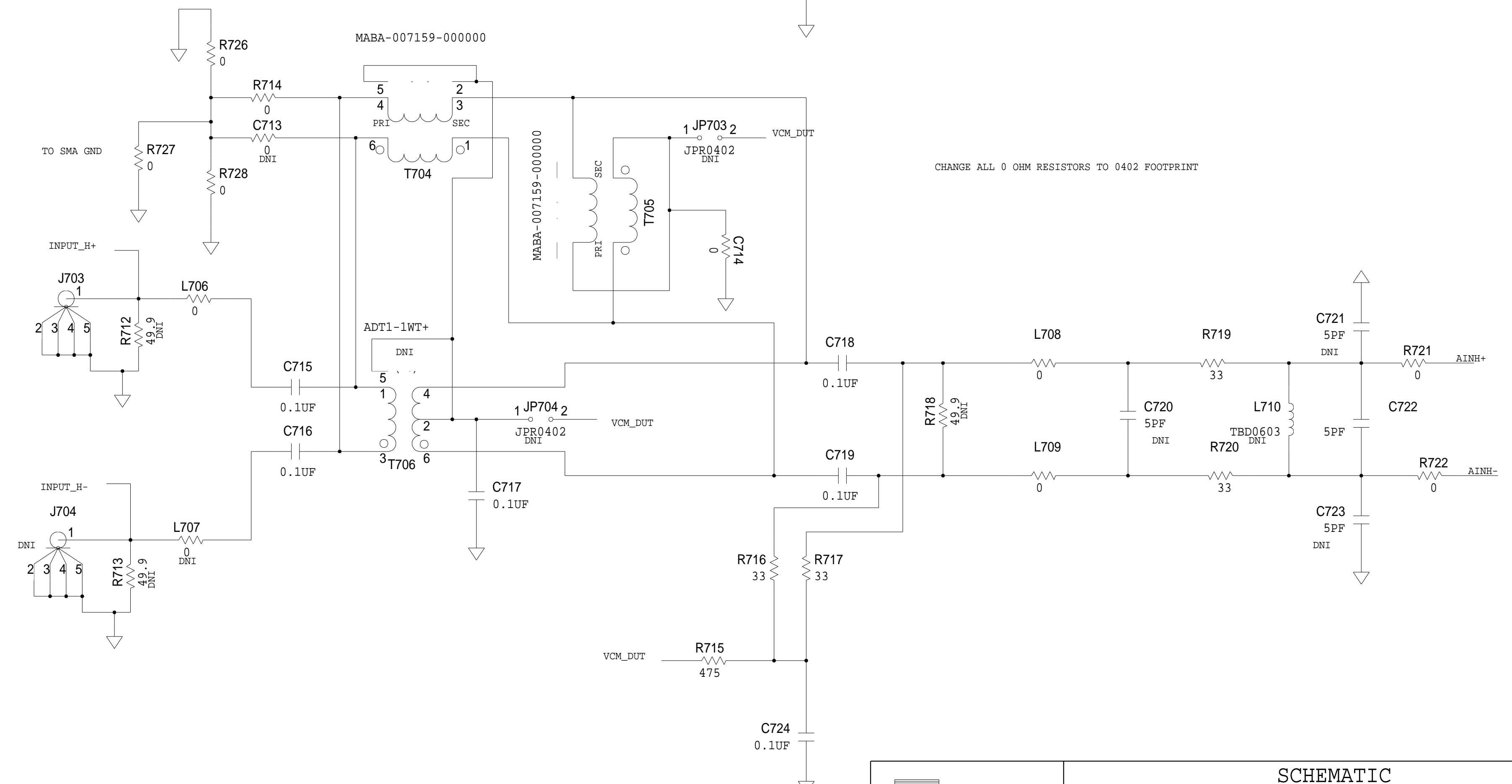
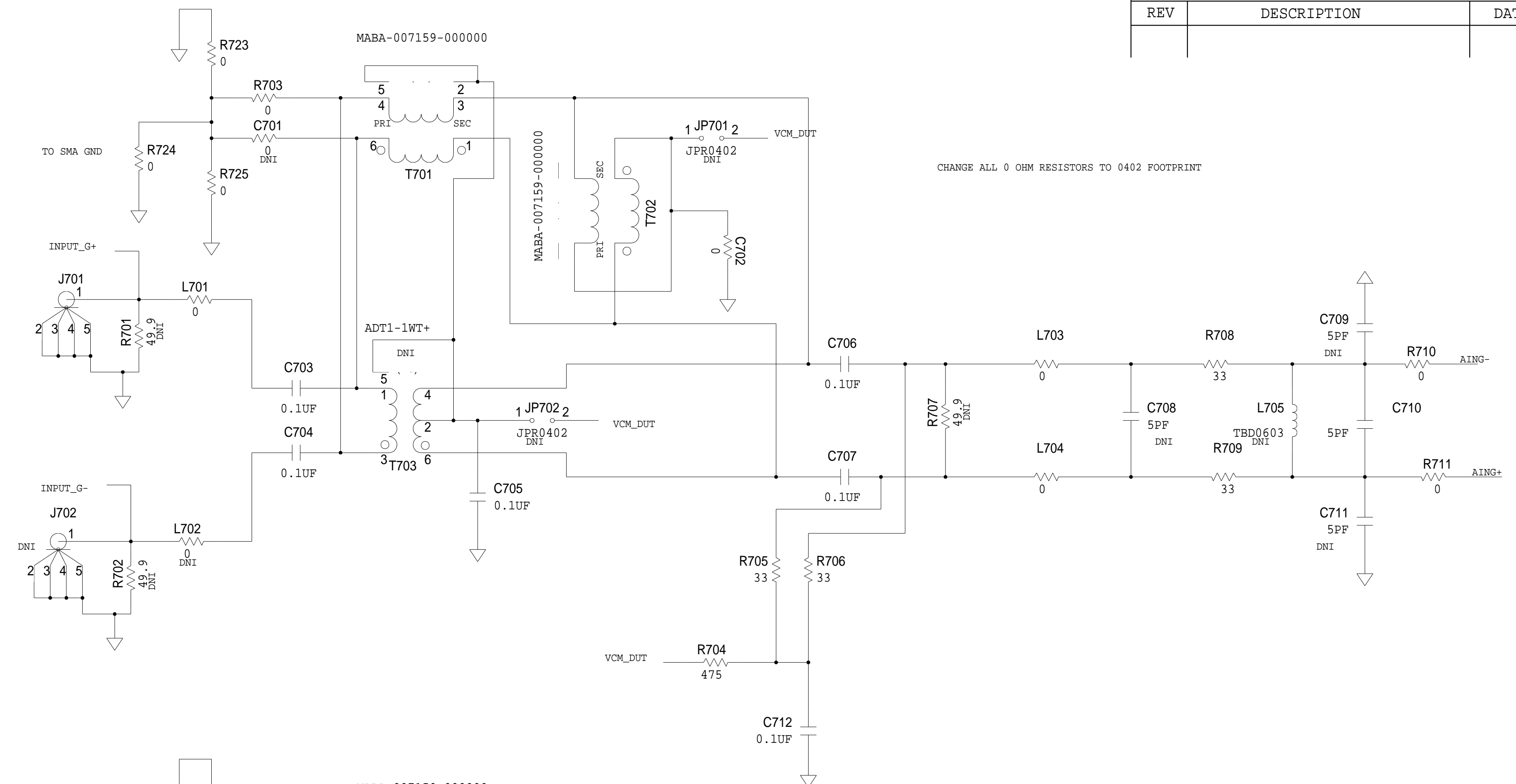
CH B ANALOG FRONT END




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	ENGINEER EVALUATION BOARD AD9257 -			
	DESIGN VIEW -	DRAWING NO. 9257EE01A		REV A
	PTD ENGINEER D. ITO	SIZE D	SCALE NONE	SHEET 5 OF 10

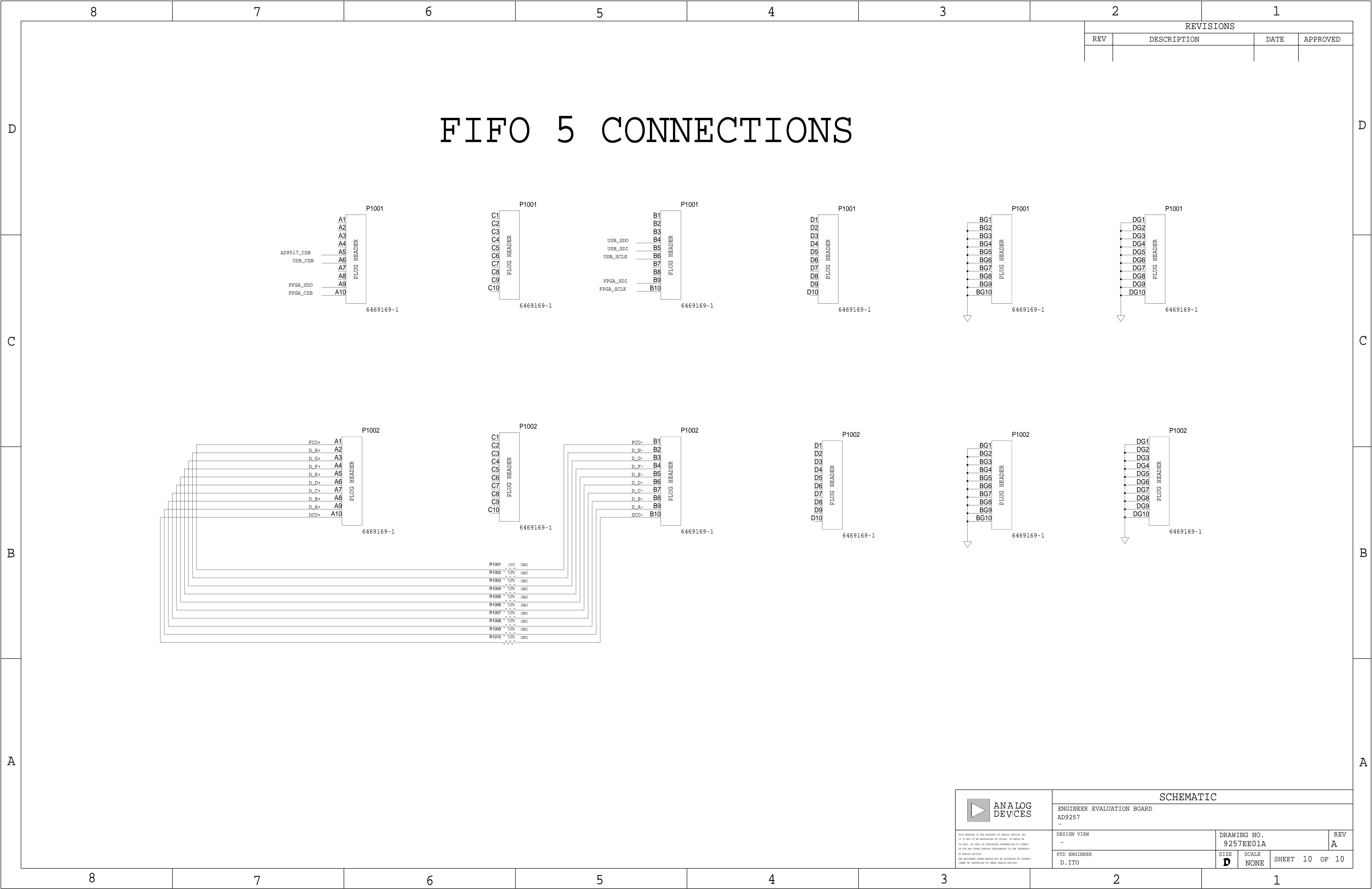






REVISIONS			
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	ENGINEER EVALUATION BOARD AD9257 -				
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	PTD ENGINEER D. ITO	SIZE D	SCALE NONE	SHEET 8 OF 10	



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PLUG HEADER

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PLUG HEADER

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PLUG HEADER

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D_H+

D_G+

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D_D+

D_C+

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D_A+

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ANALOG
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ENGINEER EVALUATION BOARD
AD9257
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SHEET 10 OF 10