

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Analog Devices			
TITLE: Eval Board Schematic			
CODE:	SIZE: A3	DRAWING NO: EVAL-AD7292SDZ	REV: A3
SCALE: not to scale		SHEET: 5	

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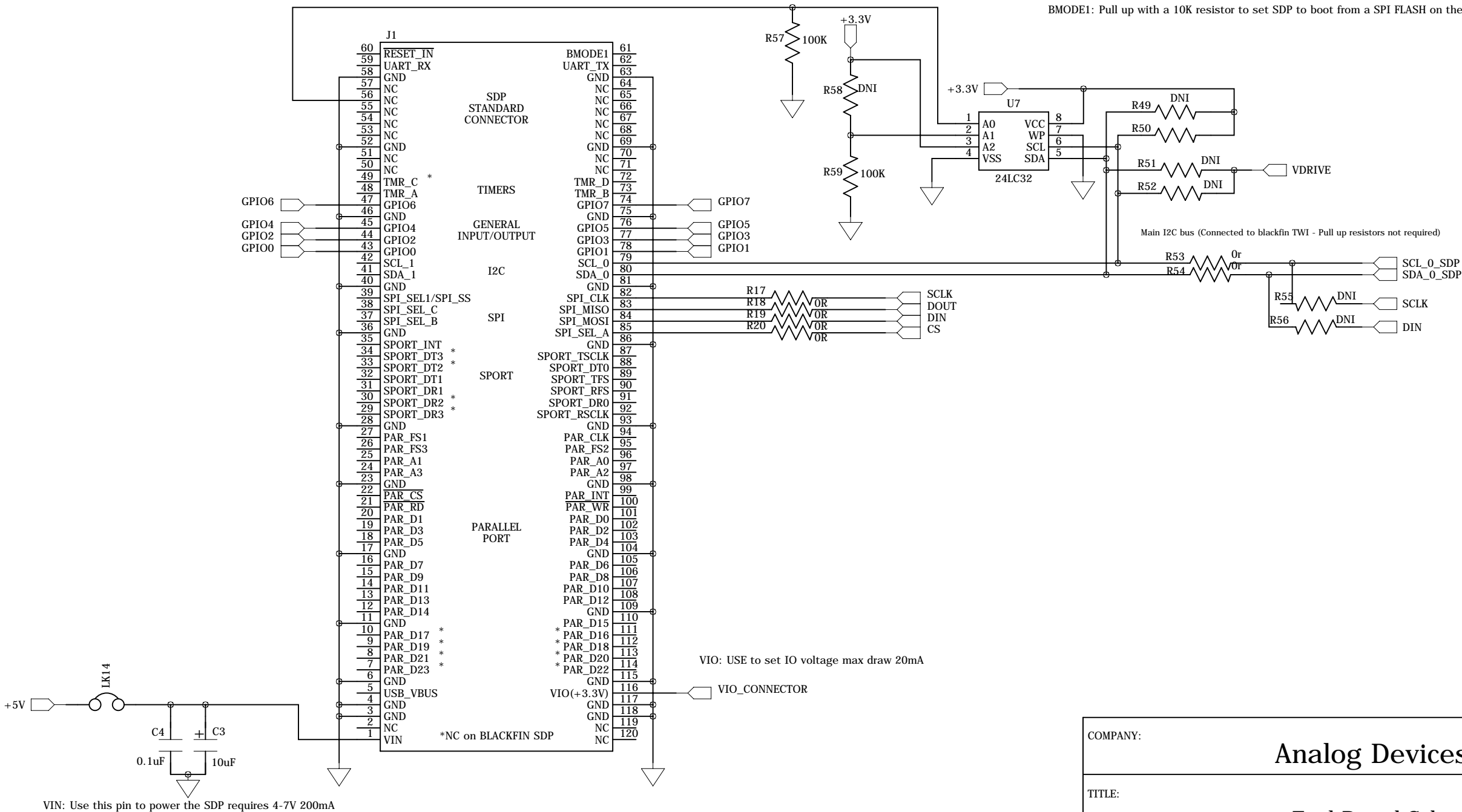
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VIN: Use this pin to power the SDP requires 4-7V 200mA

Board ID EEPROM (24LC64) must be on I2C bus 0

BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board

Main I2C bus (Connected to blackfin TWI - Pull up resistors not required)

VIO: USE to set IO voltage max draw 20mA

DRAWN:	Madden	DATED:	July 12
CHECKED:	Somers	DATED:	July 12
QUALITY CONTROL:		DATED:	
RELEASED:		DATED:	

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Analog Devices			
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	A3	EVAL-AD7292SDZ	A3
SCALE: not to scale			SHEET: 20F 5

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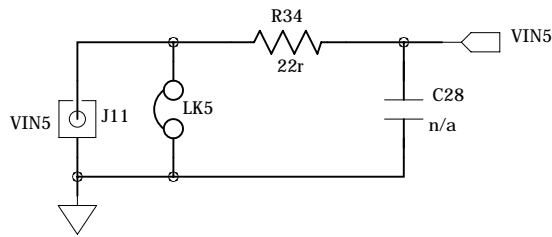
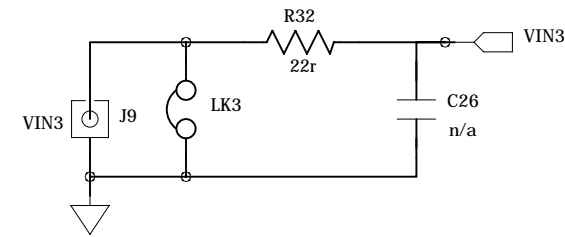
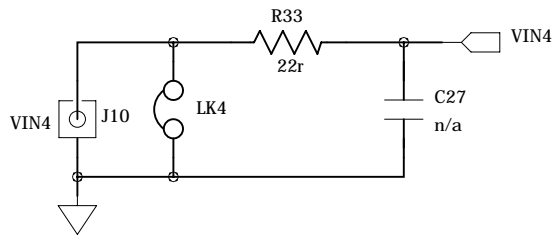
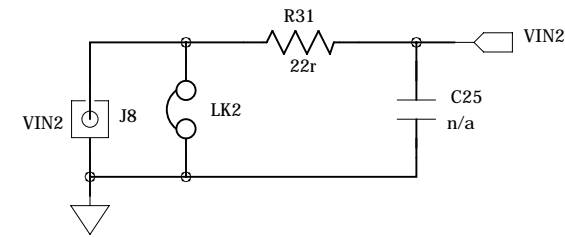
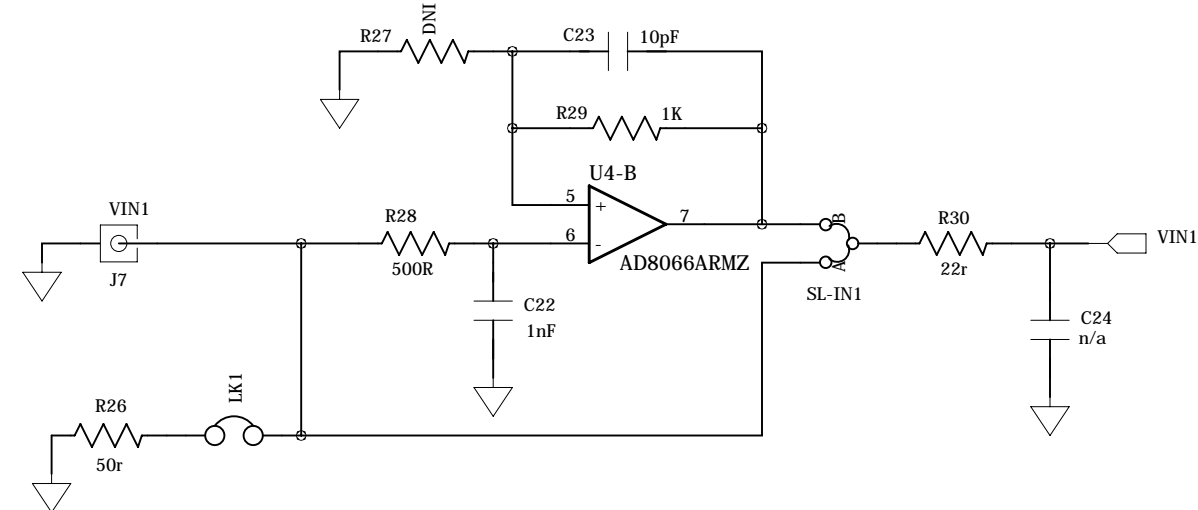
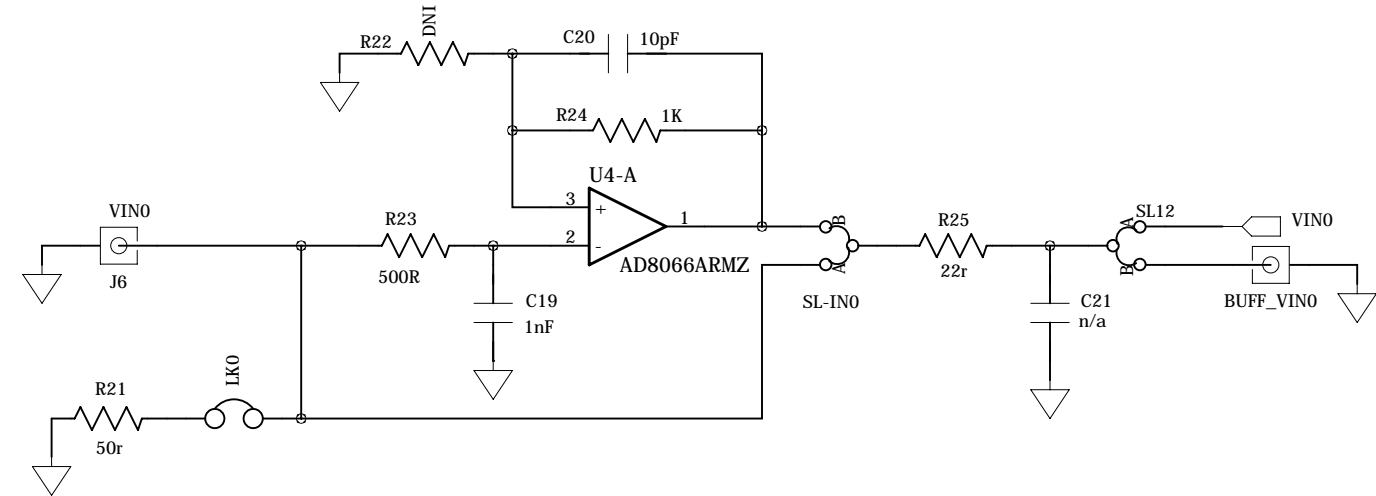
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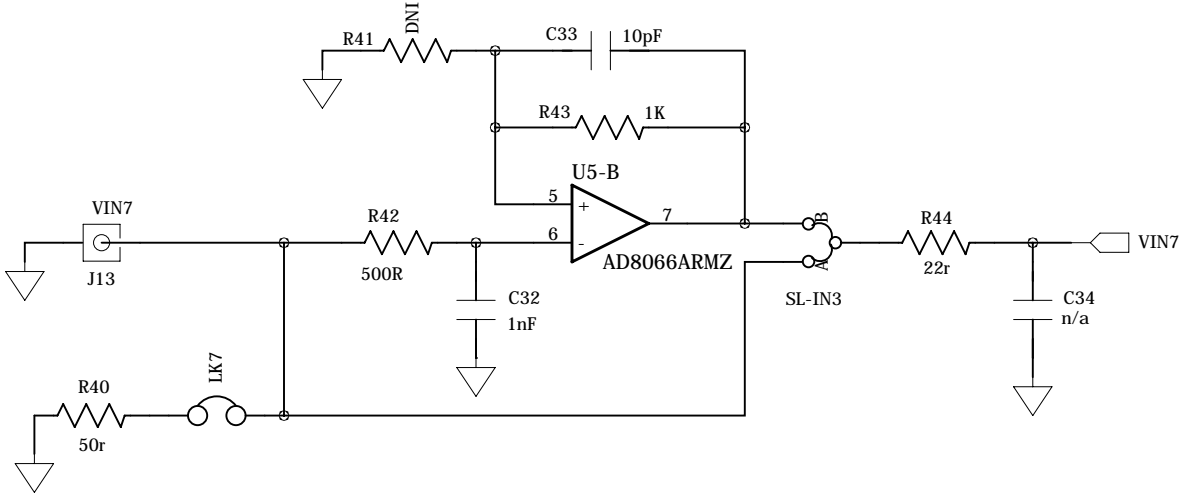
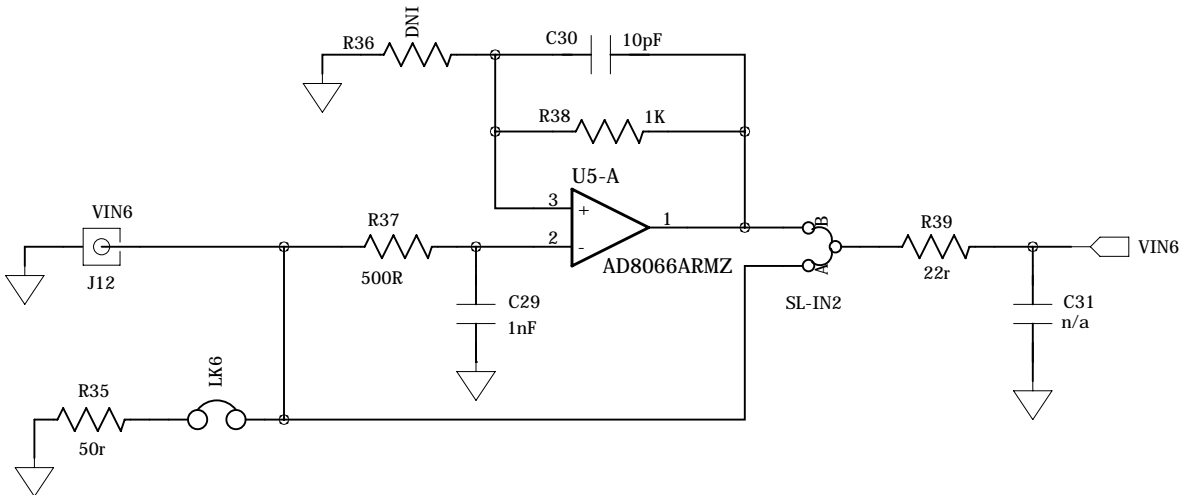
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DRAWN: Madden	DATED: July 12
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QUALITY CONTROL: .	DATED:
RELEASED:	DATED:

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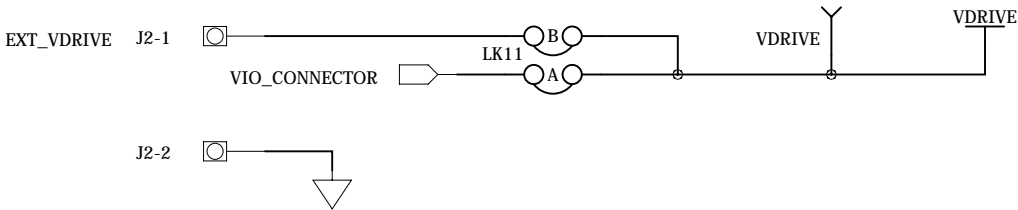
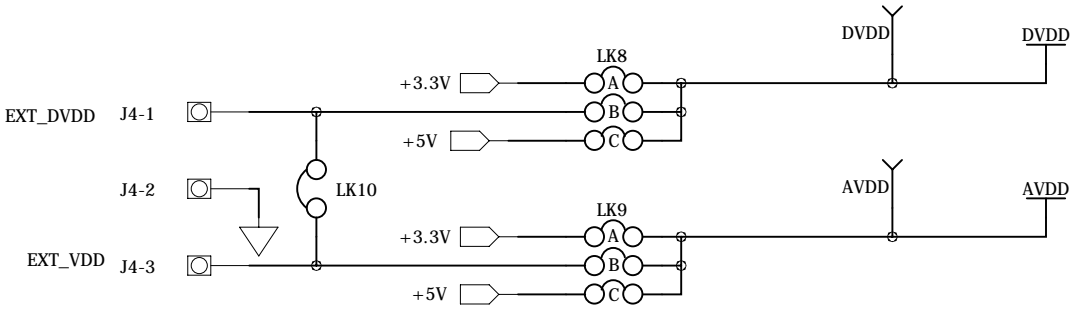
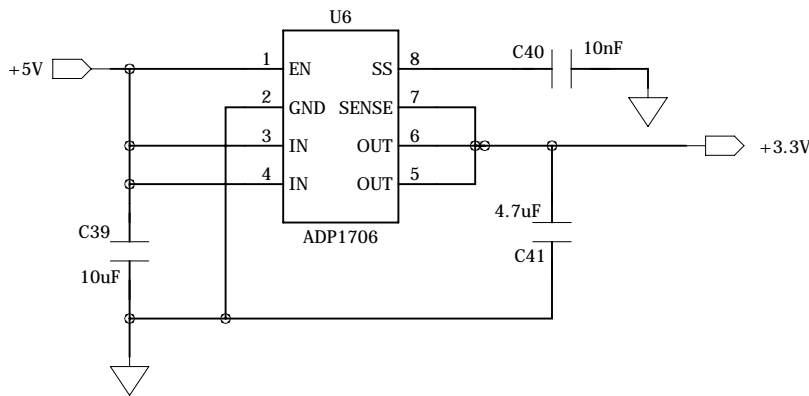
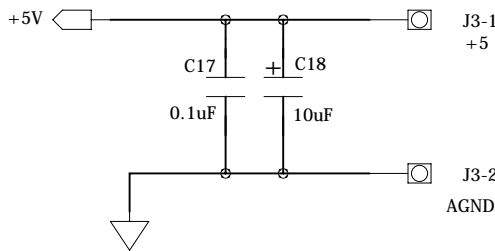
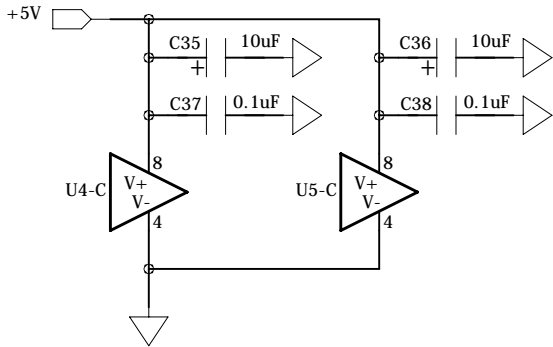
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DRAWN: Madden	DATED: July 12
CHECKED: Somers	DATED: July 12
QUALITY CONTROL: .	DATED:
RELEASED:	DATED:

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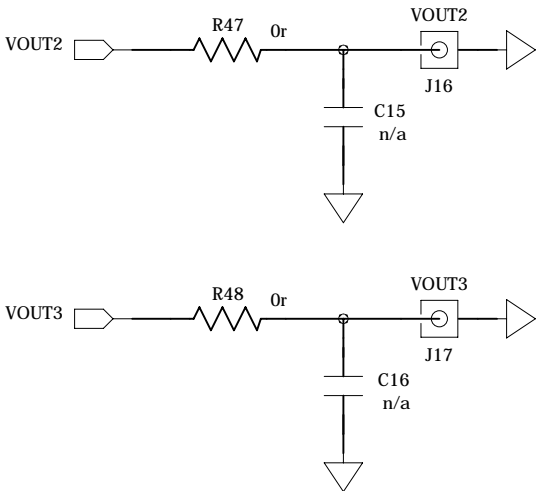
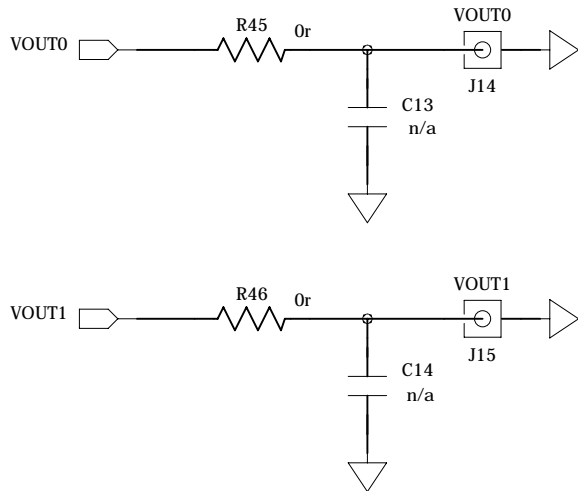
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RELEASED:	DATED: