

REV	DESCRIPTION	DATE	APPROVED

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- NOTE 1** The full-scale DAC output current is controlled by means of an external resistance (Rset) connected between the DAC Reset pin and ground. Resistor values for full-scale currents are 7.87k ohms = 5.0ma, 15.8k ohms = 2.5ma.
- NOTE 2** C33 is used for a simulated capacitance load when the DAC is in reset. This capacitance value should not exceed 10pF.
- NOTE 3** Capacitors C48 and C49 should be soldered in place when the DAC is in reset. Capacitors C35 and C36 should be soldered in place when crystal X1 is used in conjunction with the internal oscillator.
- NOTE 4** Use either resistors R4 and R5 (Comparator inputs) or R6 and R7 (Filtered Output) for the out end output. Do not use both sets at the same time.

**ANALOG DEVICES**

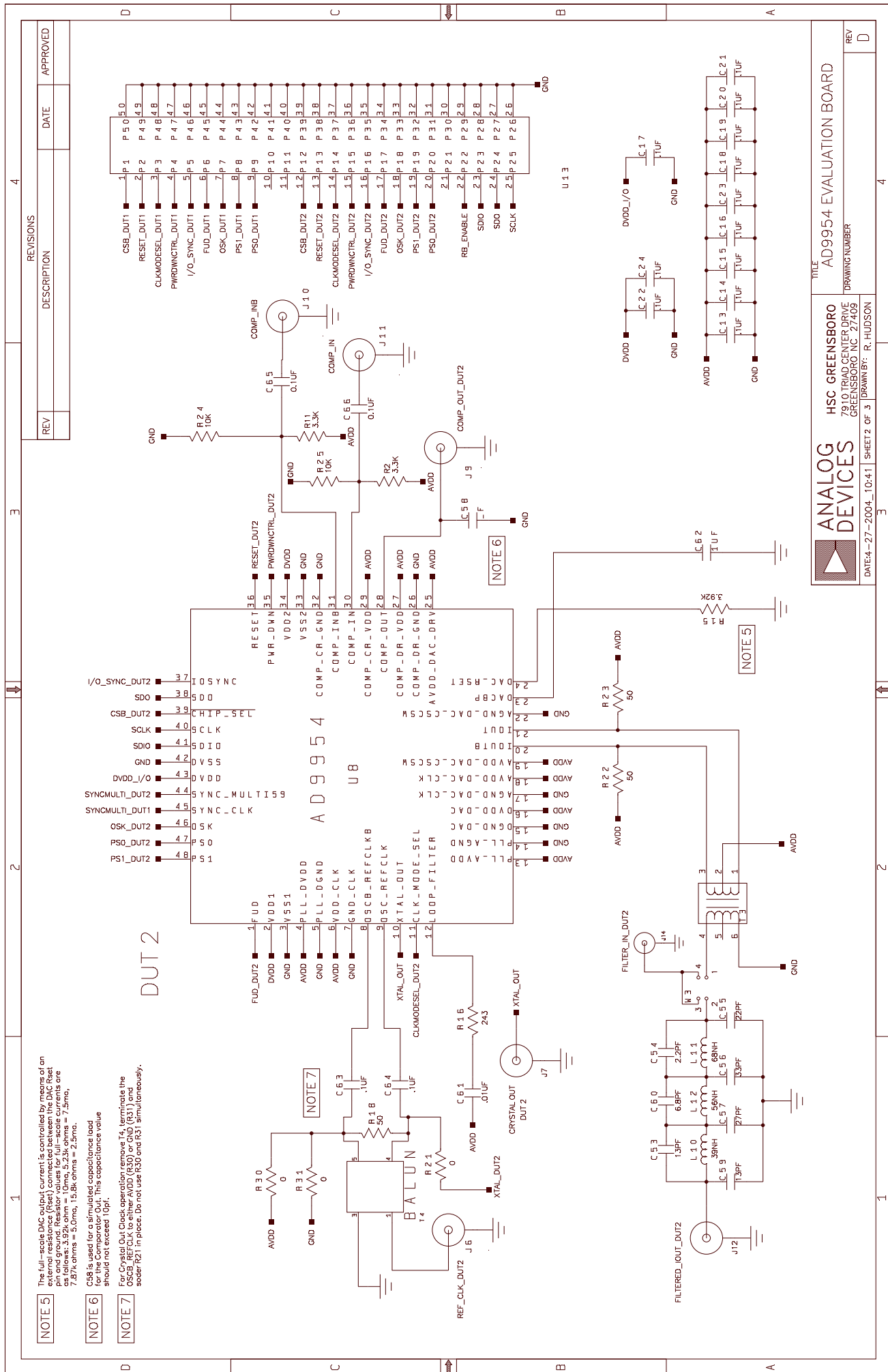
HSC GREENSBORO  
7010 TRAD CENTER DRIVE  
GREENSBORO, NC 27409

DATE: 4-23-2004 9:26 SHEET 1 OF 3 DRAWN BY: R. HUDSON

TITLE: AD9954 EVALUATION BOARD

DRAWING NUMBER

REV D



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**NOTE 5** The full-scale DAC output current is controlled by means of an external resistance (Rset) connected between the DAC Reset pin and ground. Resistor values for full-scale currents are: 7.5mA, 15.8k ohms; 7.5mA, 15.8k ohms; 2.5mA, 7.87k ohms = 5.0mA, 15.8k ohms = 2.5mA.

**NOTE 6** CSB is used for a simulated capacitance load and should not be used for a real load. This capacitance value should not exceed 10pF.

**NOTE 7** For Crystal Out Clock operation, remove the 10k resistor (R1B) and solder R21 in place. Do not use R30 and R31 simultaneously.

**ANALOG DEVICES**

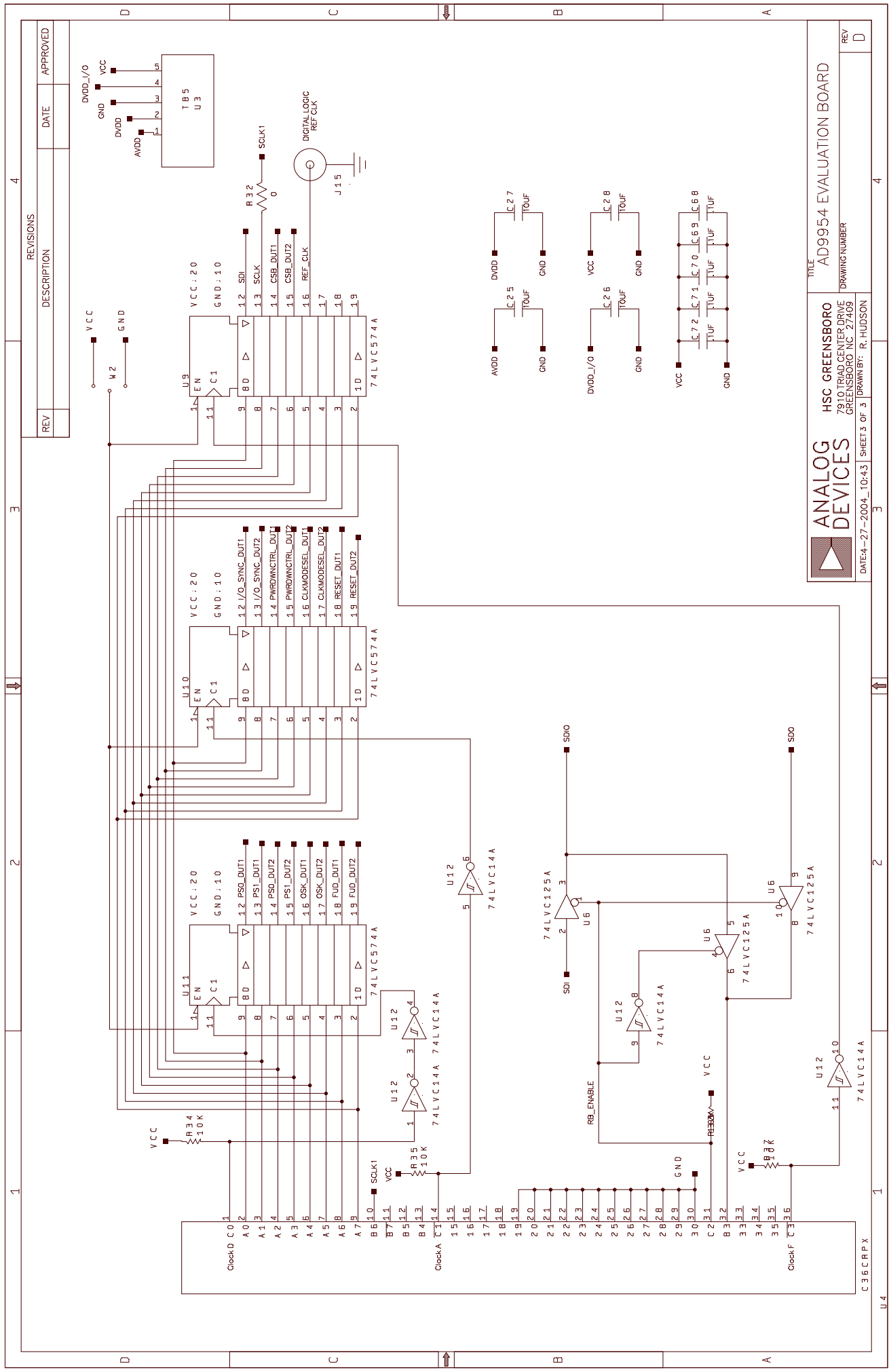
HSC GREENSBORO  
7010 TRAD CENTER DRIVE  
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DATE: 4-27-2004 10:41 SHEET 2 OF 3 DRAWN BY: R. HUDSON

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REV: D



REV	DESCRIPTION	DATE	APPROVED

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DATE: 4-27-2004 10:43 | SHEET 3 OF 3 | DRAWN BY: R. HUDSON

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ANALOG DEVICES

7010 TRIM CENTER DRIVE  
GREENSBORO, NC 27409

DATE: 4-27-2004 10:43 | SHEET 3 OF 3 | DRAWN BY: R. HUDSON

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ANALOG DEVICES

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