

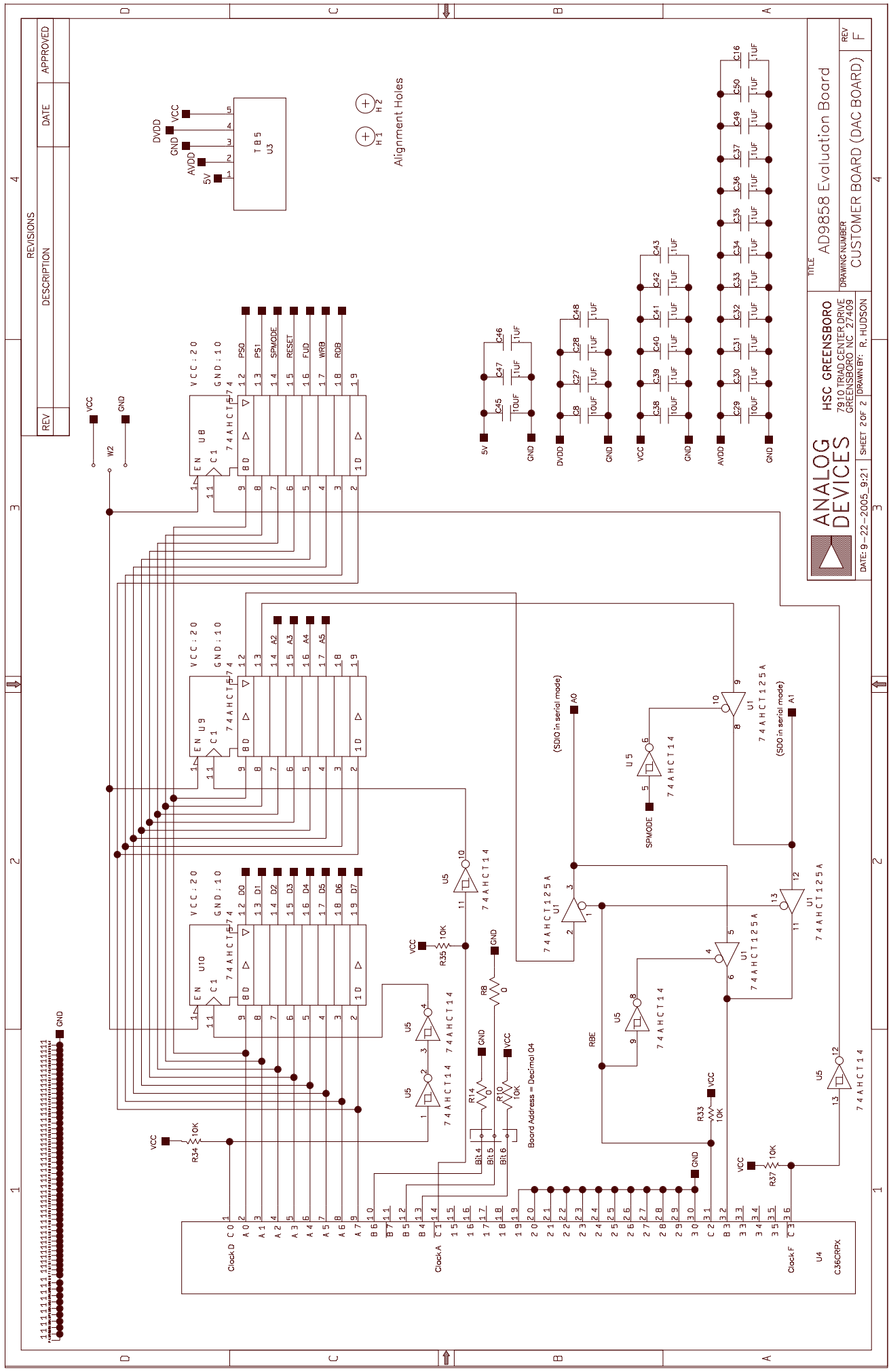
REV	DESCRIPTION	DATE	APPROVED

**DDS Analog Output**  
**PHASE Frequency Detector with Charge Pump**  
**Analog MIXER**

D7	D6	D5	D4	D3	D2	D1	D0	A5	A4	A3	A2	A1	A0	SPMODE	RESET	DVDD_4	DVDD_5	DGND4	DGND5	PS0	PS1	FUD	SNCCCLK_OUT																						
DIODE2	GNDCOMP	VCCCOMP	COMP2INB	COMP2IN	GNDPDET	VDDPDET	GNDCP2	VCCCP2	CPOUT1	CPOUT2	CPOUT3	GNDCP1	VCC_CP1	CPRESET	NC4	NC3	COMP1IN	COMP1NB	IFB	IF	RF	RFB	GND_MIX	VDD_MIX																					
GND_LO4	GND_LO3	VDD_LO4	VDD_LO3	LO	LOB	VDD_LO2	VDD_LO1	GND_LO	GND_LO	VCC_DAC1	AGND_DAC	GND_CLK4	GND_CLK3	VDD_CLK4	VDD_CLK3	VDD_CLK2	VDD_CLK1	GND_CLK1	DGND_10	DGND_11	DVDD_11	DVDD_12	GND_LO4	GND_LO3	VDD_LO4	VDD_LO3	LO	LOB	VDD_LO2	VDD_LO1	GND_LO	GND_LO	VCC_DAC1	AGND_DAC	GND_CLK4	GND_CLK3	VDD_CLK4	VDD_CLK3	VDD_CLK2	VDD_CLK1	GND_CLK1	DGND_10	DGND_11	DVDD_11	DVDD_12

**ANALOG DEVICES**  
**HSC GREENSBORO**  
**7010 TRIAD CENTER DRIVE**  
**GREENSBORO, NC 27409**  
**AD9858 Evaluation Board**  
**CUSTOMER BOARD (DAC BOARD)**

DATE: 9-23-2005\_13:32 SHEET 1 OF 2 DRAWN BY: R. HUDSON



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