1V8_MIC TO USE MICROPHONE
LOAD SWITCH AND TURN ON
CONFIGURE LDO1 OF PMIC AS STEREO LINE OUT

I2C ADDRESS: 0X30 - 0X31
I2S IS BY DEFAULT: SLAVE
LRCLK AND BCLK ARE INPUTS SINGLE ENDED

12.288MHZ CLOCK IS SHARED BY THE CODEC AND AUDIO MAX78000

STEREO LINE IN
3.5MM AUDIO JACK

MAX9867EWV+
I2S_SDI
P0_13_INTCDC
I2C1_SDA
I2C1_SCL
I2S_SCK/RV_TDI
I2S_WS/RV_TDI
I2S_SDO/RV_TDO

SPH0645LM4H-B
I2S_SDI

CONFIGURE LDO1 OF PMIC AS LOAD SWITCH AND TURN ON 1V8_MIC TO USE MICROPHONE

WS
OPERATION
0
DRIVE DATA
1
DATA TRI-STATE

3.5MM AUDIO JACK
STEREO LINE OUT SINGLE ENDED

GND REF NEAR AUDIO JACK
DG ADDRESS: EX33 - EX01
DG IS BY DEFAULT SLAVE
LRCLK AND BCLK ARE INPUTS

LINE IN LEFT
LINE IN RIGHT
STEREO LINE IN 3.5MM AUDIO JACK

Configurations and connections for audio circuits involving MAX78000 and other components are detailed in this diagram.