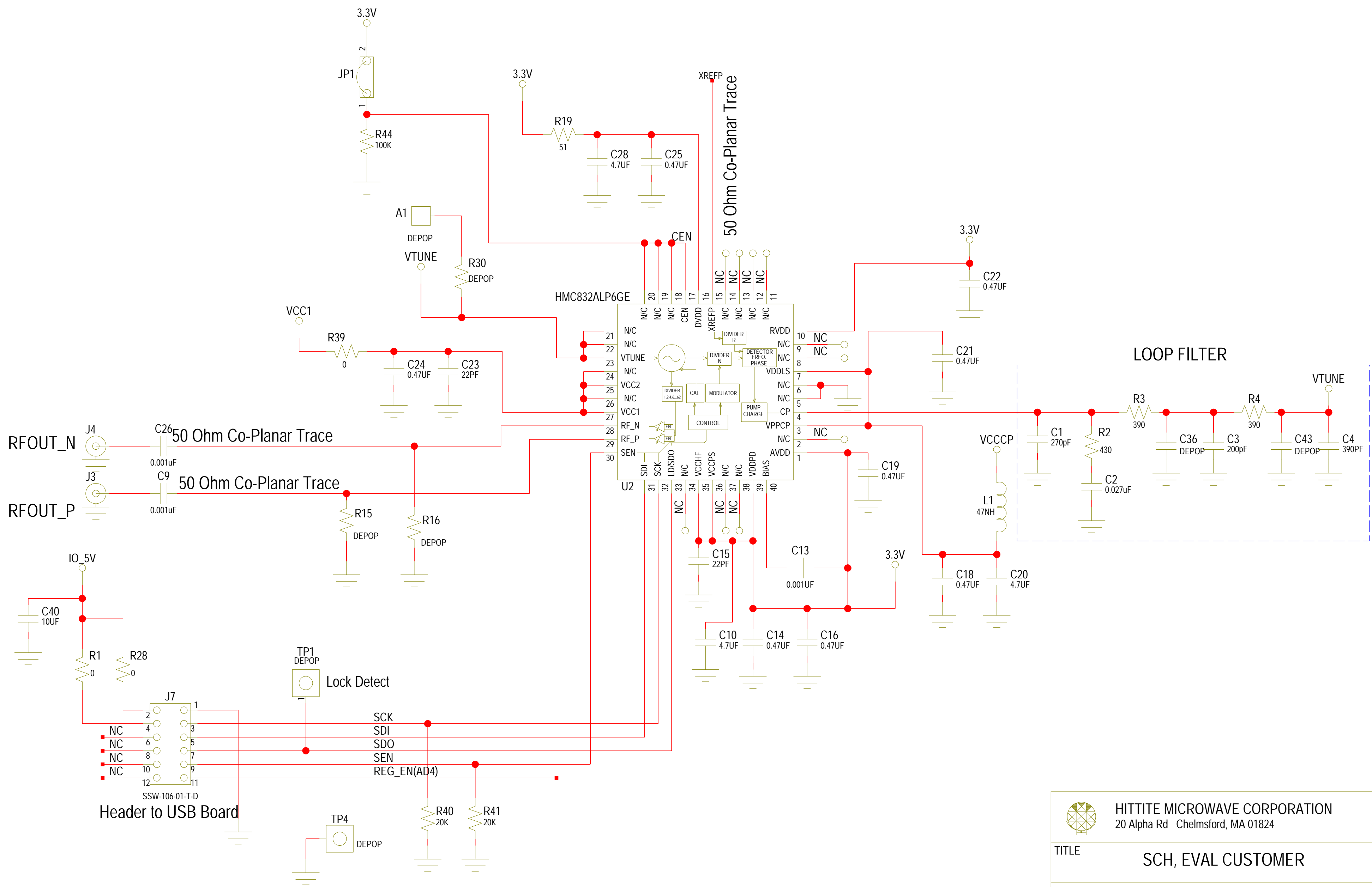



REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
A	CP130788	----	PRODUCTION RELEASE	D. ACEVAL	05/27/2013
B	CP131245	----	CHAGE AS PER CP131245	D. ACEVAL	08/20/2013

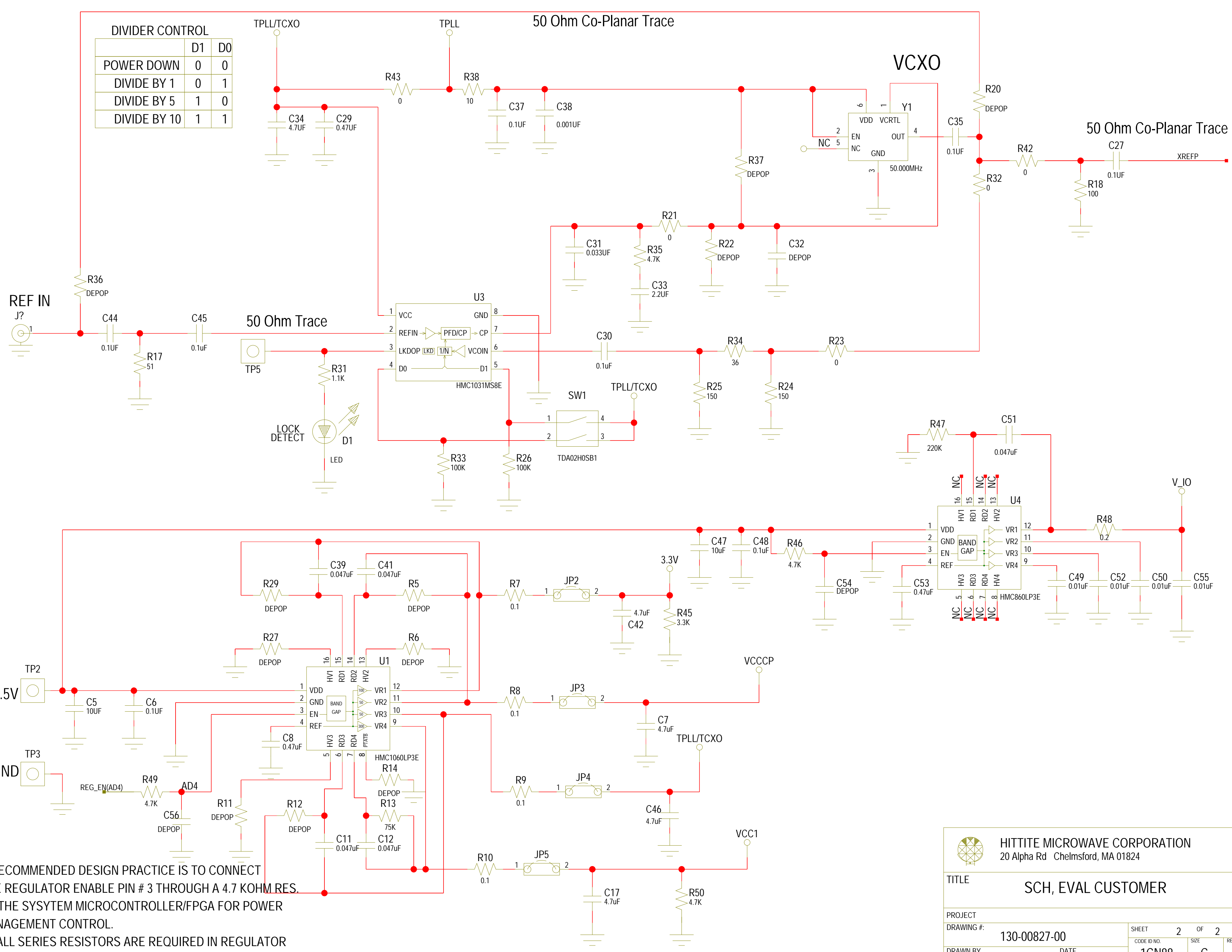


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 HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824	
TITLE SCH, EVAL CUSTOMER	
PROJECT DRAWING #: 130-00827-00	
SHEET 1 OF 2 CODE ID NO. 1CN88 SIZE C REV B	
DRAWN BY: D. ACEVAL DATE: 11/18/10	20/08/2013:10:49

DIVIDER CONTROL

	D1	D0
POWER DOWN	0	0
DIVIDE BY 1	0	1
DIVIDE BY 5	1	0
DIVIDE BY 10	1	1



A RECOMMENDED DESIGN PRACTICE IS TO CONNECT THE REGULATOR ENABLE PIN # 3 THROUGH A 4.7 KOHM RES. TO THE SYSYEM MICROCONTROLLER/FPGA FOR POWER MANAGEMENT CONTROL. SMALL SERIES RESISTORS ARE REQUIRED IN REGULATOR OUTPUT PATHS AS SHOWN

HITTITE MICROWAVE CORPORATION
 20 Alpha Rd Chelmsford, MA 01824

TITLE: SCH, EVAL CUSTOMER

PROJECT: 130-00827-00

DRAWING #: 130-00827-00

DRAWN BY: D. ACEVAL DATE: 11/18/10

SHEET 2 OF 2

CODE ID NO. 1CN88 SIZE C REV B

20/08/2013:10:42