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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

# EVAL-AD4113-ARDZ SCHEMATIC REV (B)

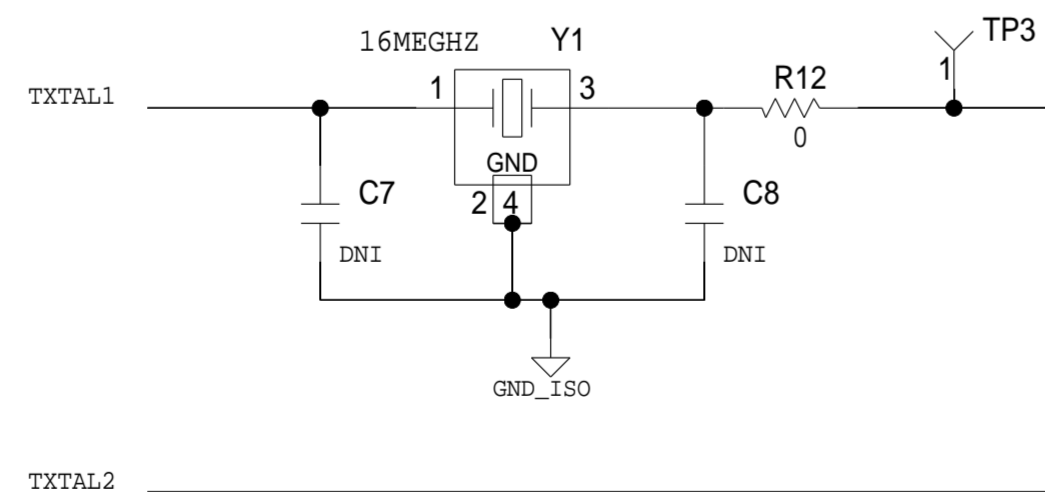
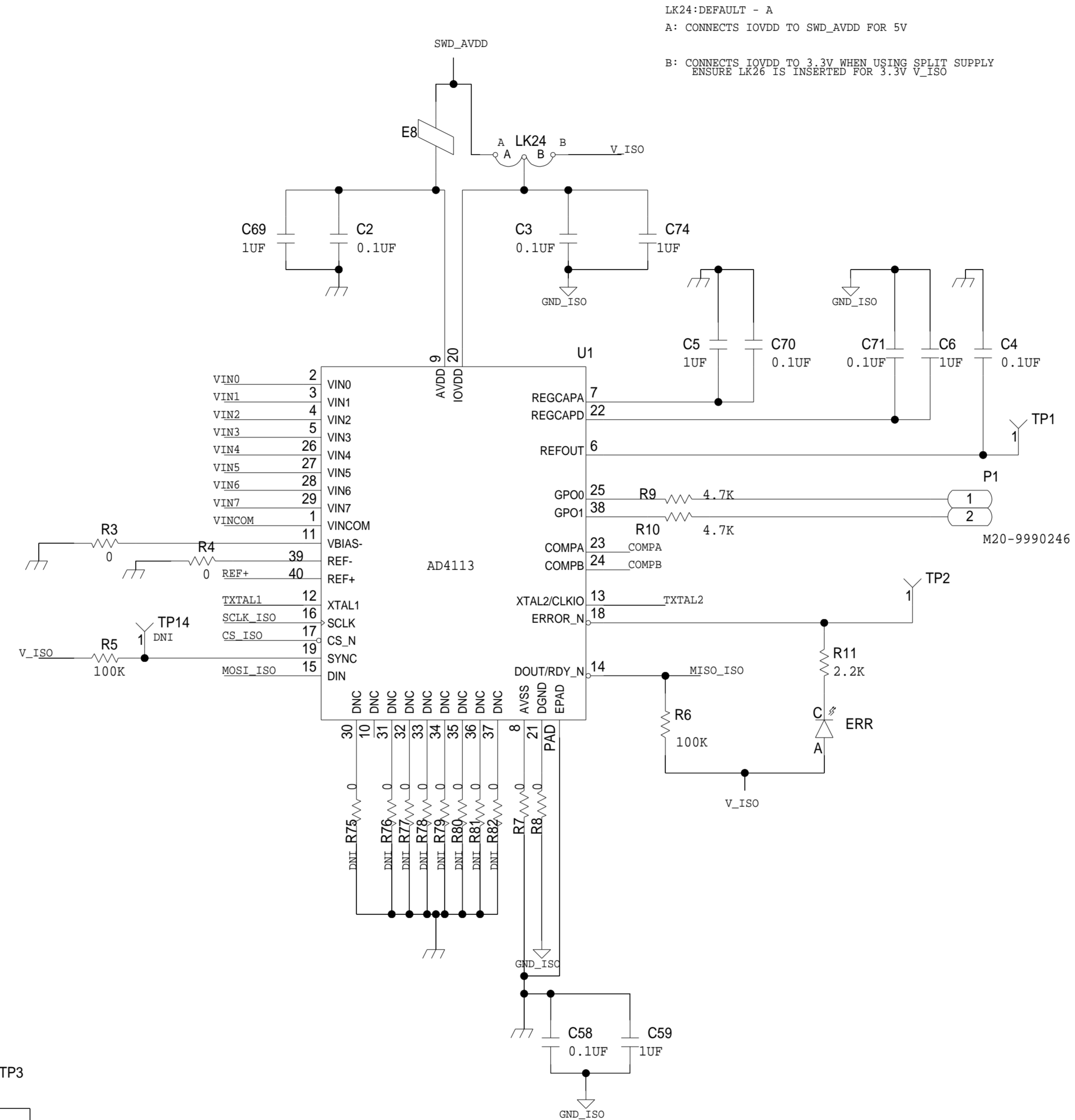
- PAGE 2 - AD4113 CIRCUITRY
- PAGE 3 - ANALOG INPUT PROTECTION
- PAGE 4 - ARDUINO UNO HEADER
- PAGE 5 - VOLTAGE REFERENCE
- PAGE 6 - POWER CIRCUITRY

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER -	DATE	<b>SCHEMATIC</b>		
HARDWARE SERVICES -				
HARDWARE SYSTEMS -		HW TYPE : Customer Evaluation		
TEST ENGINEER -		Product(s): AD4113		
COMPONENT ENGINEER -		: ADR4525		
TEST PROCESS -		PACKAGE : LeadCount-lead BodySize PackageType-family		
HARDWARE RELEASE -		: Pitch-pitch StyleVendor Style		
DESIGNER -		MASTER PROJECT TEMPLATE TBD	TESTER TEMPLATE 064489_c	DRAWING NO. 02_072892
PTD ENGINEER -		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		REV. B
CHECKER -		TOLERANCES		
		DECIMALS X.XX +0.010 X.XXX +0.005	FRACTIONS +1/32	ANGLES +2
		SIZE <b>D</b>	SCALE 1:1	CODE ID NO. CodeID
				SHEET 1 OF 6

# ADC CORE

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Product(s) : AD4113 : ADR4525		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_072892	REV B	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 2 OF 6

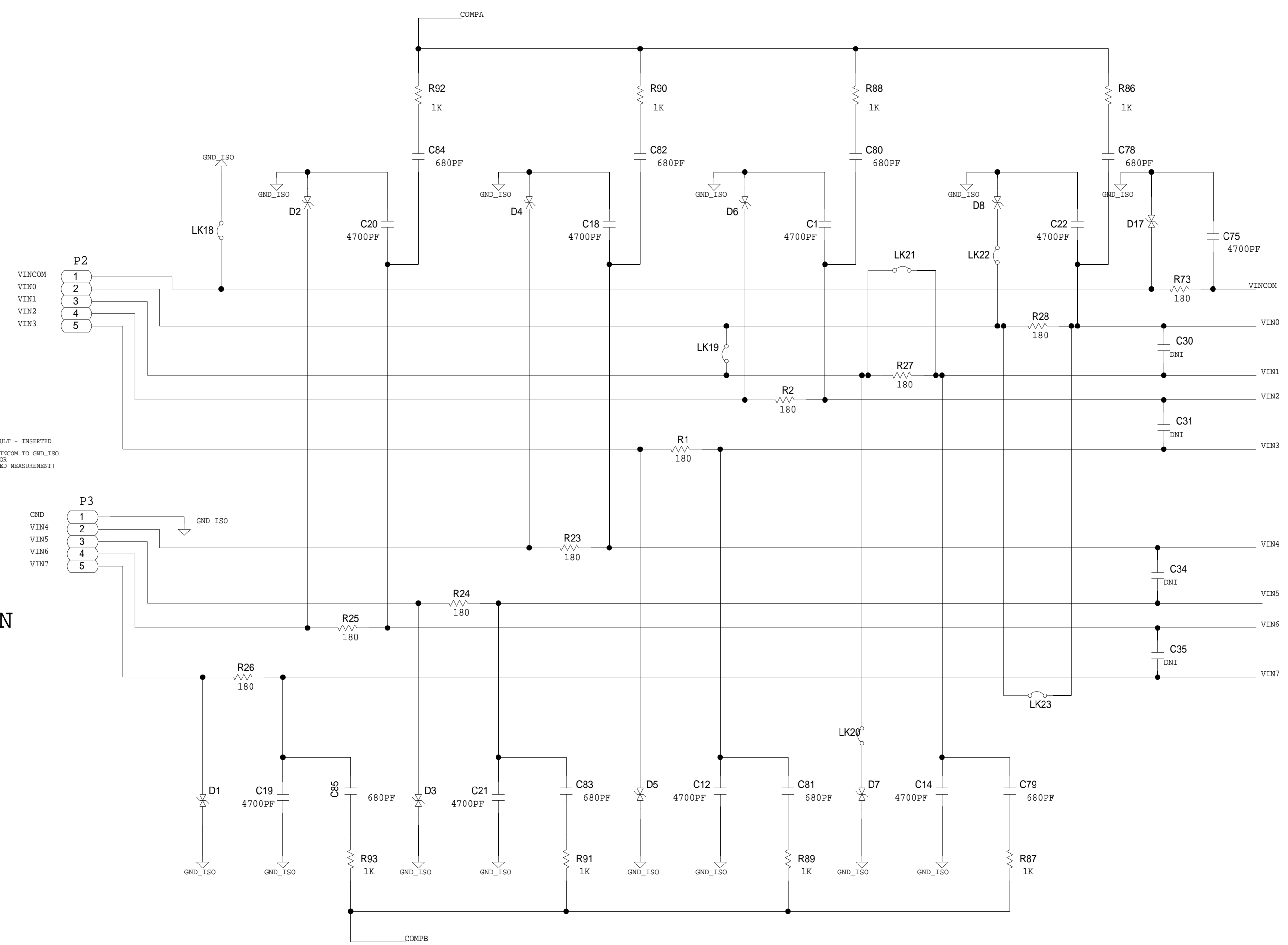
# INPUT PROTECTION

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FOR NOISE TEST: THIS ALLOWS THE ADC TO BE EVALUATED DIRECTLY  
 LK19: DEFAULT - INSERTED  
 SHORTS THE TWO INPUTS FOR NOISE TEST CONFIGURATION  
 LK20 AND LK22: DEFAULT - REMOVED  
 DISCONNECTS THE TVS DIODE FROM THE SIGNAL INPUT PATH  
 LK21 AND LK23: DEFAULT - INSERTED  
 INSERTED TO BYPASS THE RESISTOR

STANDARD VIN

LK18: DEFAULT - INSERTED  
 CONNECTS VINCOM TO GND\_ISO  
 (TYPICAL FOR SINGLE ENDED MEASUREMENT)

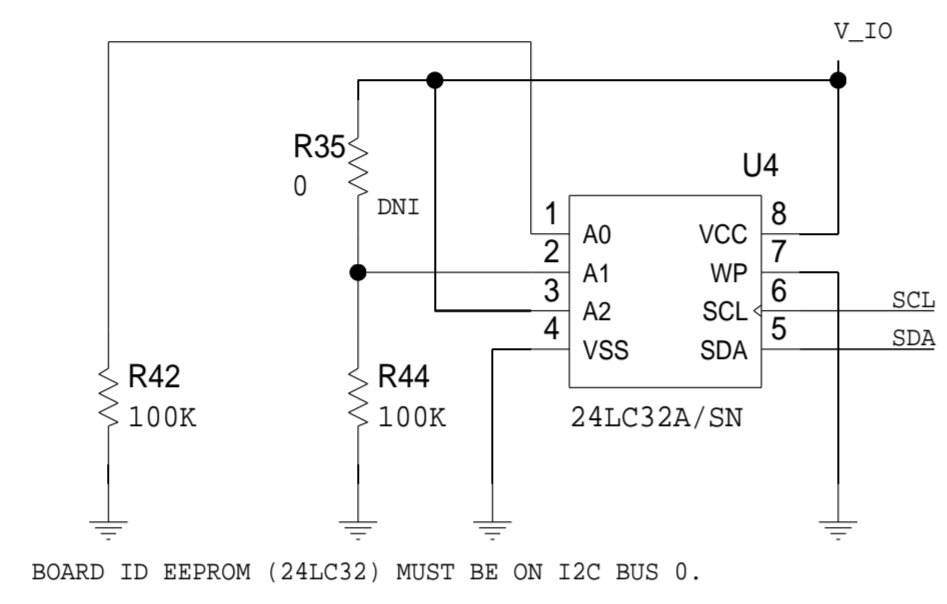


	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Product(s) : AD4113 : ADR4525		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_072892	REV B	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 3 OF 6

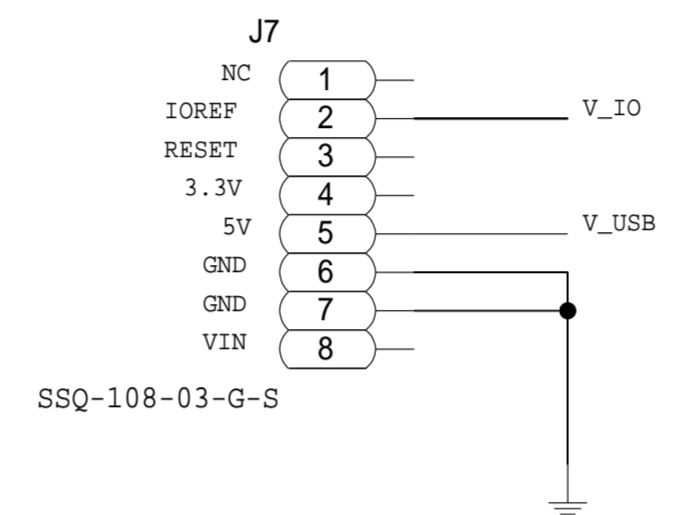
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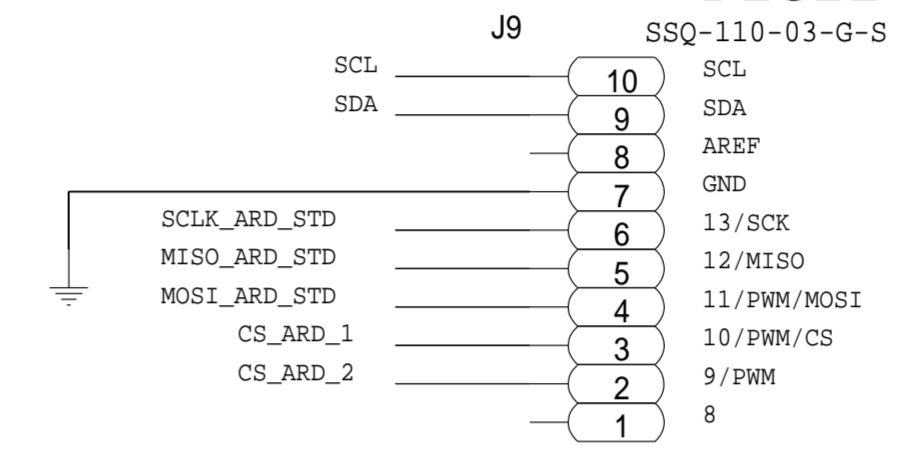
### ARDUNIO HEADER



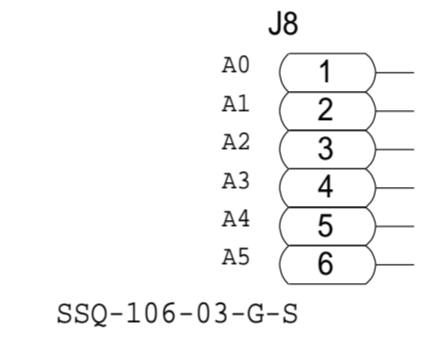
#### POWER



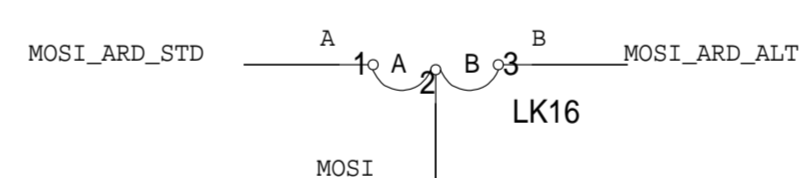
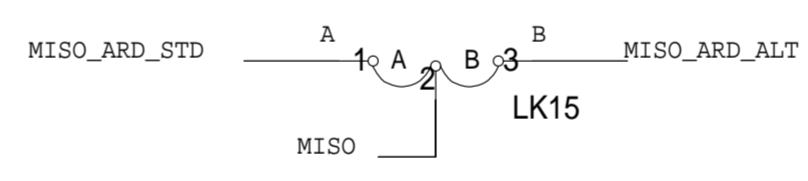
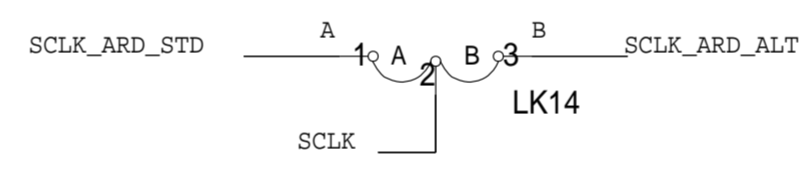
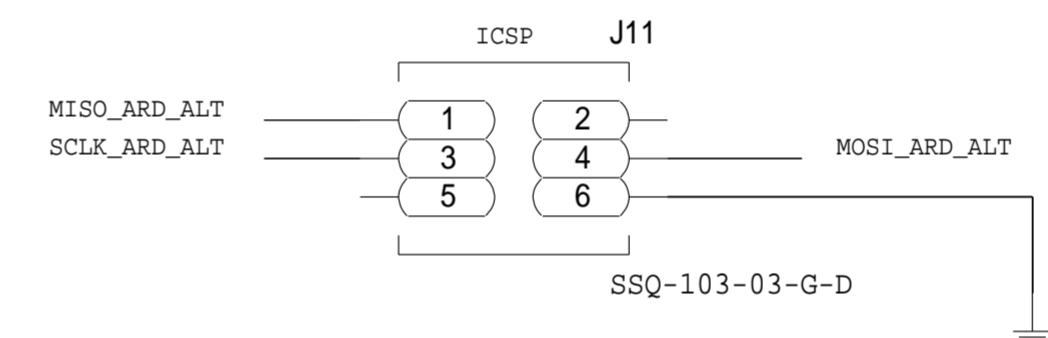
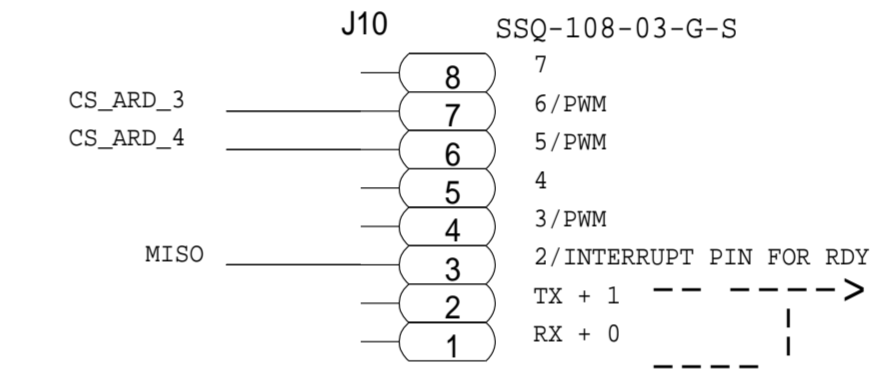
#### DIGI1



#### ANALOG IN

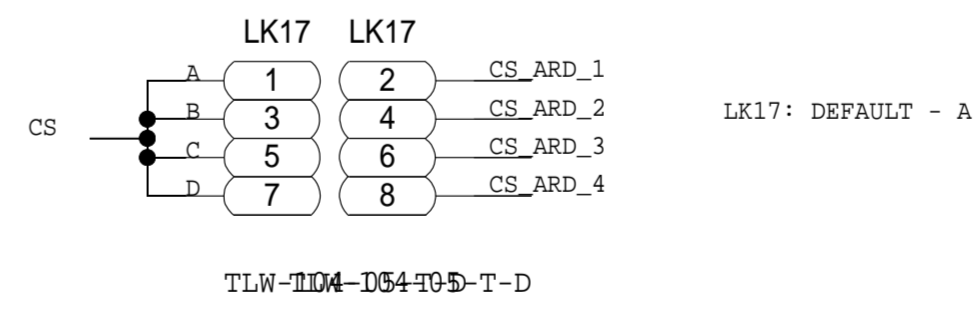


#### DIGI0



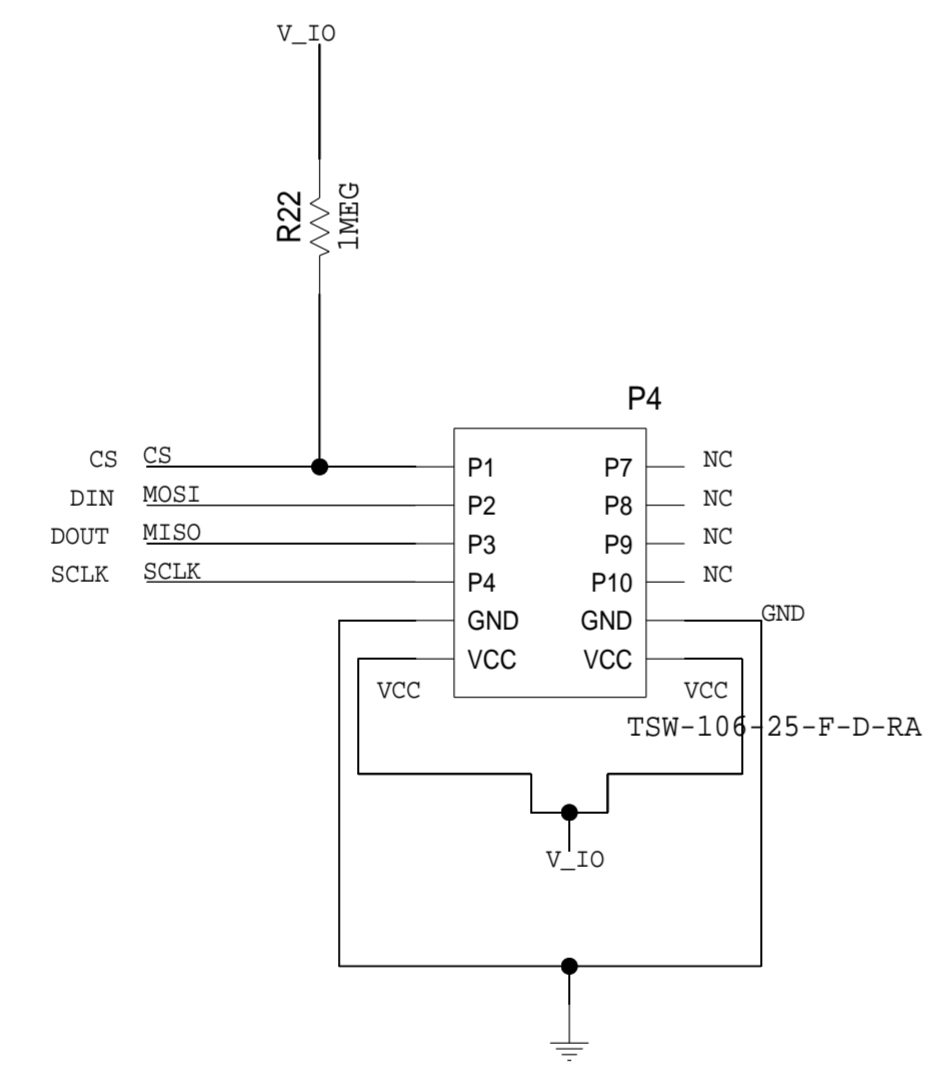
LK14, LK15, LK16: DEFAULT - A  
 A = STANDARD ARDUINO CONNECTION  
 B = ALTERNATE ARDUINO CONNECTION

#### ARDUNIO HEADER CS SELECTION SELECT DIFFERENT CS FOR STACKING MULTIPLE BOARDS



LK17: DEFAULT - A

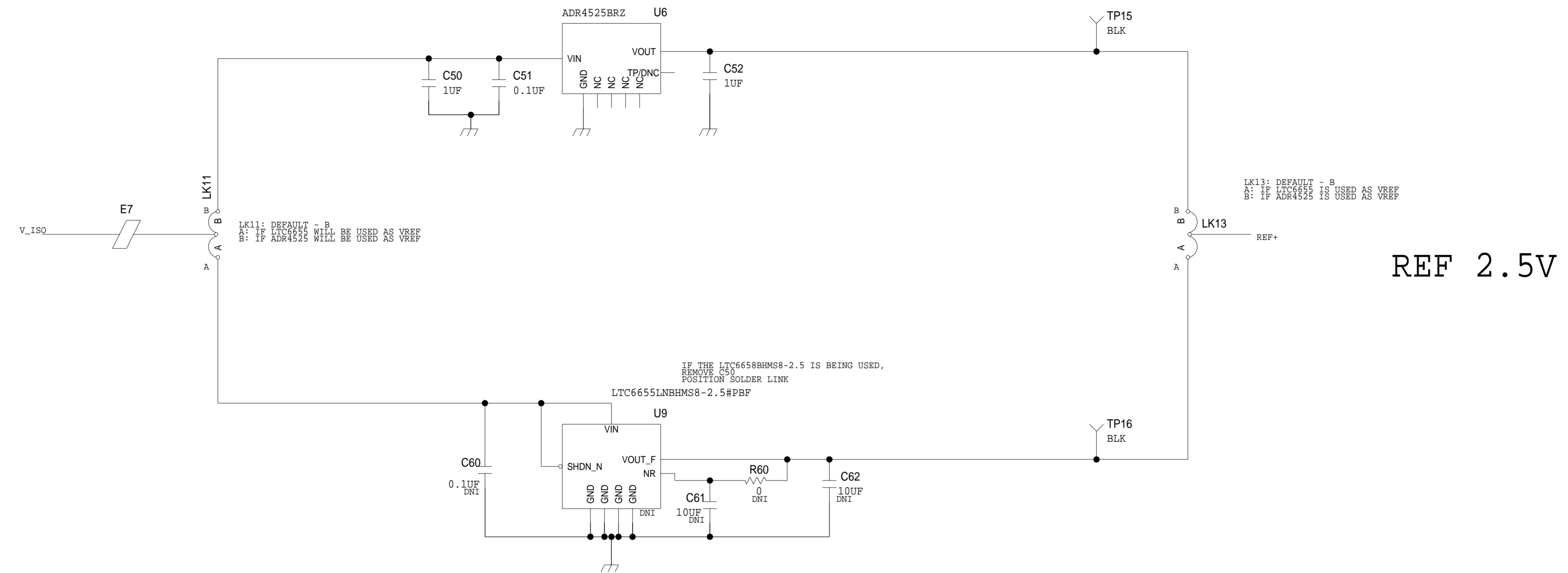
CONNECT THE DOUT PIN TO PIN 2 OF THE ARDUINO HEADER TO USE IT AS AN INTERRUPT PIN



	SCHEMATIC		
	HW TYPE : Customer Evaluation Product(s): AD4113 : ADR4525		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_072892	REV B	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 4 OF 6

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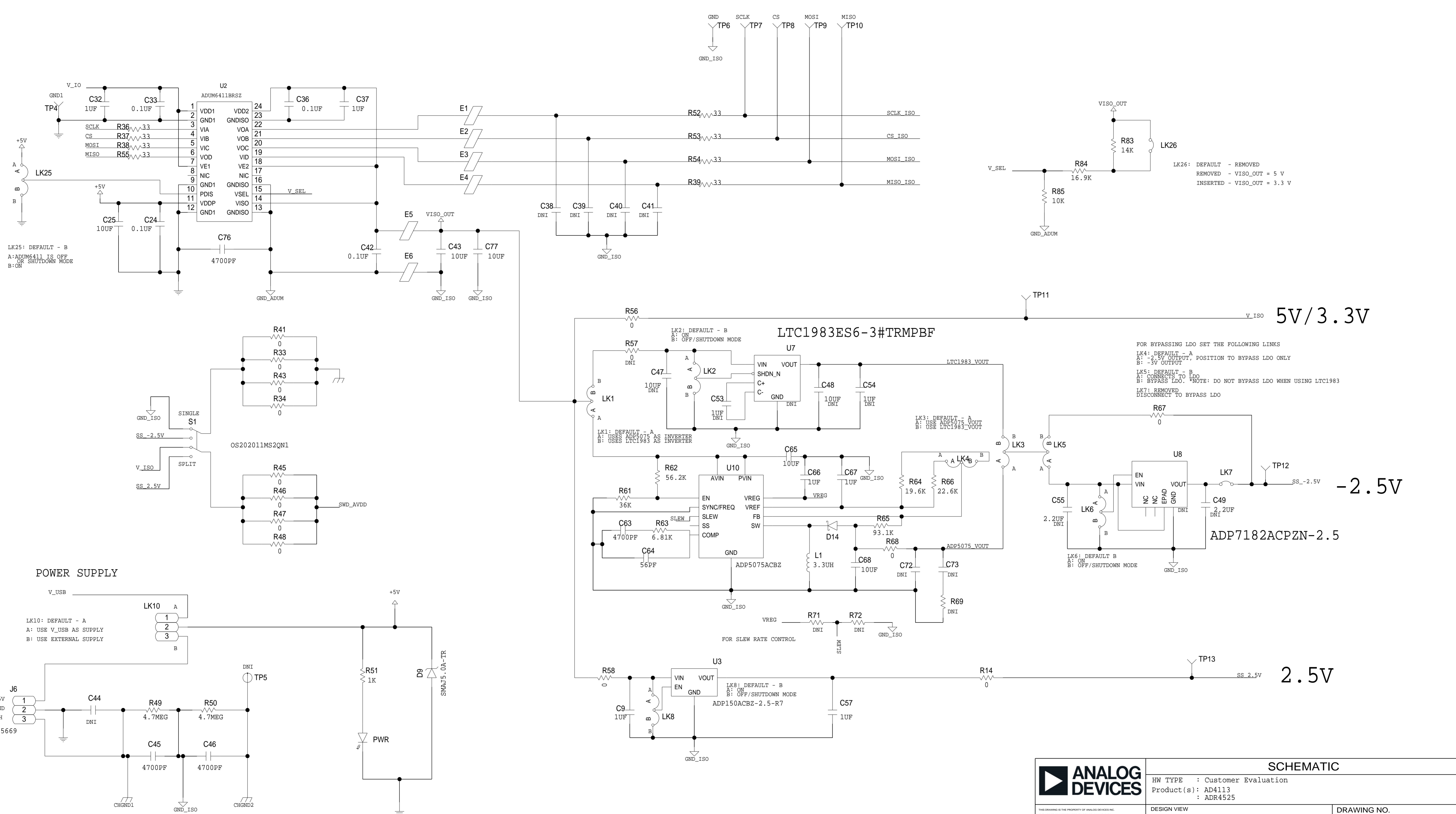
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Product(s): AD4113 : ADR4525		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_072892	REV B	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 5 OF 6

# POWER SECTION

REVISIONS			
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FOR BYPASSING LDO SET THE FOLLOWING LINKS  
 LK4: DEFAULT - A  
 A: -2.5V OUTPUT, POSITION TO BYPASS LDO ONLY  
 B: -3V OUTPUT  
 LK5: DEFAULT - B  
 A: CONNECTS TO LDO  
 B: BYPASS LDO. NOTE: DO NOT BYPASS LDO WHEN USING LTC1983  
 LK7: REMOVED  
 DISCONNECT TO BYPASS LDO

ANALOG DEVICES		SCHEMATIC	
HW TYPE : Customer Evaluation		DRAWING NO. 02_072892	
Product(s) : AD4113 :ADR4525		DESIGN VIEW <DESIGN_VIEW>	REV B
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 6 OF 6