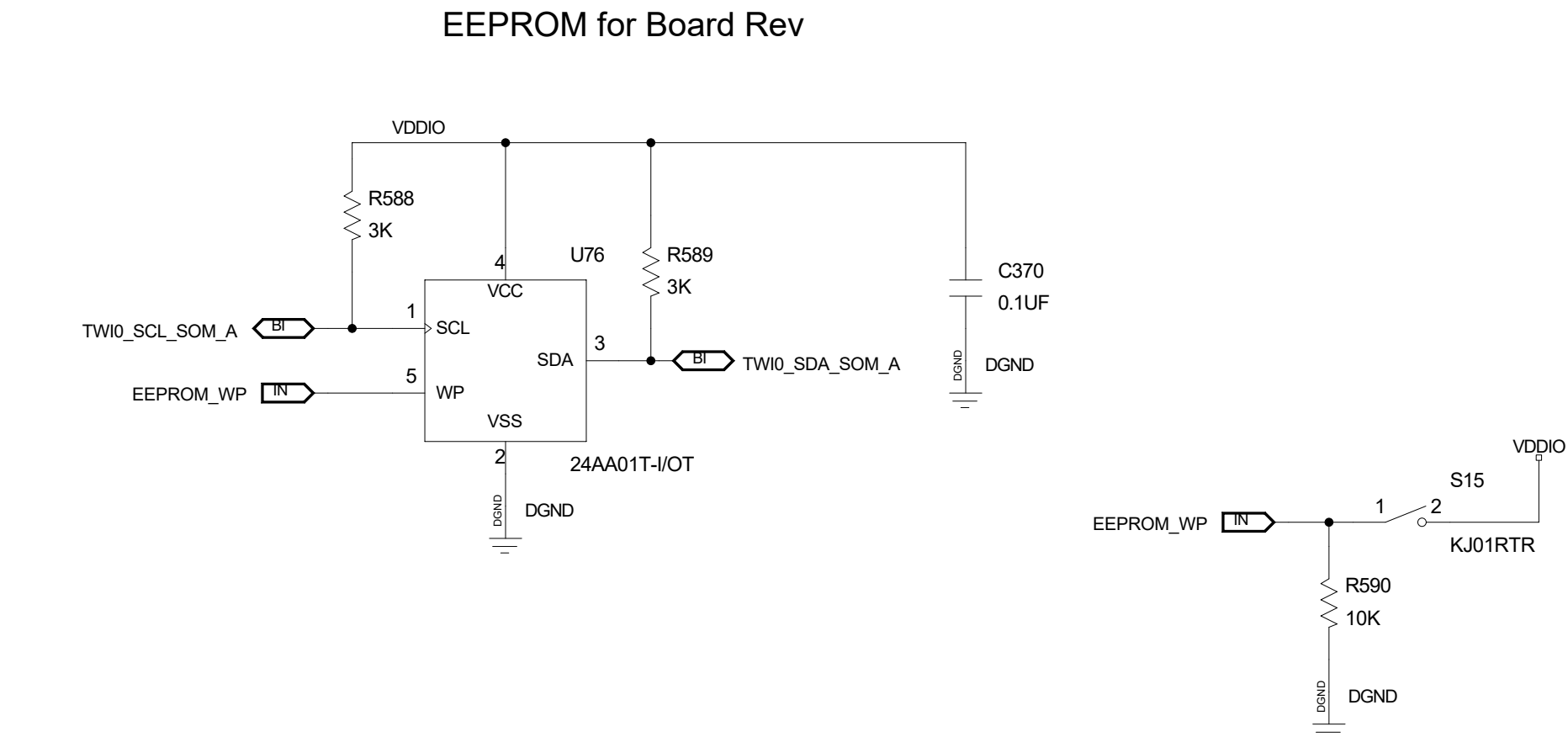
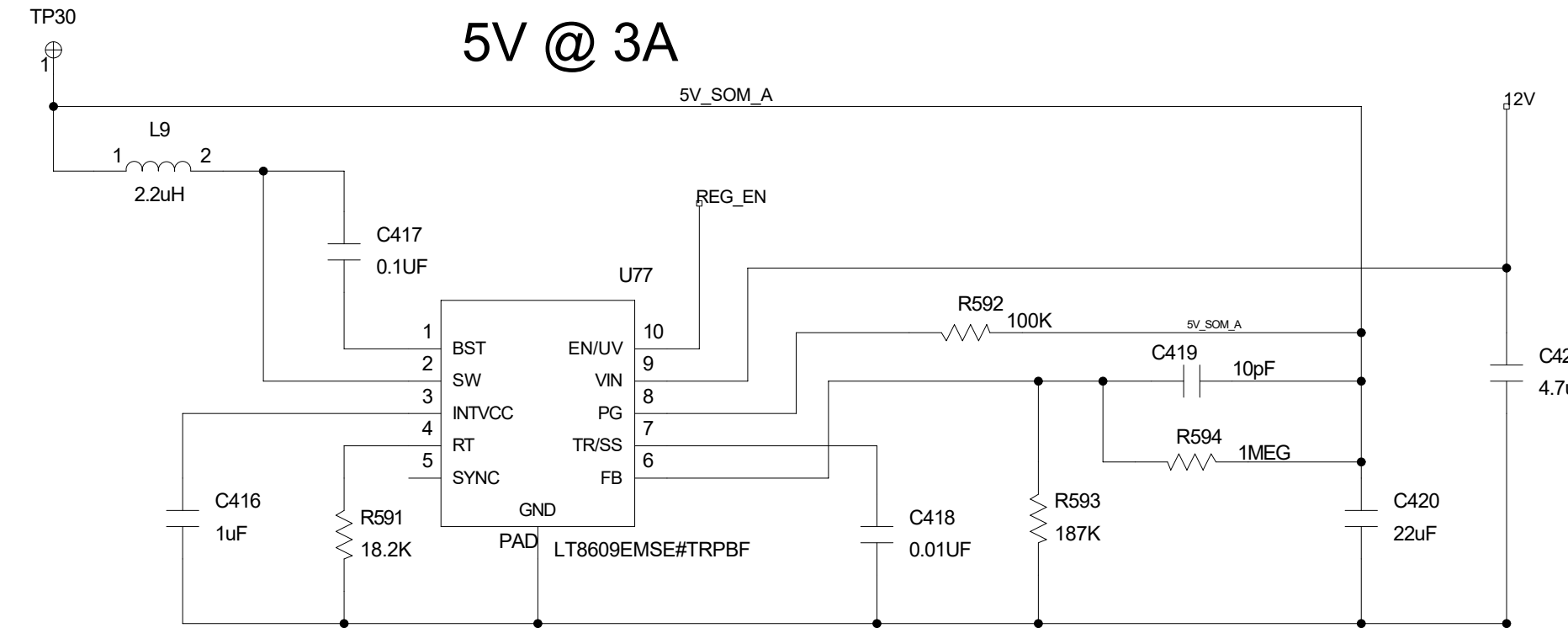
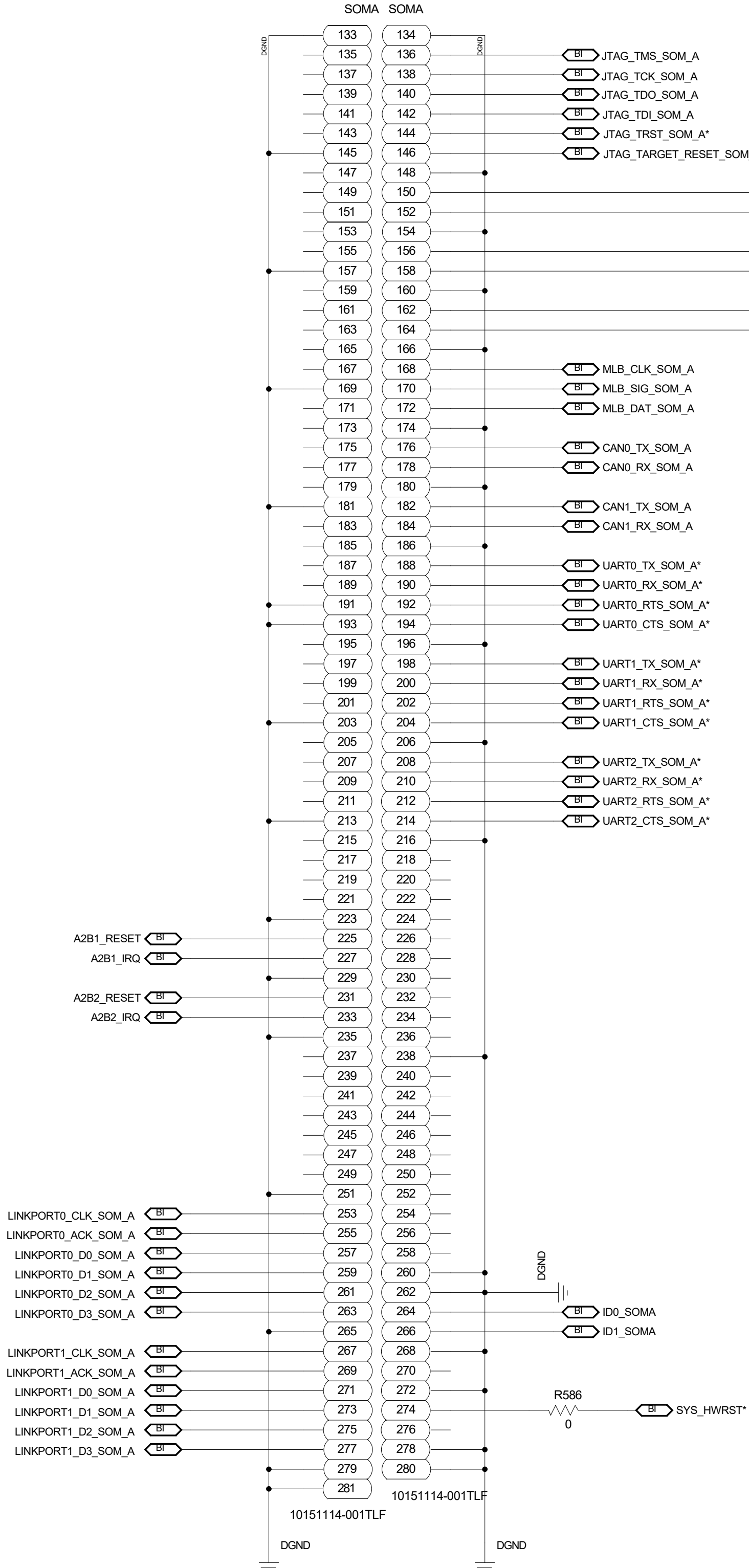
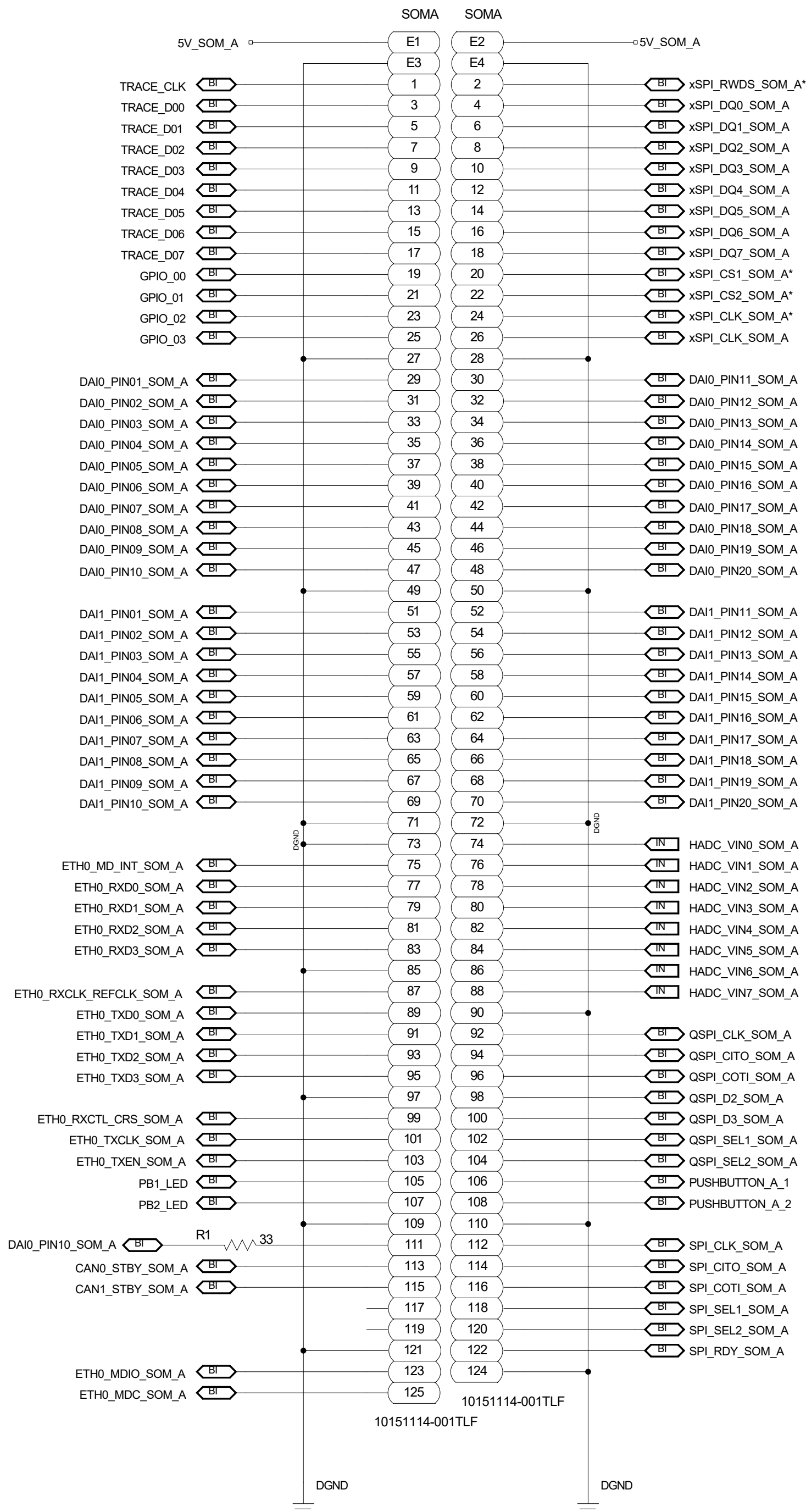


[illegible]

SOM A Connector

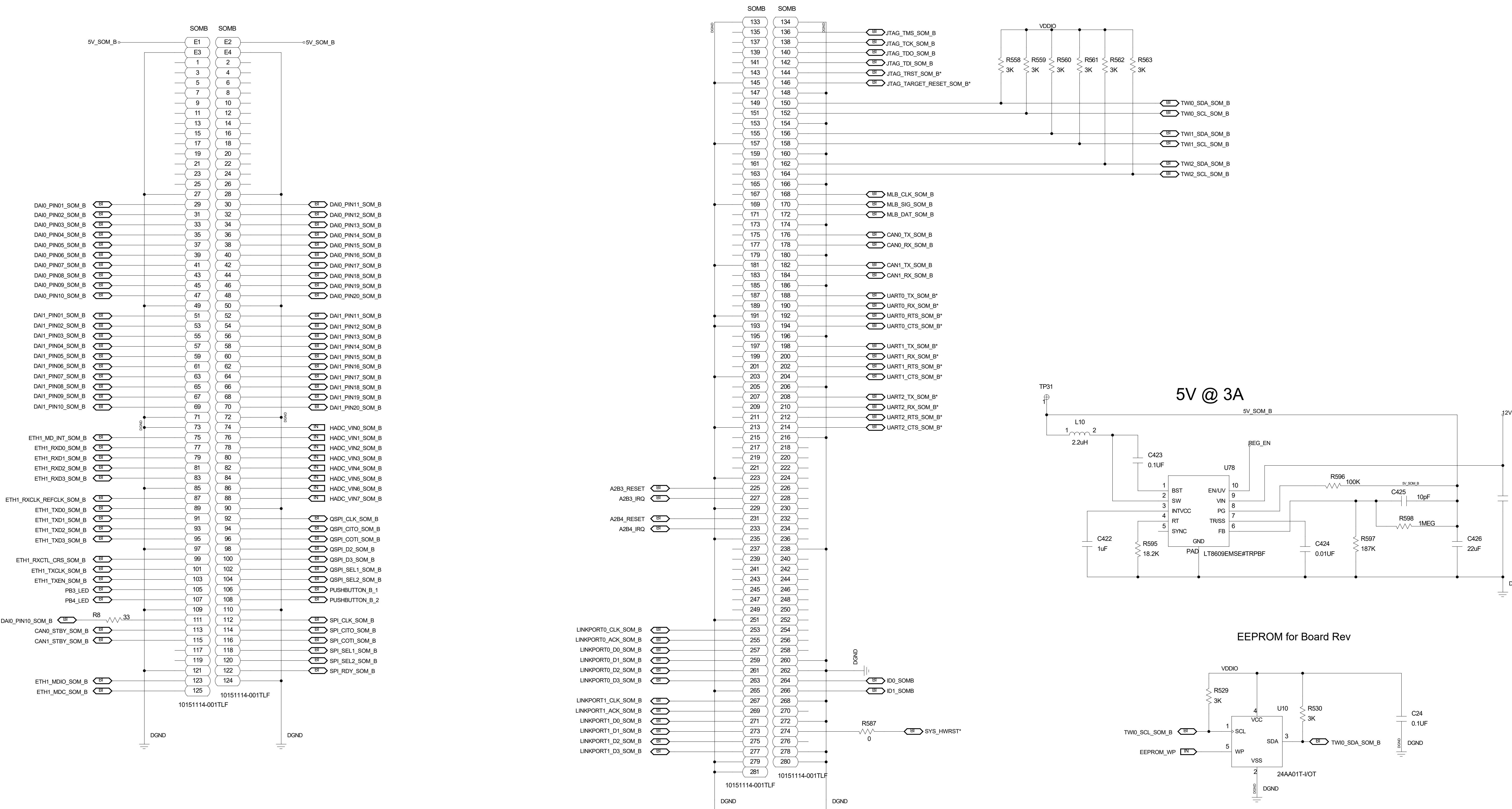
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM		DRAWING NO. 02_081712	
DESIGN VIEW \$DESIGN_VIEW		REV C	
PTD ENGINEER D. HARRINGTON		SIZE D	SCALE 1:1
		SHEET 2	OF 26

SOM B Connector

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



HW TYPE : Customer Evaluation Z
Product(s): SOMCRR2
: SOM

DESIGN VIEW
\$DESIGN_VIEW

PTD ENGINEER
D. HARRINGTON

DRAWING NO.
02_081712

SIZE
D

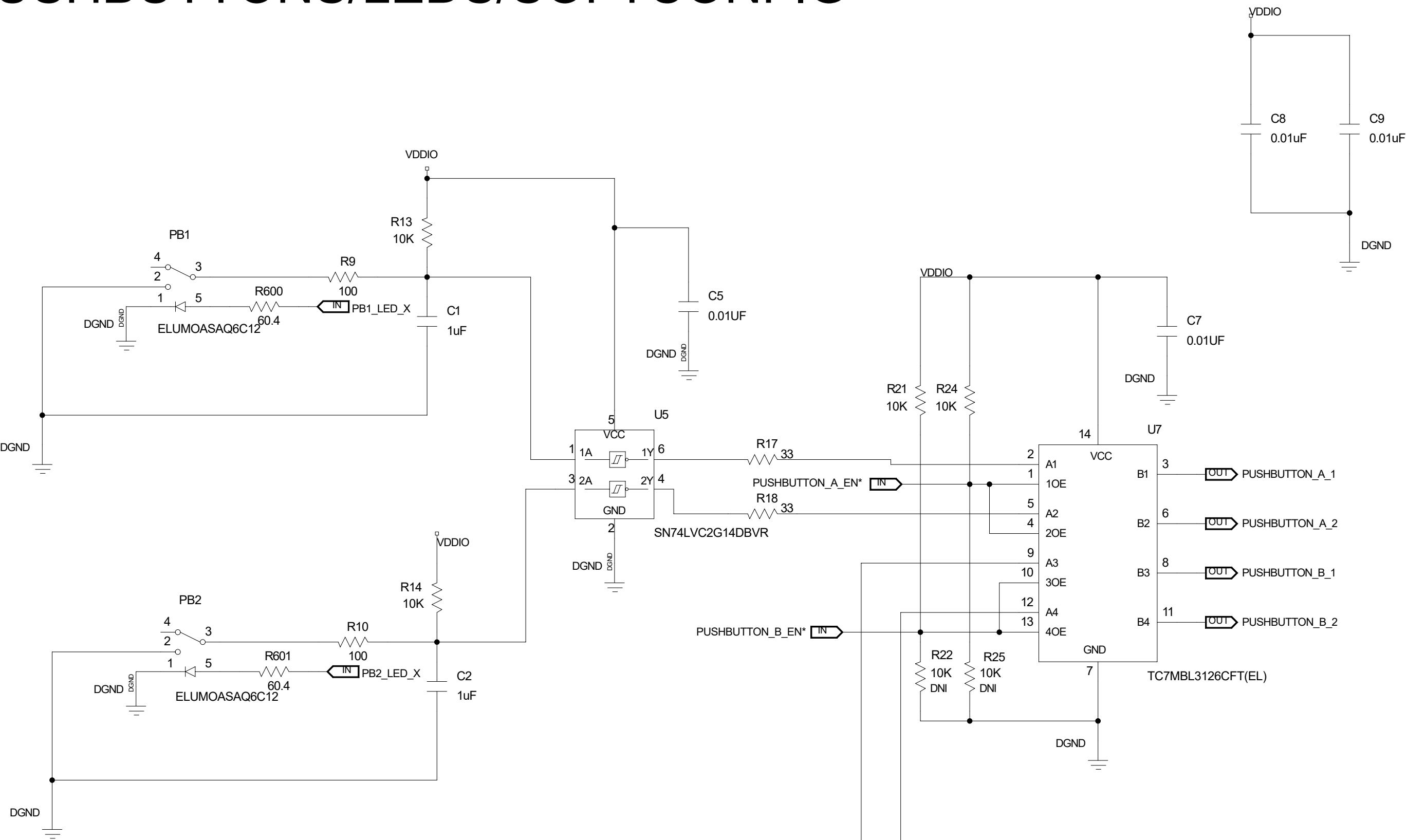
SCALE
1:1

SHEET 3 OF 26

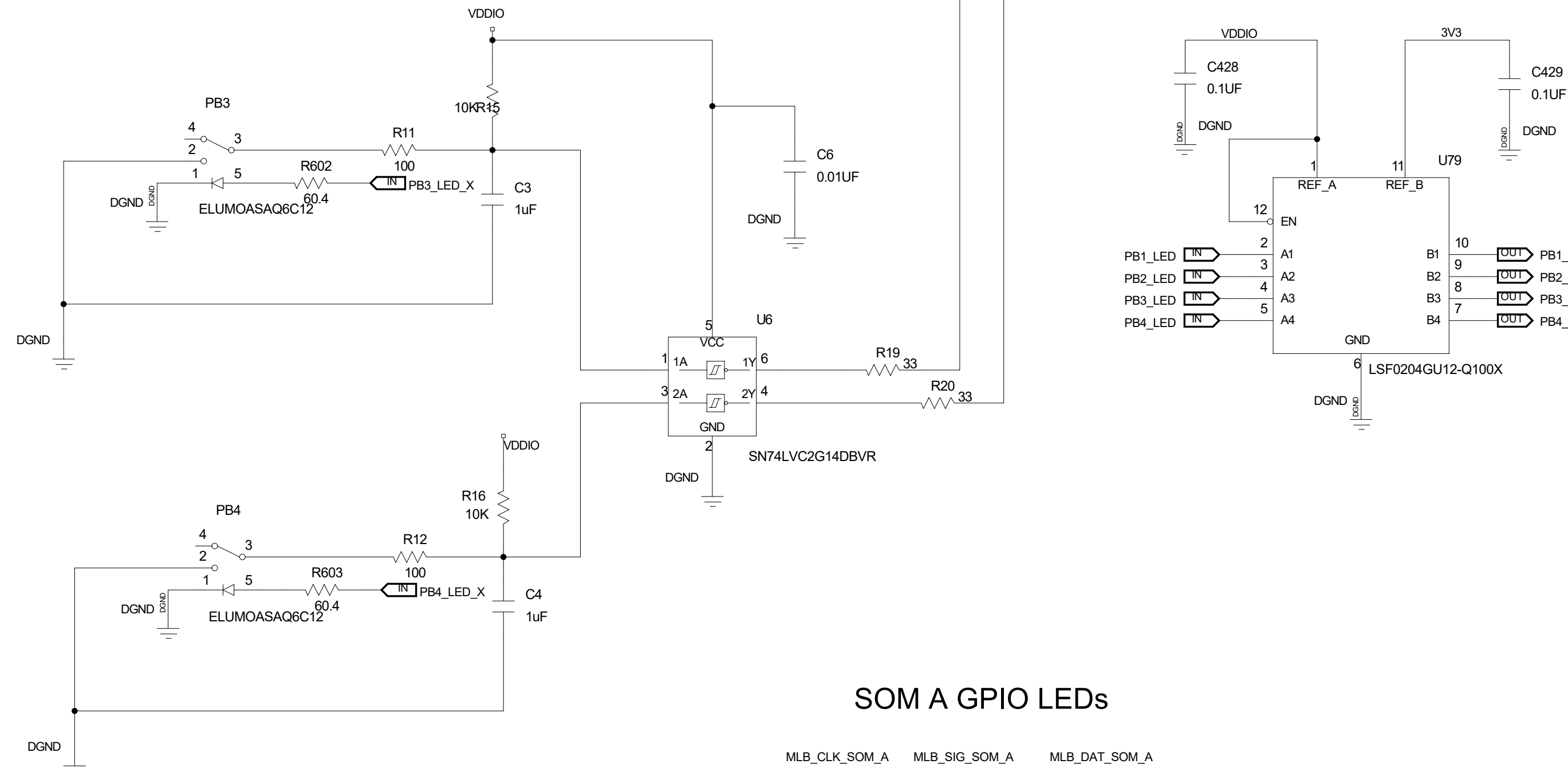
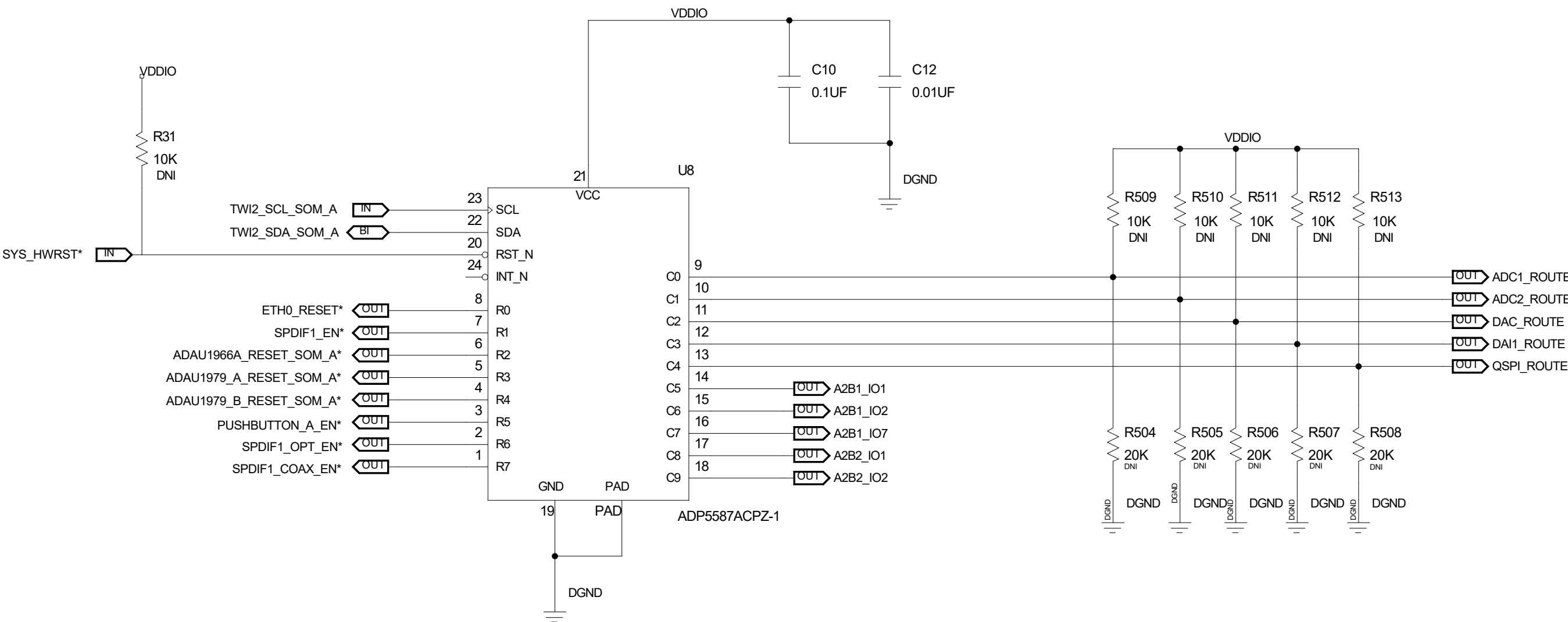
REV
C

PUSHBUTTONS/LEDS/SOFTCONFIG

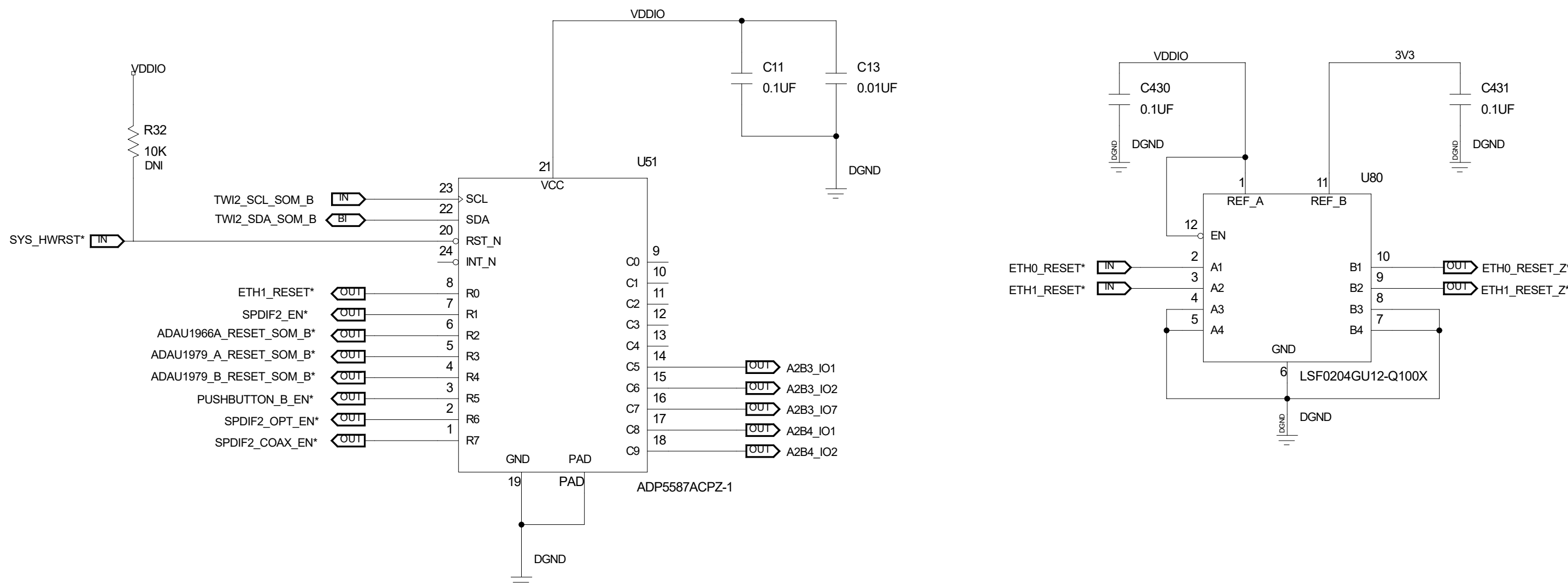
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



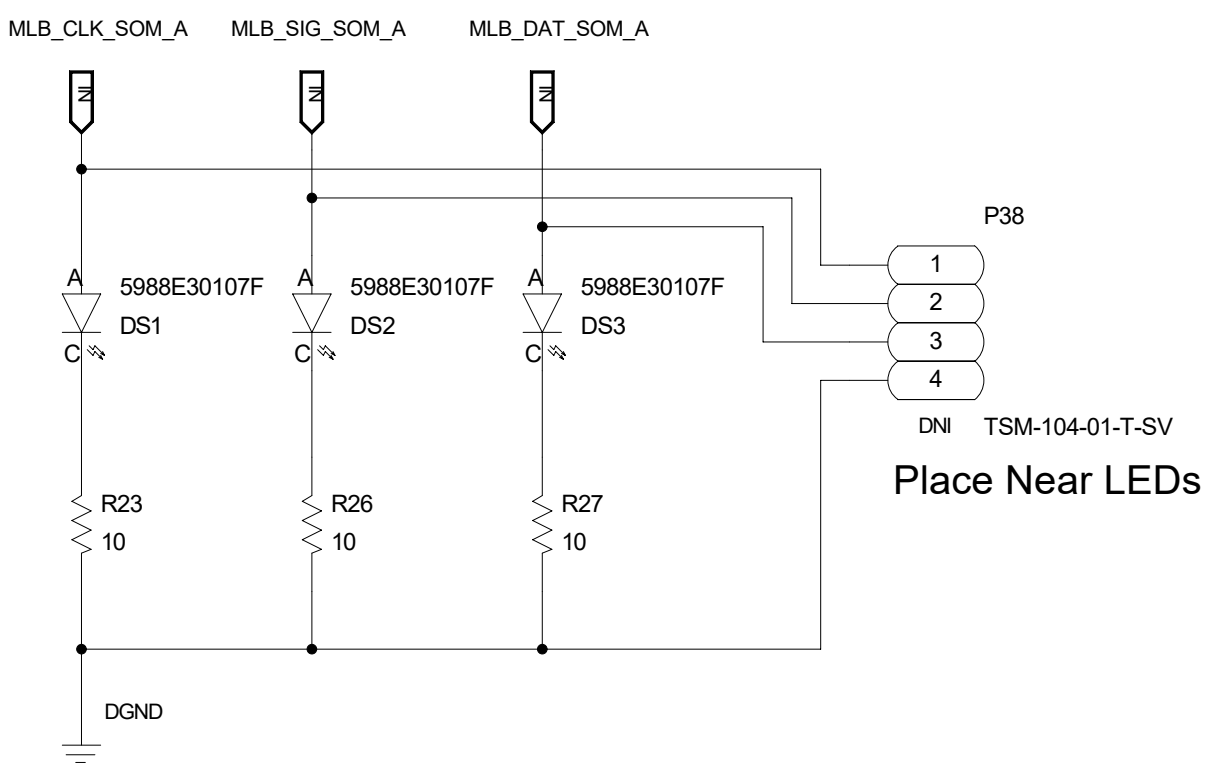
TWI ADDRESS 0X30
0110000X X: 1 FOR READ, 0 FOR WRITE



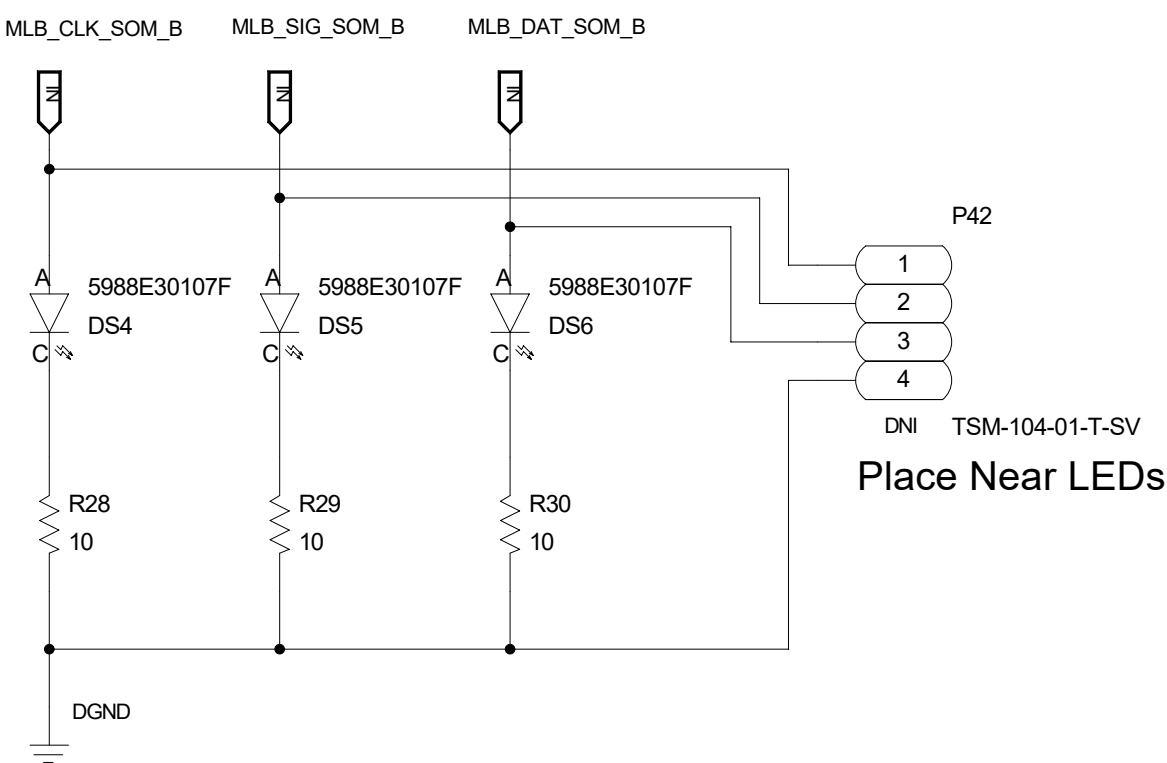
TWI ADDRESS 0X30
0110000X X: 1 FOR READ, 0 FOR WRITE



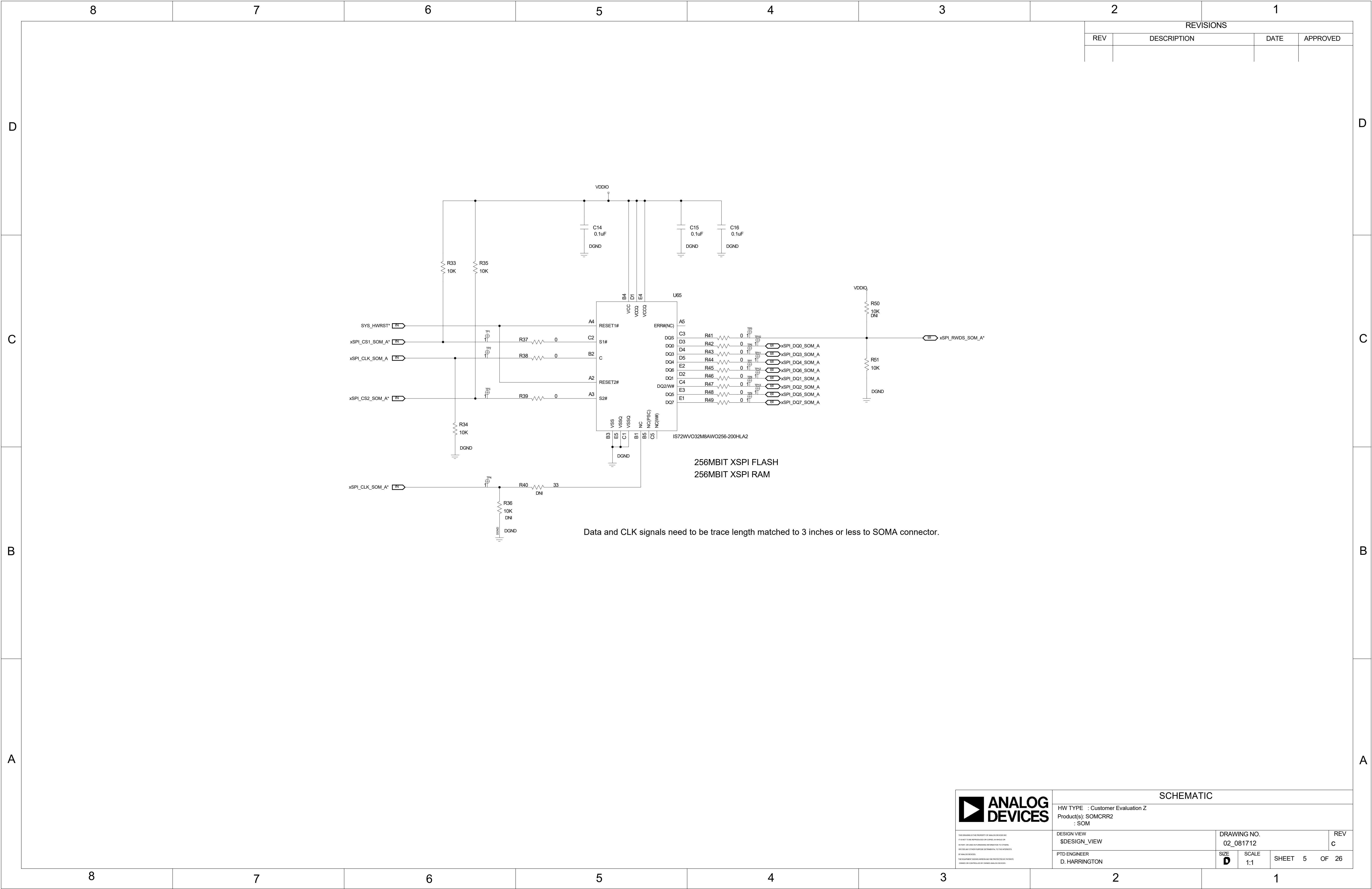
SOM A GPIO LEDs



SOM B GPIO LEDs

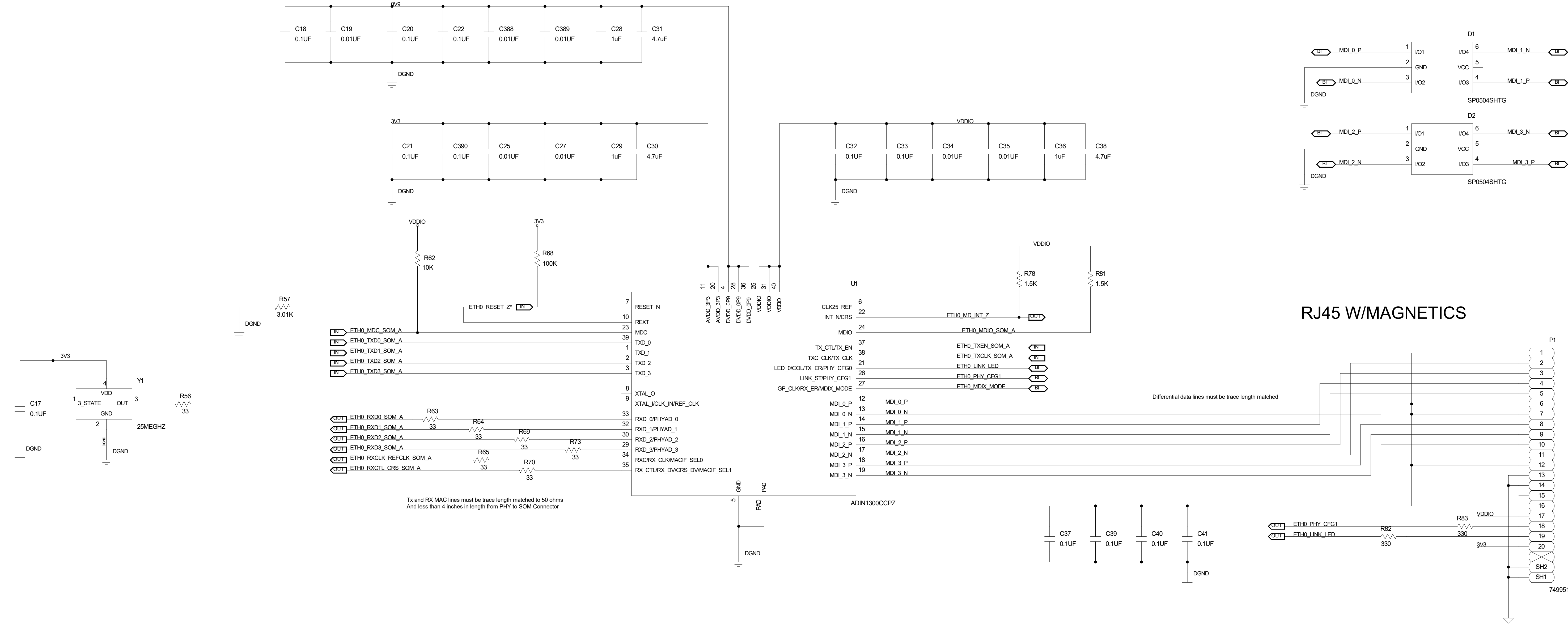


SCHEMATIC			
HW TYPE : Customer Evaluation Z			
Product(s): SOMCRR2			
: SOM			
DESIGN VIEW	DRAWING NO.		REV
\$DESIGN_VIEW	02_081712		C
PTD ENGINEER	SIZE	SCALE	SHEET
D. HARRINGTON	D	1:1	4 OF 26



GIGABIT ETHERNET RGMII

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



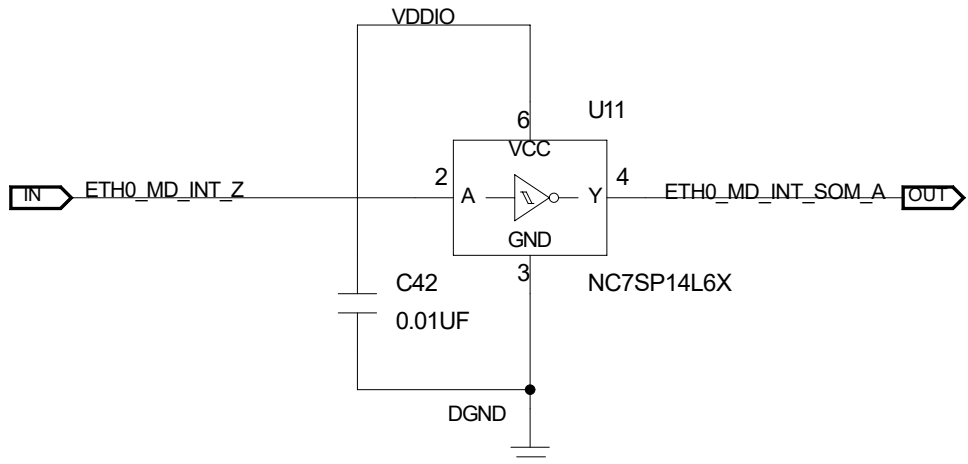
RJ45 W/MAGNETICS

MAC CONFIG

PHY ADDRESS 0X00

AUDO MDIX CONFIG MODE 4

PHY CONFIG MODE 4

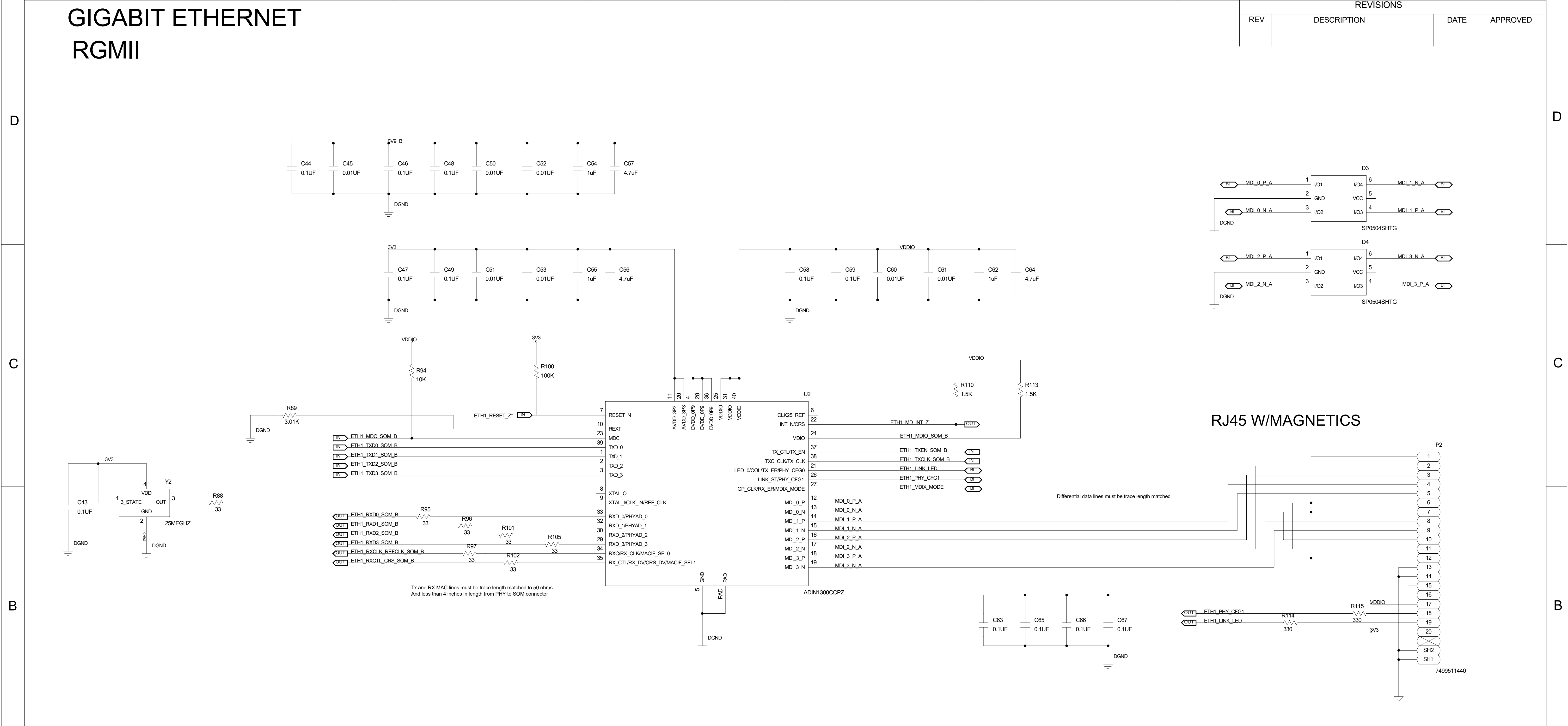
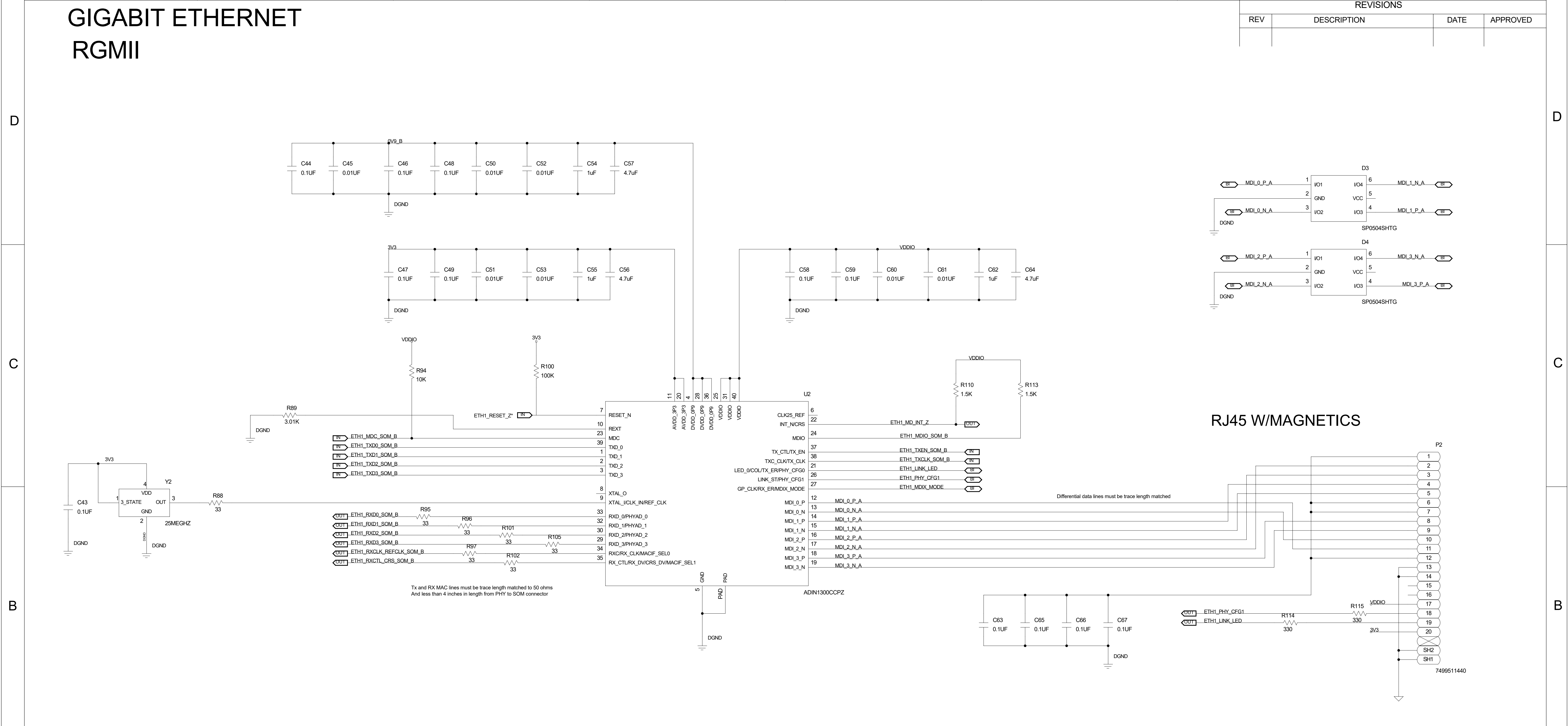


SCHEMATIC

HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM			
DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_081712		REV c
PTD ENGINEER D. HARRINGTON	SIZE D	SCALE 1:1	SHEET 6 OF 26

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---



D

C

B

GIGABIT ETHERNET RGMII

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

3V3

DGND

C43 0.1UF

Y2 25MEGHZ

R88 33

R89 3.01K

ETH1_MDC_SOM_B

ETH1_TXD0_SOM_B

ETH1_TXD1_SOM_B

ETH1_TXD2_SOM_B

ETH1_TXD3_SOM_B

ETH1_RXD0_SOM_B

ETH1_RXD1_SOM_B

ETH1_RXD2_SOM_B

ETH1_RXD3_SOM_B

ETH1_RXCLK_REFCLK_SOM_B

ETH1_RXCTL_CRS_SOM_B

R94 10K

R95 33

R96 33

R101 33

R105 33

R97 33

R102 33

ETH1_RESET_Z*

R100 100K

VDDIO

C44 0.1UF

C45 0.01UF

C46 0.1UF

C48 0.1UF

C50 0.01UF

C52 0.01UF

C54 1uF

C57 4.7uF

C47 0.1UF

C49 0.1UF

C51 0.01UF

C53 0.01UF

C55 1uF

C56 4.7uF

C58 0.1UF

C59 0.1UF

C60 0.01UF

C61 0.01UF

C62 1uF

C64 4.7uF

VDDIO

R110 1.5K

R113 1.5K

U2 ADIN1300CCP2

RESET_N

REXT

MDC

TXD_0

TXD_1

TXD_2

TXD_3

XTAL_O

XTAL_I/CLK_INREF_CLK

RXD_0/PHYAD_0

RXD_1/PHYAD_1

RXD_2/PHYAD_2

RXD_3/PHYAD_3

RXC/RX_CLK/MACIF_SEL0

RX_CTL/RX_DV/CRS_DV/MACIF_SEL1

MDIO

CLK25_REF

INT_N/CRS

ETH1_MDIO_SOM_B

ETH1_TXEN_SOM_B

ETH1_TXCLK_SOM_B

ETH1_LINK_LED

ETH1_PHY_CFG1

ETH1_MDIX_MODE

MDI_0_P

MDI_0_N

MDI_1_P

MDI_1_N

MDI_2_P

MDI_2_N

MDI_3_P

MDI_3_N

Differential data lines must be trace length matched

C63 0.1UF

C65 0.1UF

C66 0.1UF

C67 0.1UF

ETH1_PHY_CFG1

ETH1_LINK_LED

R114 330

R115 330

P2

7499511440

RJ45 W/MAGNETICS

D3 SP0504SHTG

D4 SP0504SHTG

MDI_0_P_A

MDI_0_N_A

MDI_1_P_A

MDI_1_N_A

MDI_2_P_A

MDI_2_N_A

MDI_3_P_A

MDI_3_N_A

IO1

IO2

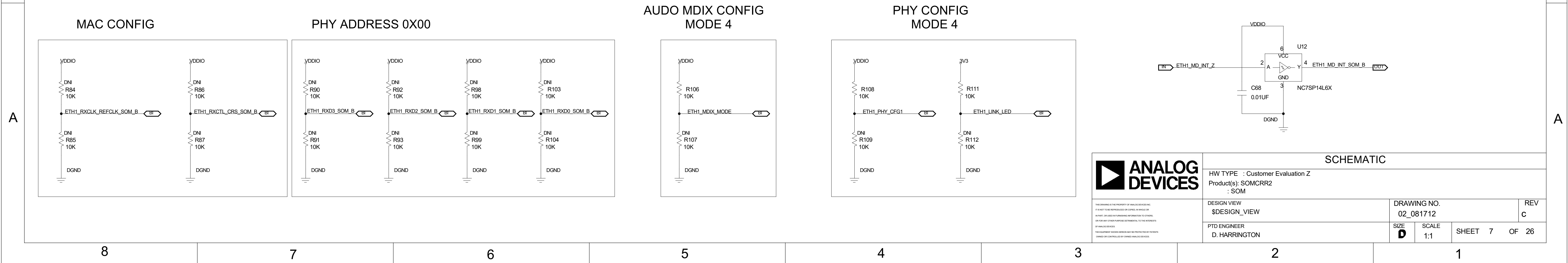
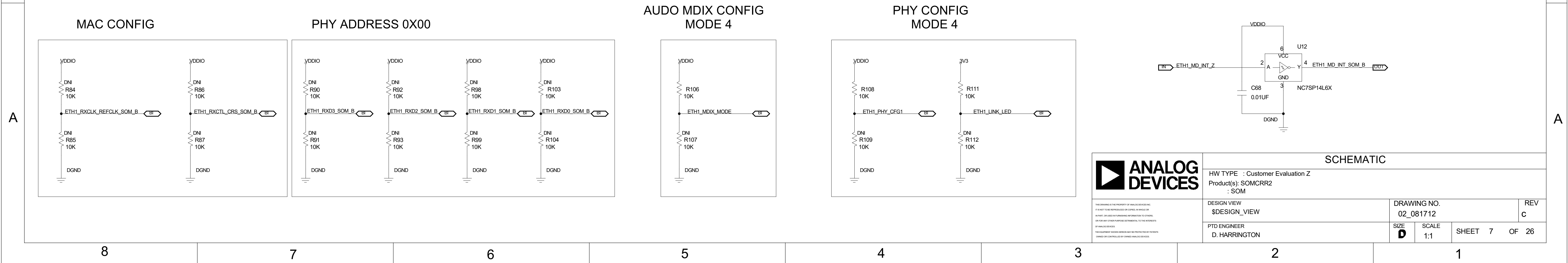
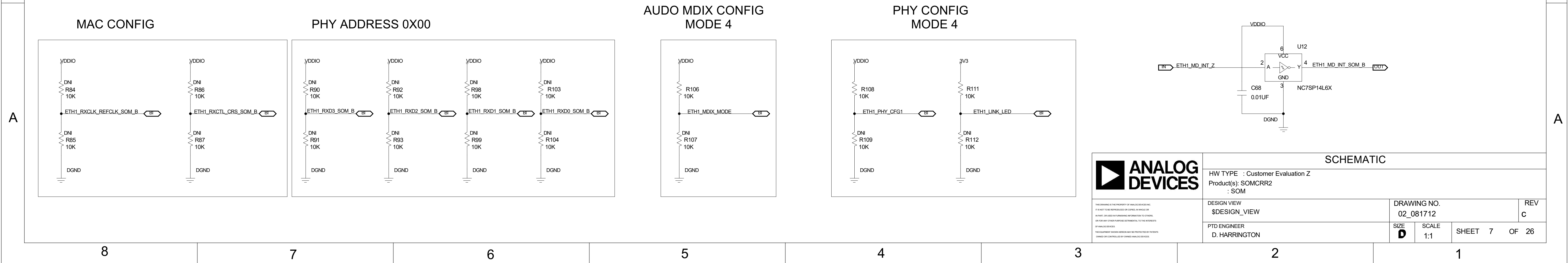
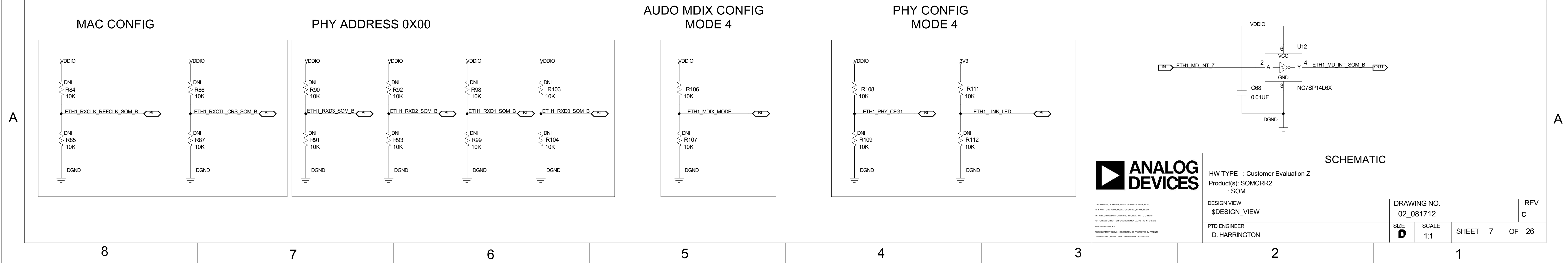
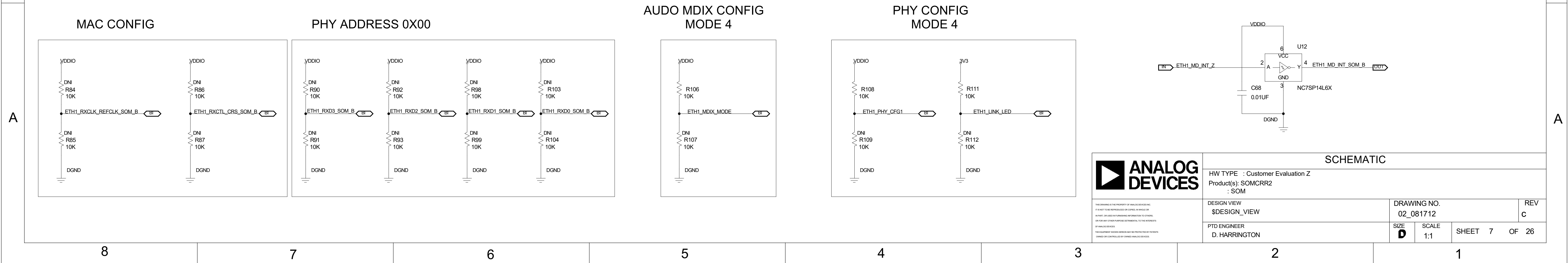
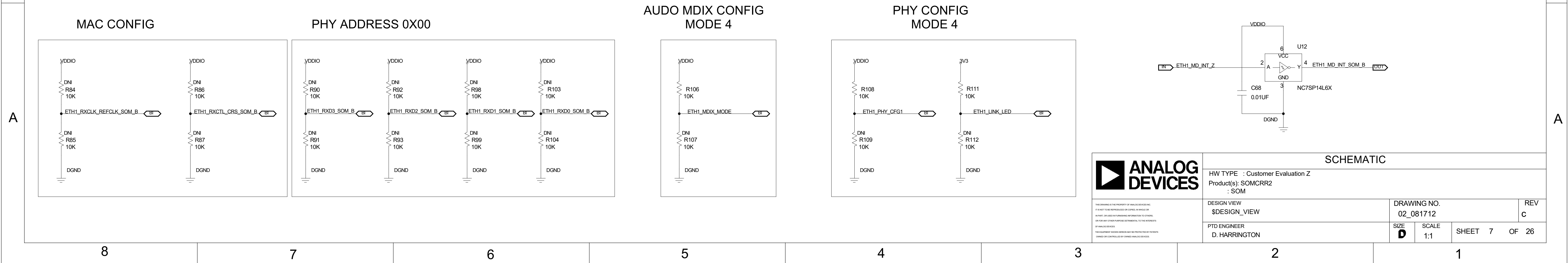
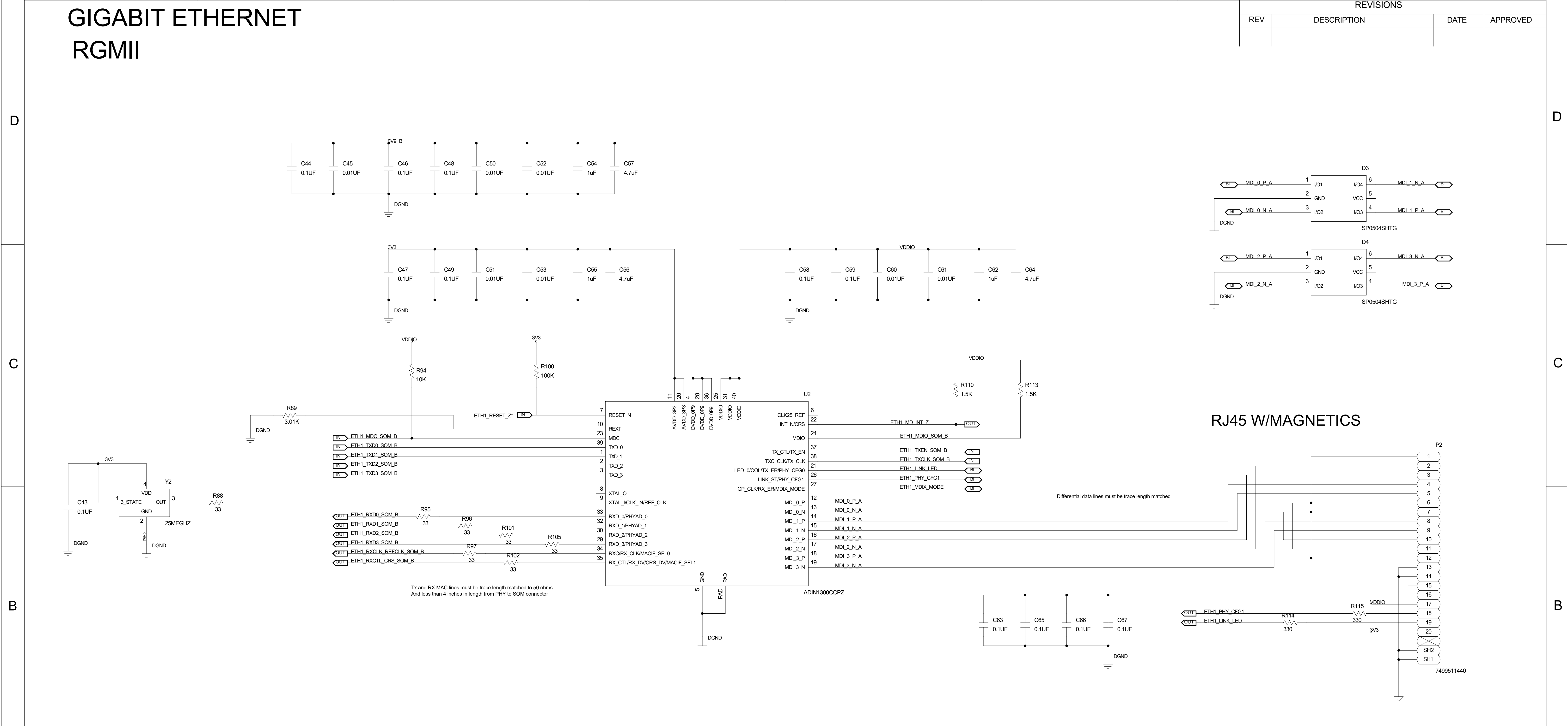
IO3

IO4

GND

VCC

DGND



MAC CONFIG

8

PHY ADDRESS 0X00

7

AUDIO MDIX CONFIG MODE 4

5

PHY CONFIG MODE 4

4

1

2

ANALOG DEVICES		SCHEMATIC				
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART. ANY USE OF THIS DRAWING IS LIMITED TO THE SPECIFIC PROJECT AND IS NOT TO BE USED FOR ANY OTHER PROJECT. THE EQUIPMENT DESIGN HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.		HW TYPE : Customer Evaluation Z		DRAWING NO.		REV
		Product(s): SOMCRR2		02_081712		c
		: SOM		SIZE D		SCALE 1:1
PTD ENGINEER D. HARRINGTON						

MAC CONFIG

PHY ADDRESS 0X00

AUDO MDIX CONFIG
MODE 4

PHY CONFIG
MODE 4

SCHEMATIC				
HW TYPE : Customer Evaluation Z				
Product(s): SOMCRR2				
: SOM				
DESIGN VIEW			DRAWING NO.	
\$DESIGN_VIEW			02_081712	
PTD ENGINEER			SIZE	REV
D. HARRINGTON			D 1:1	c
			SHEET 7	OF 26

8

7

6

5

4

3

2

1

MAC CONFIG

PHY ADDRESS 0X00

AUDO MDIX CONFIG
MODE 4

PHY CONFIG
MODE 4

ANALOG DEVICES		SCHEMATIC					
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART. ANY USE OF THIS DRAWING IS LIMITED TO THE SPECIFIC PROJECT AND IS NOT TO BE USED FOR ANY OTHER PROJECT. THE EQUIPMENT DESIGN HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.		HW TYPE : Customer Evaluation Z					
		Product(s): SOMCRR2					
		: SOM					
DESIGN VIEW		\$DESIGN_VIEW		DRAWING NO.		REV	
PTD ENGINEER		D. HARRINGTON		02_081712		c	
SIZE	D	SCALE	1:1	SHEET	7	OF	26

MAC CONFIG

PHY ADDRESS 0X00

AUDO MDIX CONFIG
MODE 4

PHY CONFIG
MODE 4

ANALOG DEVICES		SCHEMATIC					
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		Product(s): SOMCRR2					
		: SOM					
DESIGN VIEW		\$DESIGN_VIEW		DRAWING NO.		REV	
PTD ENGINEER		D. HARRINGTON		02_081712		c	
SIZE	D	SCALE	1:1	SHEET	7	OF	26

MAC CONFIG

PHY ADDRESS 0X00

AUDIO MDIX CONFIG MODE 4

PHY CONFIG MODE 4

2

1

		SCHEMATIC			
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES, INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART. ANY USE OF THIS DRAWING IS LIMITED TO THE SPECIFIC PROJECT FOR WHICH IT WAS PROVIDED. IT IS NOT TO BE USED FOR ANY OTHER PROJECT WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT DESCRIBED HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.		HW TYPE : Customer Evaluation Z			
		Product(s): SOMCRR2			
		: SOM			
DESIGN VIEW		DRAWING NO.		REV	
\$DESIGN_VIEW		02_081712		c	
PTD ENGINEER		SIZE	SCALE	SHEET	OF
D. HARRINGTON		D	1:1	7	26

[illegible]

ADCs

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

7-Bit Address 0x11

7-Bit Address 0x71

Clock Routing



HW TYPE : Customer Evaluation Z
Product(s): SOMCRR2
: SOM

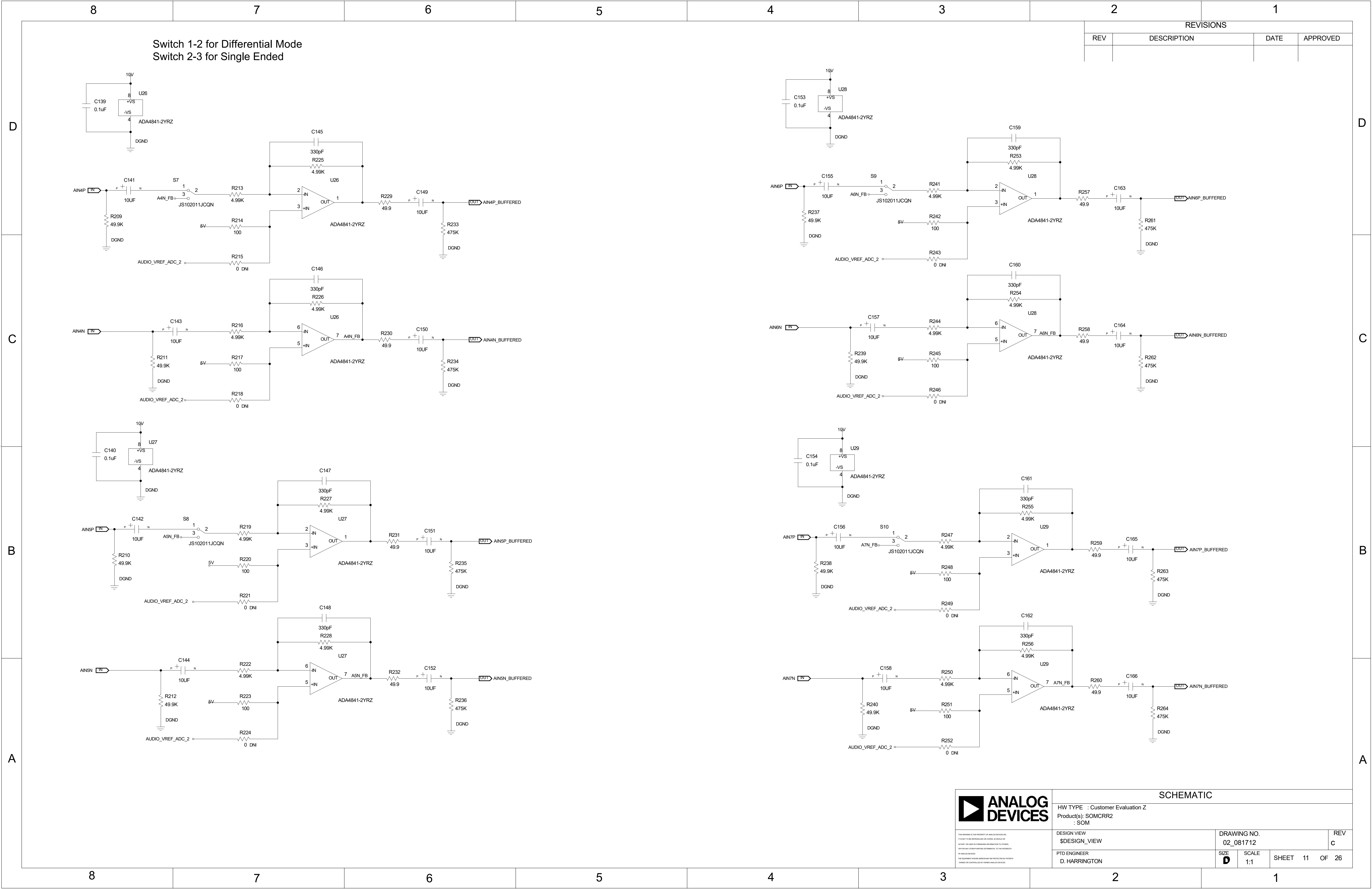
DESIGN VIEW
\$DESIGN_VIEW

PTD ENGINEER
D. HARRINGTON

DRAWING NO.
02_081712

REV
c

SIZE D	SCALE 1:1	SHEET 9	OF 26
-----------	--------------	------------	----------



C162

330pF

R256

4.99K

U29

AIN6P

C155

10UF

S9

1

2

A6N_FB

3

JS102011JCQN

R241

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R243

49.9

C163

10UF

AIN6P_BUFFERED

R237

49.9K

DGND

5V

R242

100

AUDIO_VREF_ADC_2

R243

0 DNI

C159

330pF

R253

4.99K

U28

C160

330pF

R254

4.99K

U28

AIN6N

C157

10UF

R244

4.99K

6

-IN

OUT

7

A6N_FB

R258

49.9

C164

10UF

AIN6N_BUFFERED

R239

49.9K

DGND

5V

R245

100

AUDIO_VREF_ADC_2

R246

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

AIN4P

C153

0.1uF

U28

ADA4841-2YRZ

DGND

C155

10UF

S9

1

2

A6N_FB

3

JS102011JCQN

R241

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R243

49.9

C163

10UF

AIN6P_BUFFERED

R237

49.9K

DGND

5V

R242

100

AUDIO_VREF_ADC_2

R243

0 DNI

C159

330pF

R253

4.99K

U28

C160

330pF

R254

4.99K

U28

AIN4N

C143

10UF

R216

4.99K

6

-IN

OUT

7

A4N_FB

R230

49.9

C150

10UF

AIN4N_BUFFERED

R211

49.9K

DGND

5V

R217

100

AUDIO_VREF_ADC_2

R218

0 DNI

C146

330pF

R226

4.99K

U26

C147

330pF

R227

4.99K

U27

AIN5P

C142

10UF

S8

1

2

A5N_FB

3

JS102011JCQN

R219

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R231

49.9

C151

10UF

AIN5P_BUFFERED

R210

49.9K

DGND

5V

R220

100

AUDIO_VREF_ADC_2

R221

0 DNI

C149

330pF

R225

4.99K

U26

C150

330pF

R226

4.99K

U26

AIN5N

C144

10UF

R222

4.99K

6

-IN

OUT

7

A5N_FB

R232

49.9

C152

10UF

AIN5N_BUFFERED

R212

49.9K

DGND

5V

R223

100

AUDIO_VREF_ADC_2

R224

0 DNI

C147

330pF

R227

4.99K

U27

C148

330pF

R228

4.99K

U27

AIN7P

C156

10UF

S10

1

2

A7N_FB

3

JS102011JCQN

R247

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R249

49.9

C165

10UF

AIN7P_BUFFERED

R238

49.9K

DGND

5V

R248

100

AUDIO_VREF_ADC_2

R249

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

AIN7N

C158

10UF

R250

4.99K

6

-IN

OUT

7

A7N_FB

R260

49.9

C166

10UF

AIN7N_BUFFERED

R240

49.9K

DGND

5V

R251

100

AUDIO_VREF_ADC_2

R252

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

AIN6P

C153

0.1uF

U28

ADA4841-2YRZ

DGND

C155

10UF

S9

1

2

A6N_FB

3

JS102011JCQN

R241

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R243

49.9

C163

10UF

AIN6P_BUFFERED

R237

49.9K

DGND

5V

R242

100

AUDIO_VREF_ADC_2

R243

0 DNI

C159

330pF

R253

4.99K

U28

C160

330pF

R254

4.99K

U28

AIN6N

C157

10UF

R244

4.99K

6

-IN

OUT

7

A6N_FB

R258

49.9

C164

10UF

AIN6N_BUFFERED

R239

49.9K

DGND

5V

R245

100

AUDIO_VREF_ADC_2

R246

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

AIN4P

C139

0.1uF

U26

ADA4841-2YRZ

DGND

C141

10UF

S7

1

2

A4N_FB

3

JS102011JCQN

R213

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R229

49.9

C149

10UF

AIN4P_BUFFERED

R209

49.9K

DGND

5V

R214

100

AUDIO_VREF_ADC_2

R215

0 DNI

C145

330pF

R225

4.99K

U26

C146

330pF

R226

4.99K

U26

AIN4N

C143

10UF

R216

4.99K

6

-IN

OUT

7

A4N_FB

R230

49.9

C150

10UF

AIN4N_BUFFERED

R211

49.9K

DGND

5V

R217

100

AUDIO_VREF_ADC_2

R218

0 DNI

C147

330pF

R227

4.99K

U27

C148

330pF

R228

4.99K

U27

AIN5P

C142

10UF

S8

1

2

A5N_FB

3

JS102011JCQN

R219

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R231

49.9

C151

10UF

AIN5P_BUFFERED

R210

49.9K

DGND

5V

R220

100

AUDIO_VREF_ADC_2

R221

0 DNI

C149

330pF

R225

4.99K

U26

C150

330pF

R226

4.99K

U26

AIN5N

C144

10UF

R222

4.99K

6

-IN

OUT

7

A5N_FB

R232

49.9

C152

10UF

AIN5N_BUFFERED

R212

49.9K

DGND

5V

R223

100

AUDIO_VREF_ADC_2

R224

0 DNI

C147

330pF

R227

4.99K

U27

C148

330pF

R228

4.99K

U27

AIN7P

C156

10UF

S10

1

2

A7N_FB

3

JS102011JCQN

R247

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R249

49.9

C165

10UF

AIN7P_BUFFERED

R238

49.9K

DGND

5V

R248

100

AUDIO_VREF_ADC_2

R249

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

AIN7N

C158

10UF

R250

4.99K

6

-IN

OUT

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A7N_FB

R260

49.9

C166

10UF

AIN7N_BUFFERED

R240

49.9K

DGND

5V

R251

100

AUDIO_VREF_ADC_2

R252

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

AIN6P

C153

0.1uF

U28

ADA4841-2YRZ

DGND

C155

10UF

S9

1

2

A6N_FB

3

JS102011JCQN

R241

4.99K

2

-IN

OUT

1

ADA4841-2YRZ

R243

49.9

C163

10UF

AIN6P_BUFFERED

R237

49.9K

DGND

5V

R242

100

AUDIO_VREF_ADC_2

R243

0 DNI

C159

330pF

R253

4.99K

U28

C160

330pF

R254

4.99K

U28

AIN6N

C157

10UF

R244

4.99K

6

-IN

OUT

7

A6N_FB

R258

49.9

C164

10UF

AIN6N_BUFFERED

R239

49.9K

DGND

5V

R245

100

AUDIO_VREF_ADC_2

R246

0 DNI

C161

330pF

R255

4.99K

U29

C162

330pF

R256

4.99K

U29

87654321

SCHEMATIC

ANALOG
DEVICES

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DEVICES INC.

HW TYPE : Customer Evaluation Z
Product(s): SOMCRR2
: SOM

DESIGN VIEW
\$DESIGN_VIEW

PTD ENGINEER
D. HARRINGTON

DRAWING NO.
02_081712

REV
c

SIZE
D

SCALE
1:1

SHEET 11 OF 26

8 7 6 5 4 3 2 1

DAC

REV DESCRIPTION DATE APPROVED

3V3

C177 10uF

C178 0.1uF

R284 1K

Q1 FZT953TA

C179 0.1uF

C180 10uF

DGND

3V3

C184 10uF

C185 10uF

C187 10uF

C189 10uF

C192 0.1uF

C194 0.1uF

C195 0.1uF

C196 0.1uF

C197 0.1uF

C198 0.1uF

DGND

24.576MHz

P36

69157-102HLF

3V3

C172 0.1uF

DGND

DAC_AUDIO_CLK

R281

0

DGND

C175 0.1uF

C174 390pF

C176 5600pF

R280 562

DGND

3V3

U30 NXU0104GU12-Q100X

VDDIO

C168 0.1uF

DGND

3V3

OE

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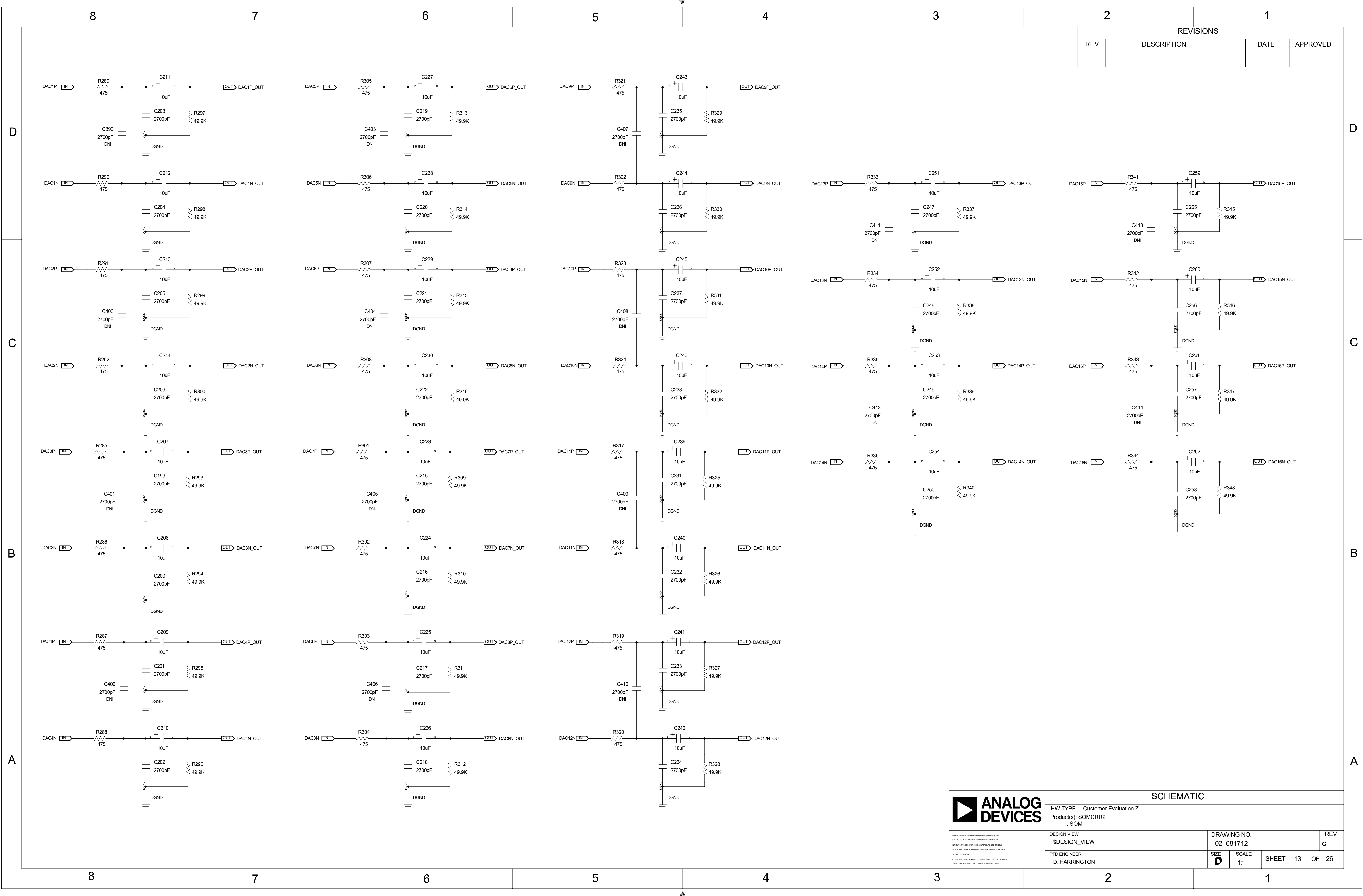
457

Schematic diagram of the output filter stage. It shows a common-emitter amplifier with a collector load resistor R180 and a bypass capacitor C196. The collector is connected to a series combination of capacitors C197 and C198, which are connected to the output terminal. The emitter is connected to ground through a resistor R181.



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SCHEMATIC									
HW TYPE : Customer Evaluation Z									
Product(s): SOMCRR2									
: SOM									
DESIGN VIEW					DRAWING NO.				REV
\$DESIGN_VIEW					02_081712				C
PTD ENGINEER					SIZE	SCALE			
D. HARRINGTON					D	1:1	SHEET	12	OF 26

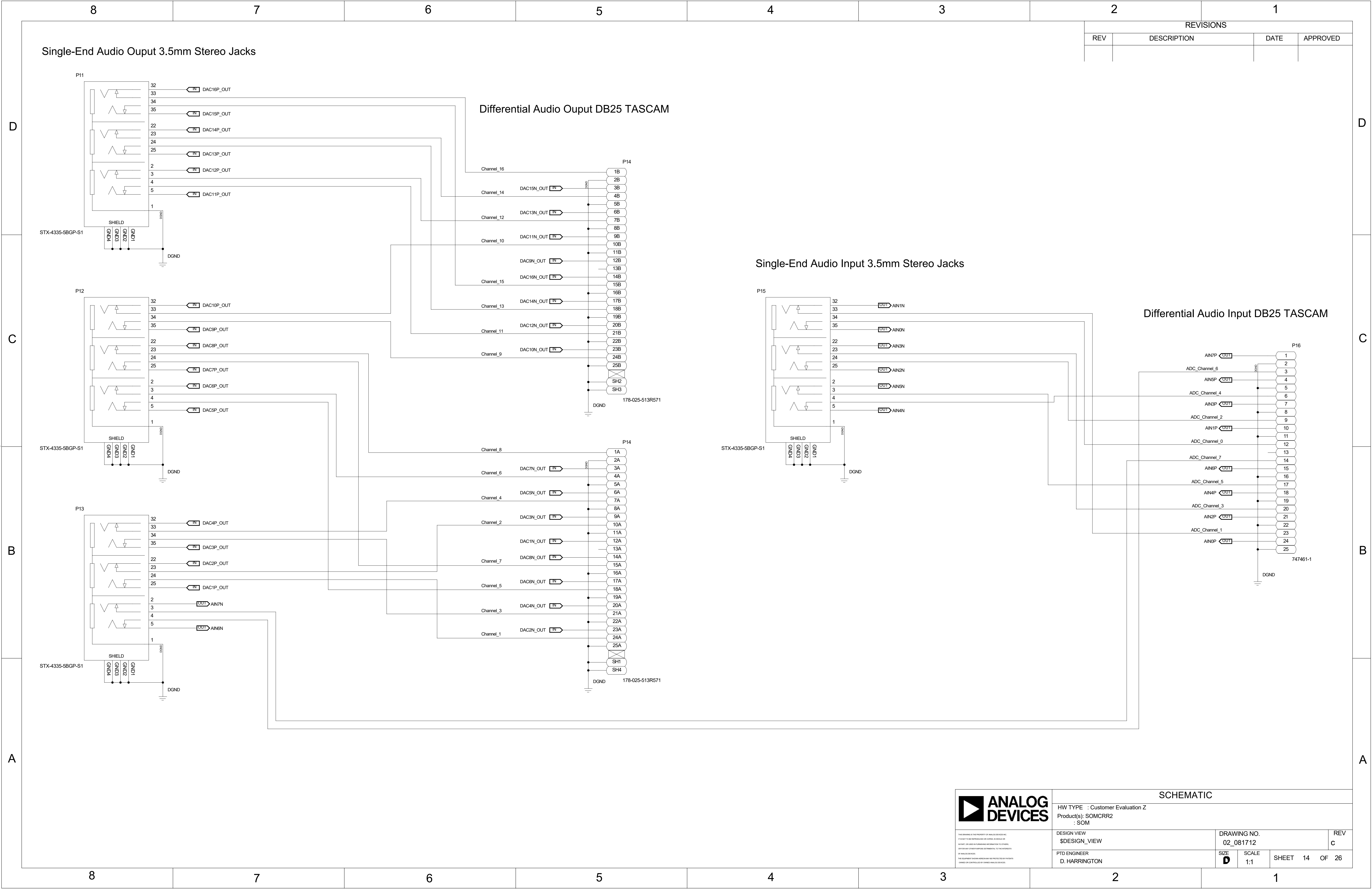


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



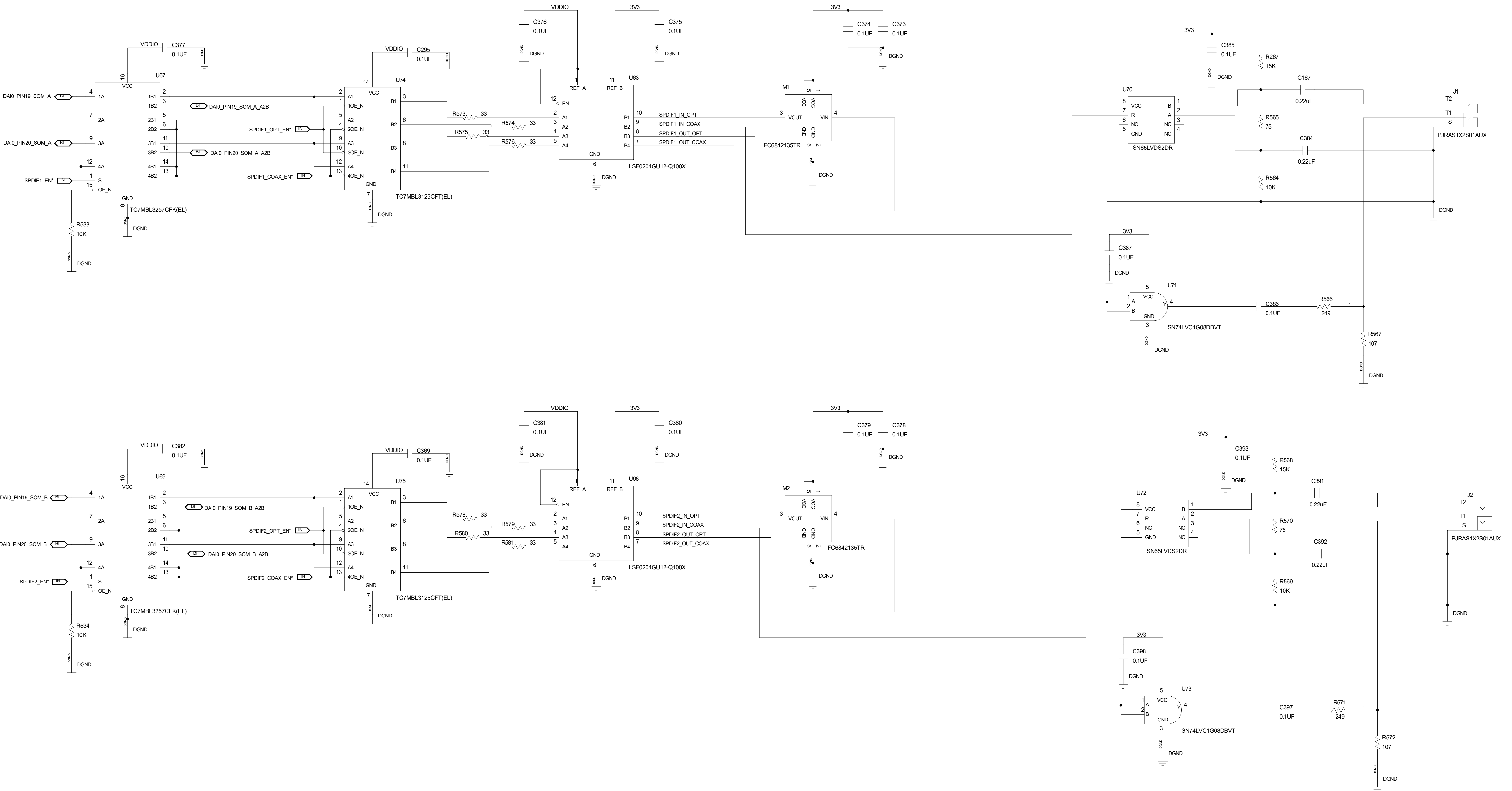
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SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM			
DESIGN VIEW \$DESIGN_VIEW		DRAWING NO. 02_081712	
PTD ENGINEER D. HARRINGTON		SIZE D	SCALE 1:1
SHEET 13 OF 26		REV c	



SPDIF

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



HW TYPE : Customer Evaluation Z
Product(s): SOMCRR2
: SOM

DESIGN VIEW
\$DESIGN_VIEW

PTD ENGINEER
D. HARRINGTON

DRAWING NO.
02_081712

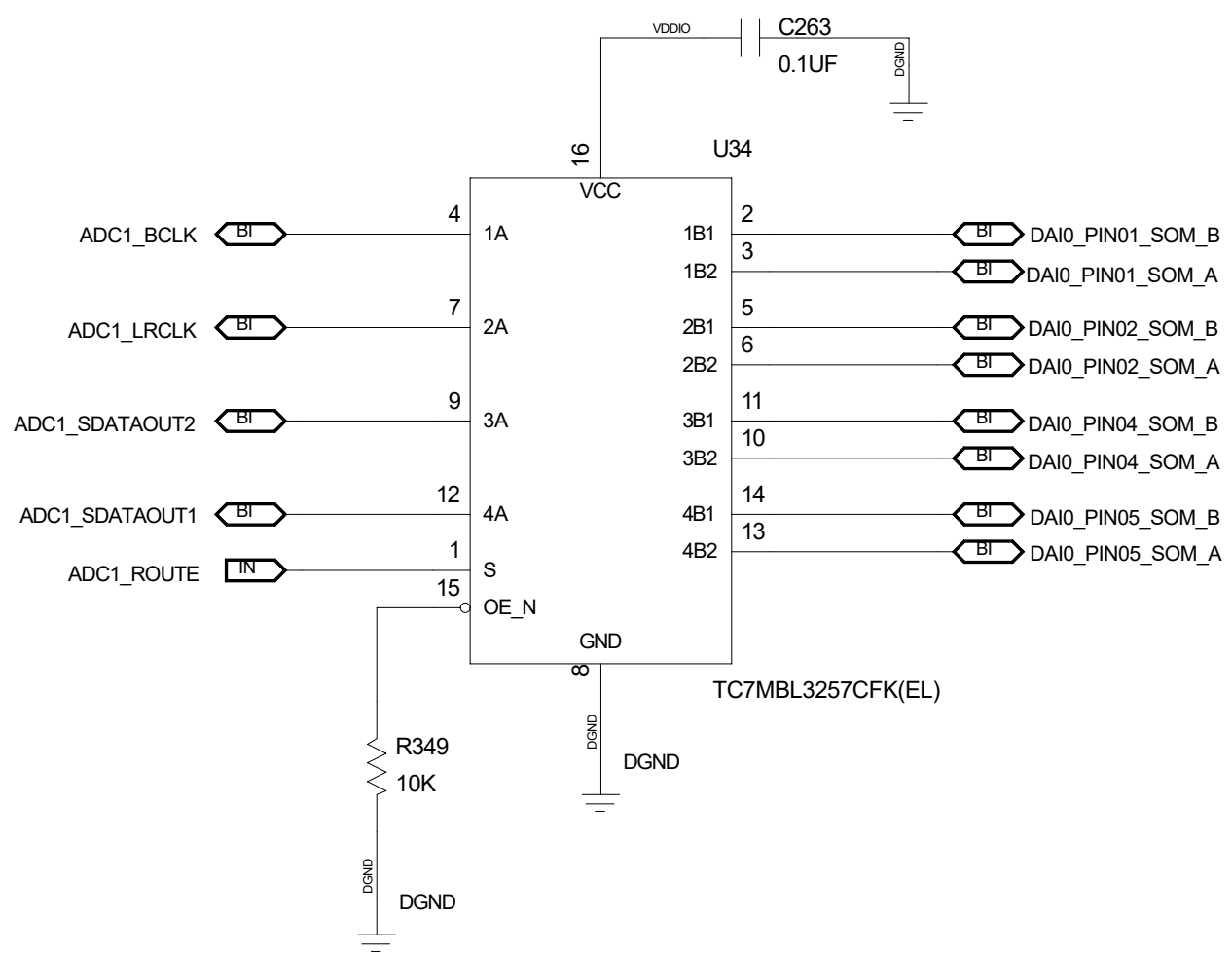
REV
c

SIZE D	SCALE 1:1	SHEET 15	OF 26
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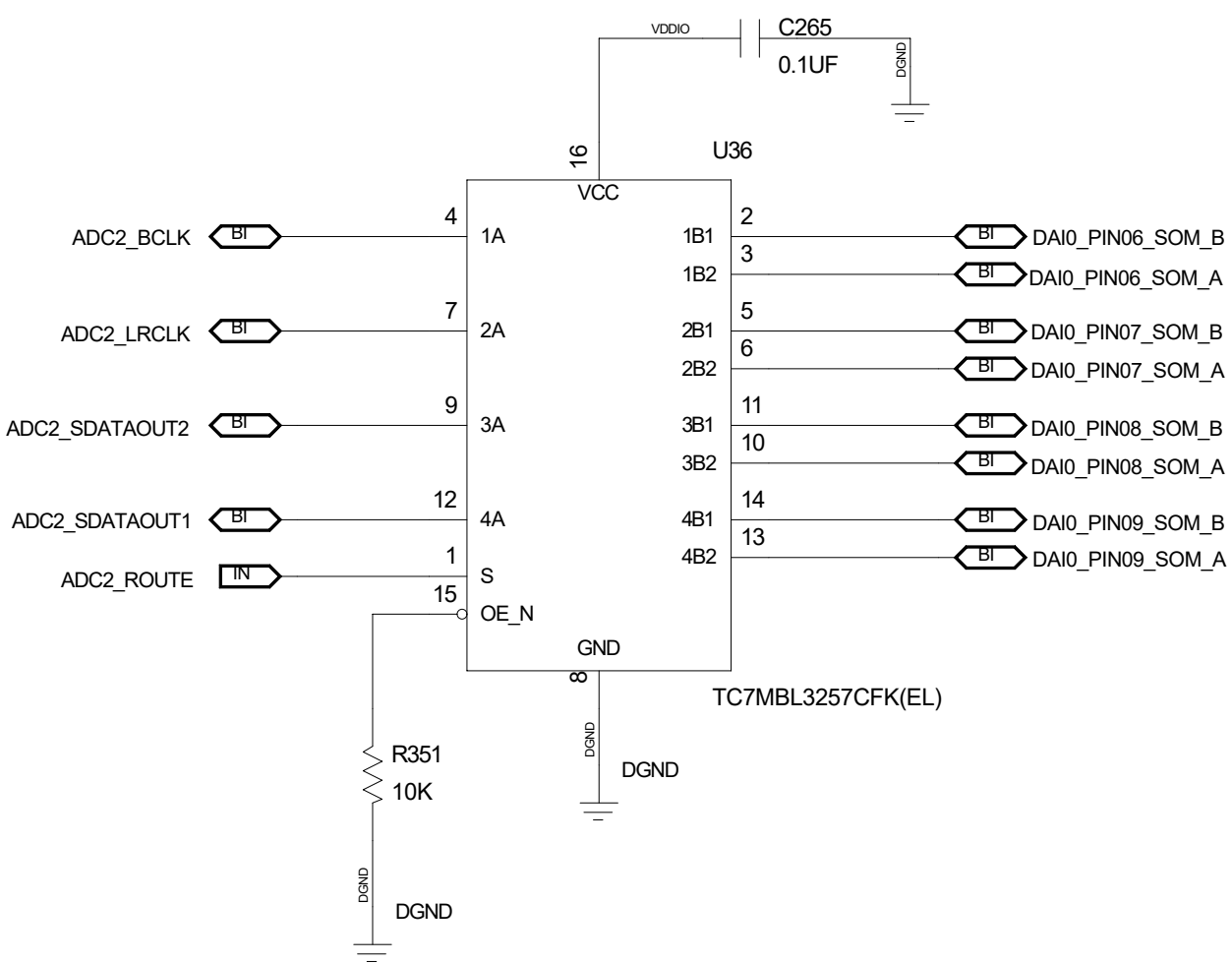
ADCs and DAC can be configured to either SOM. Defaults to SOM A

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

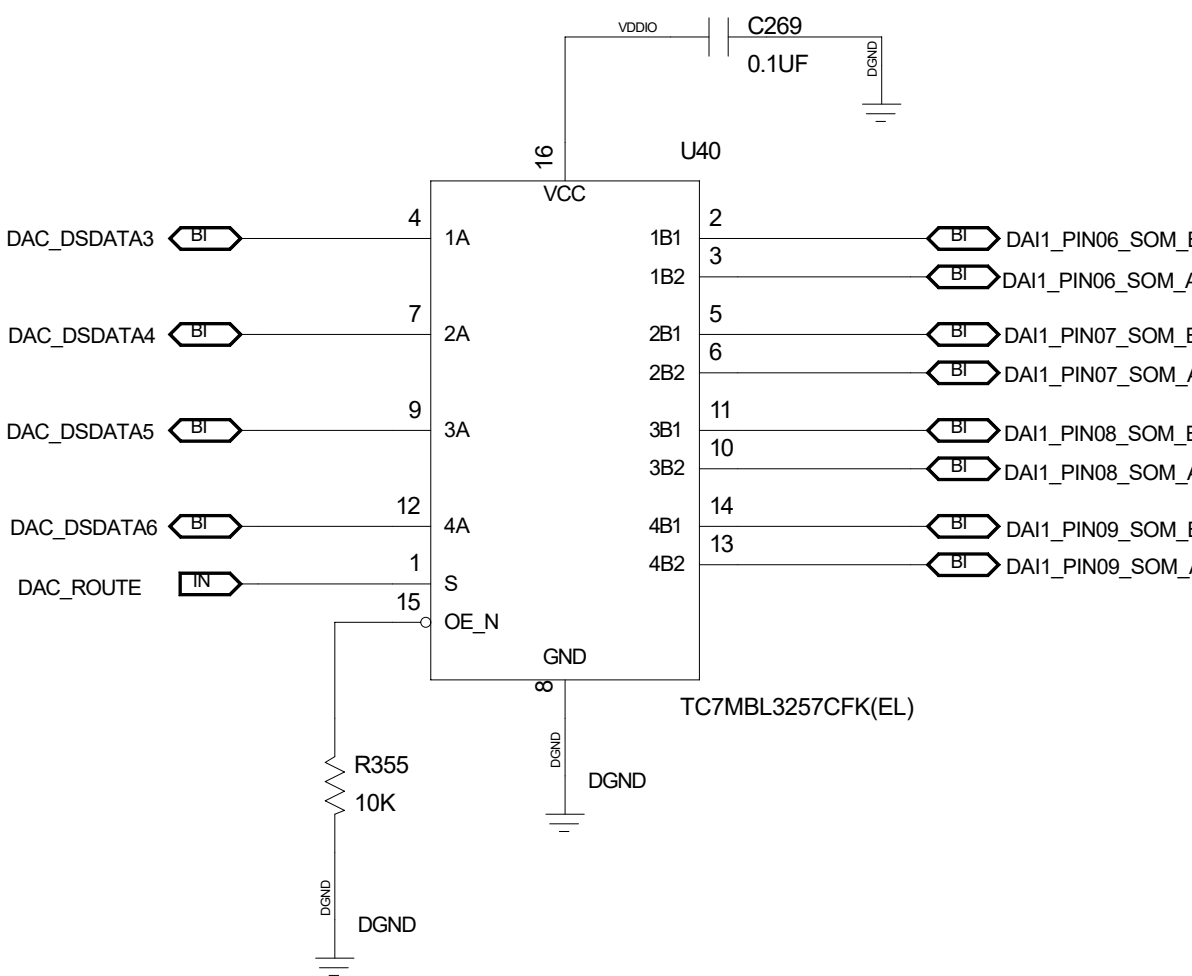
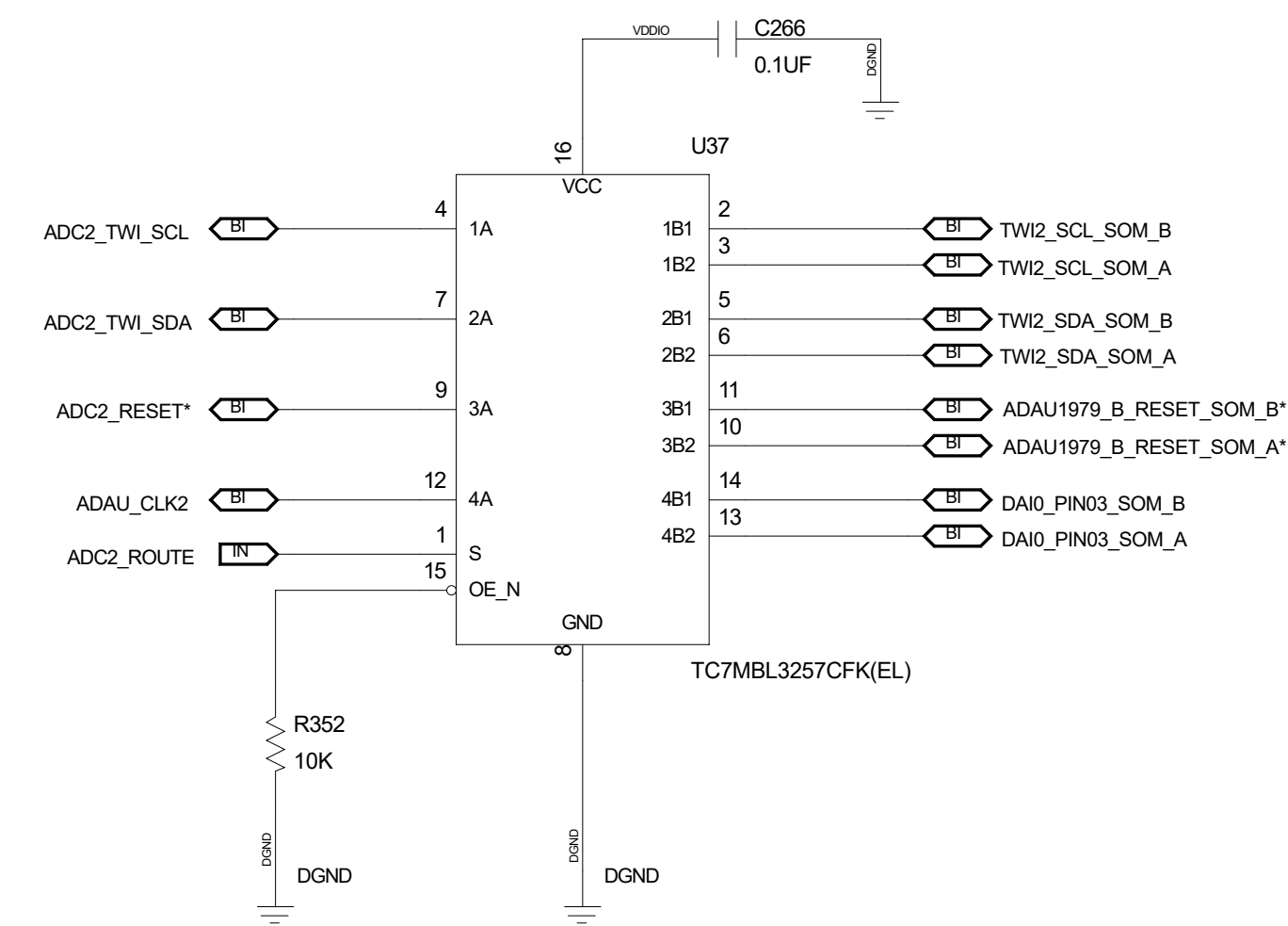
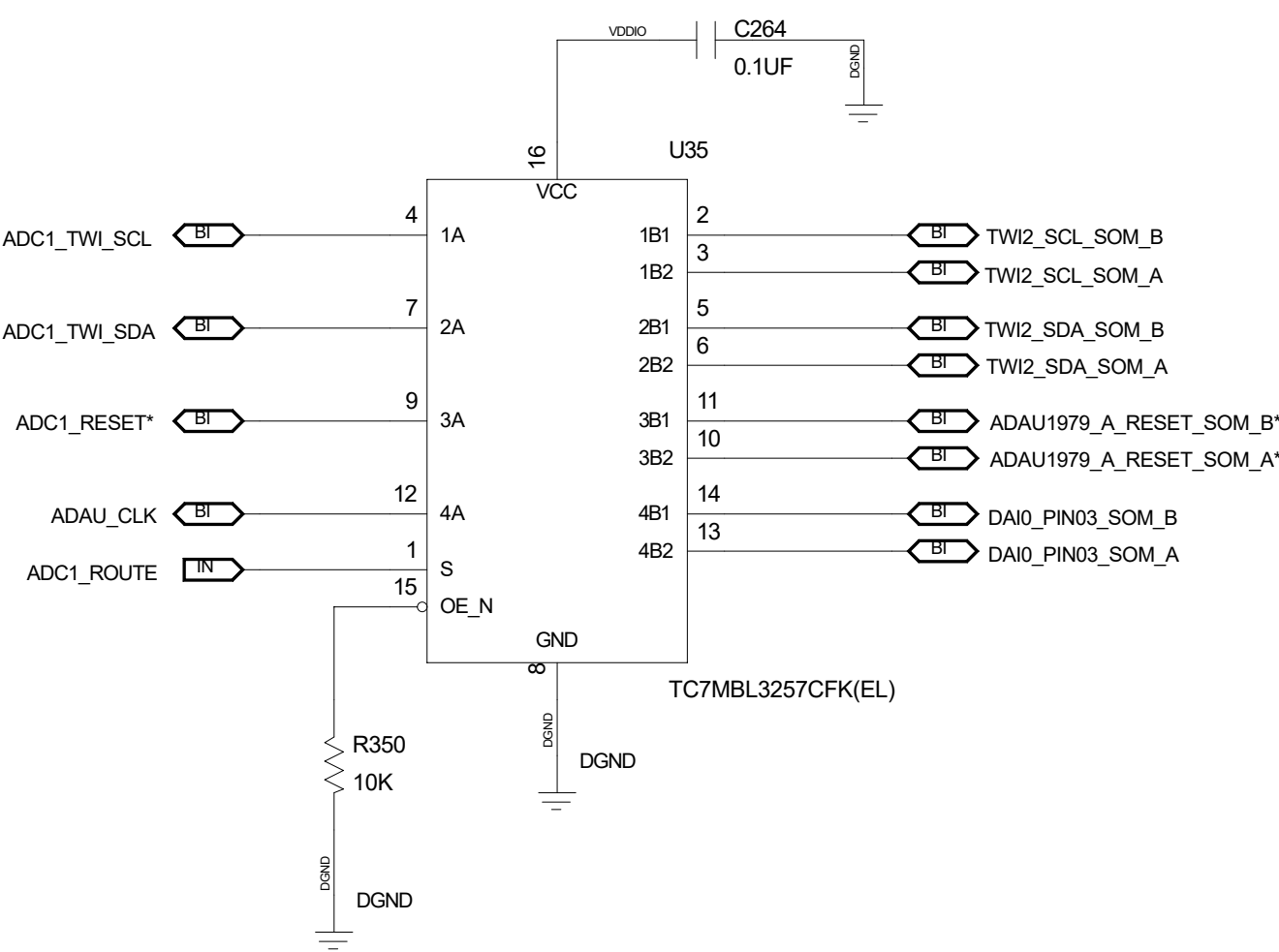
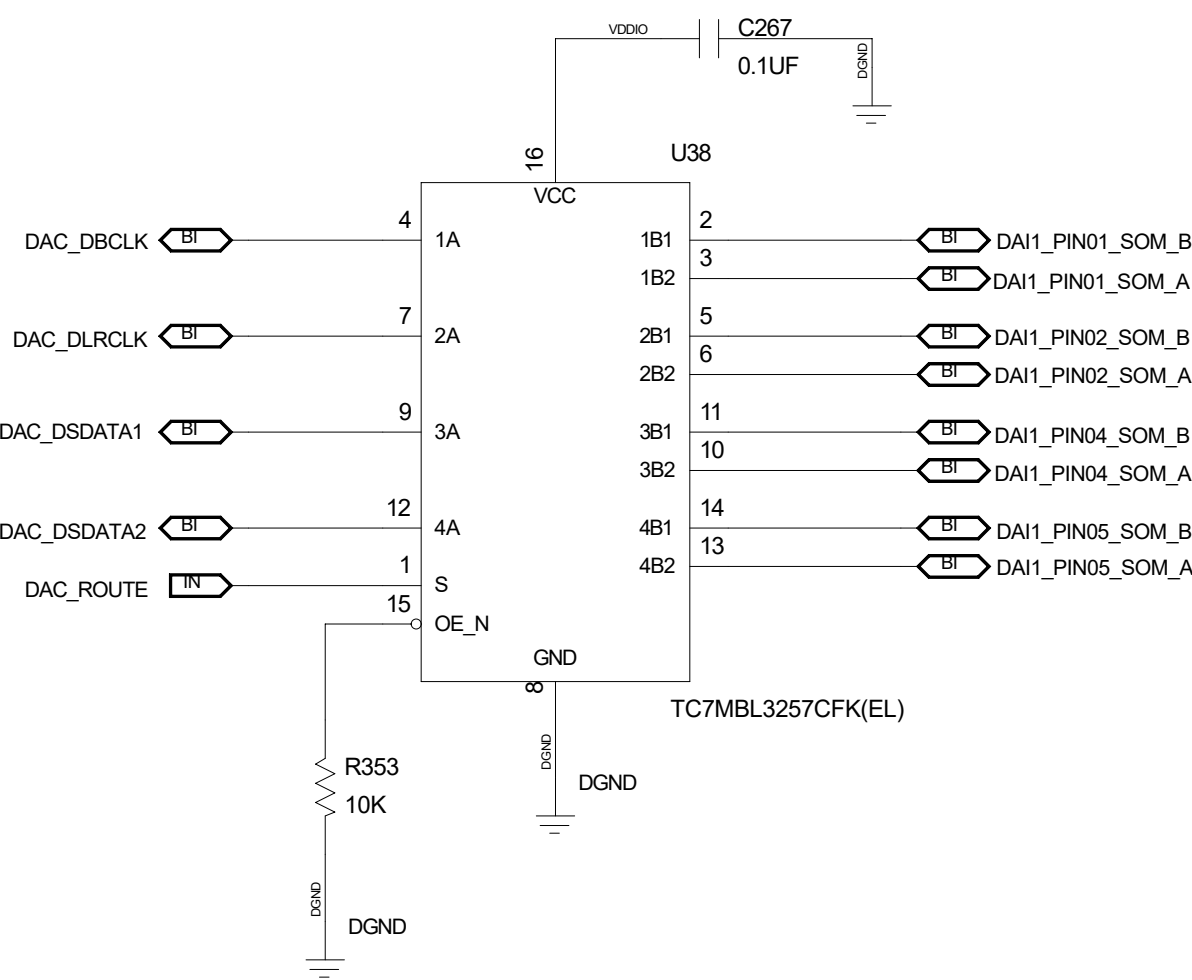
ADC #1



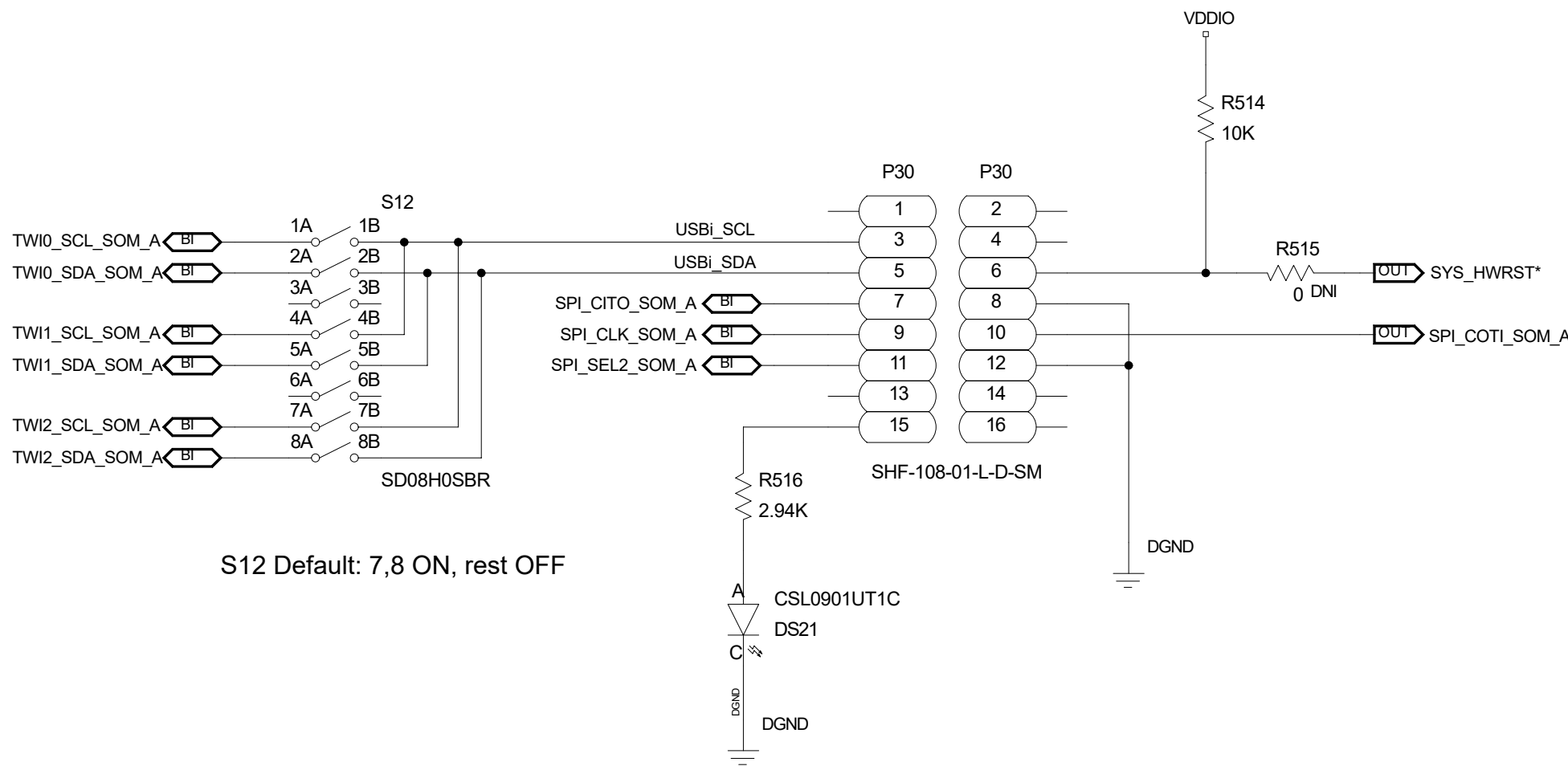
ADC #2



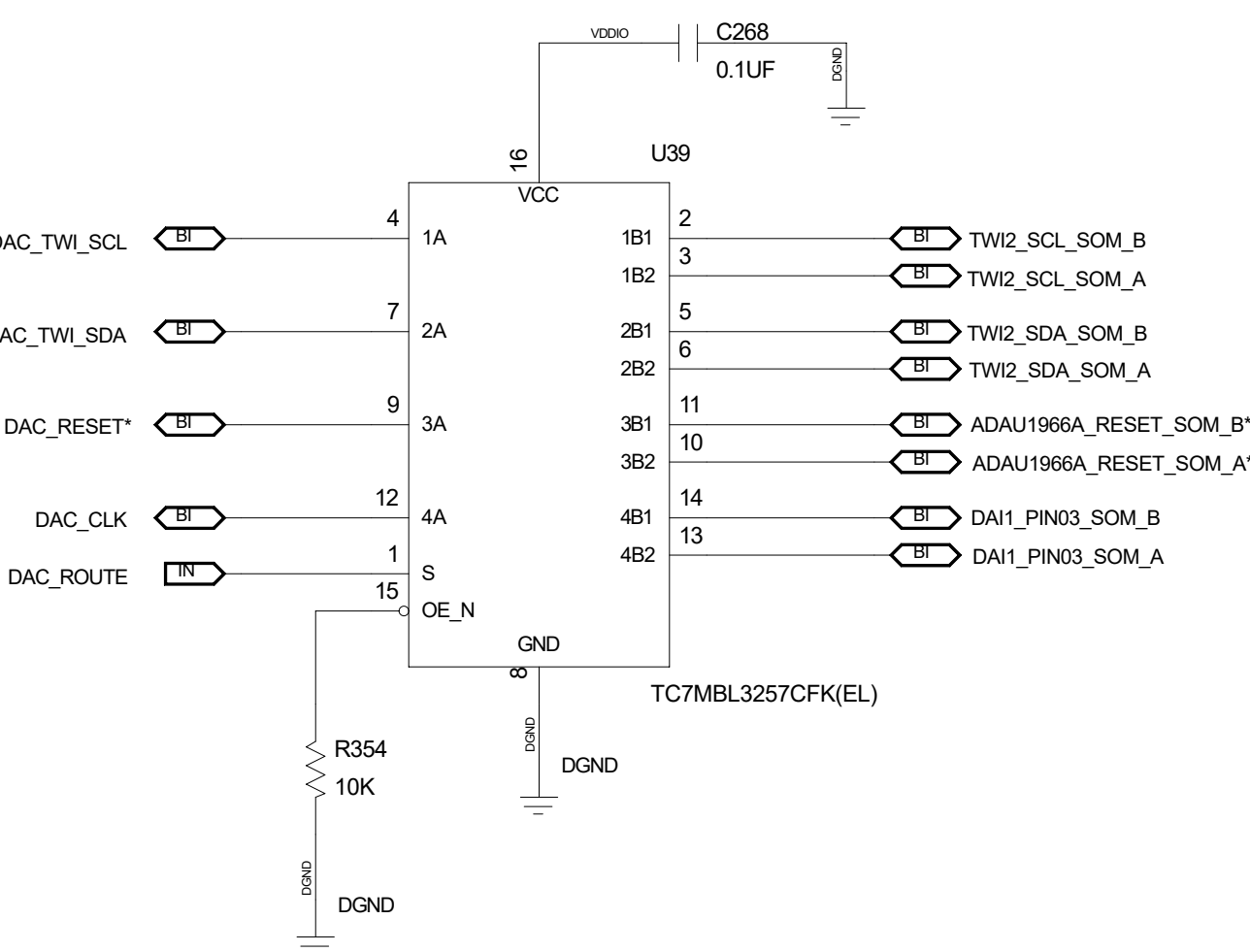
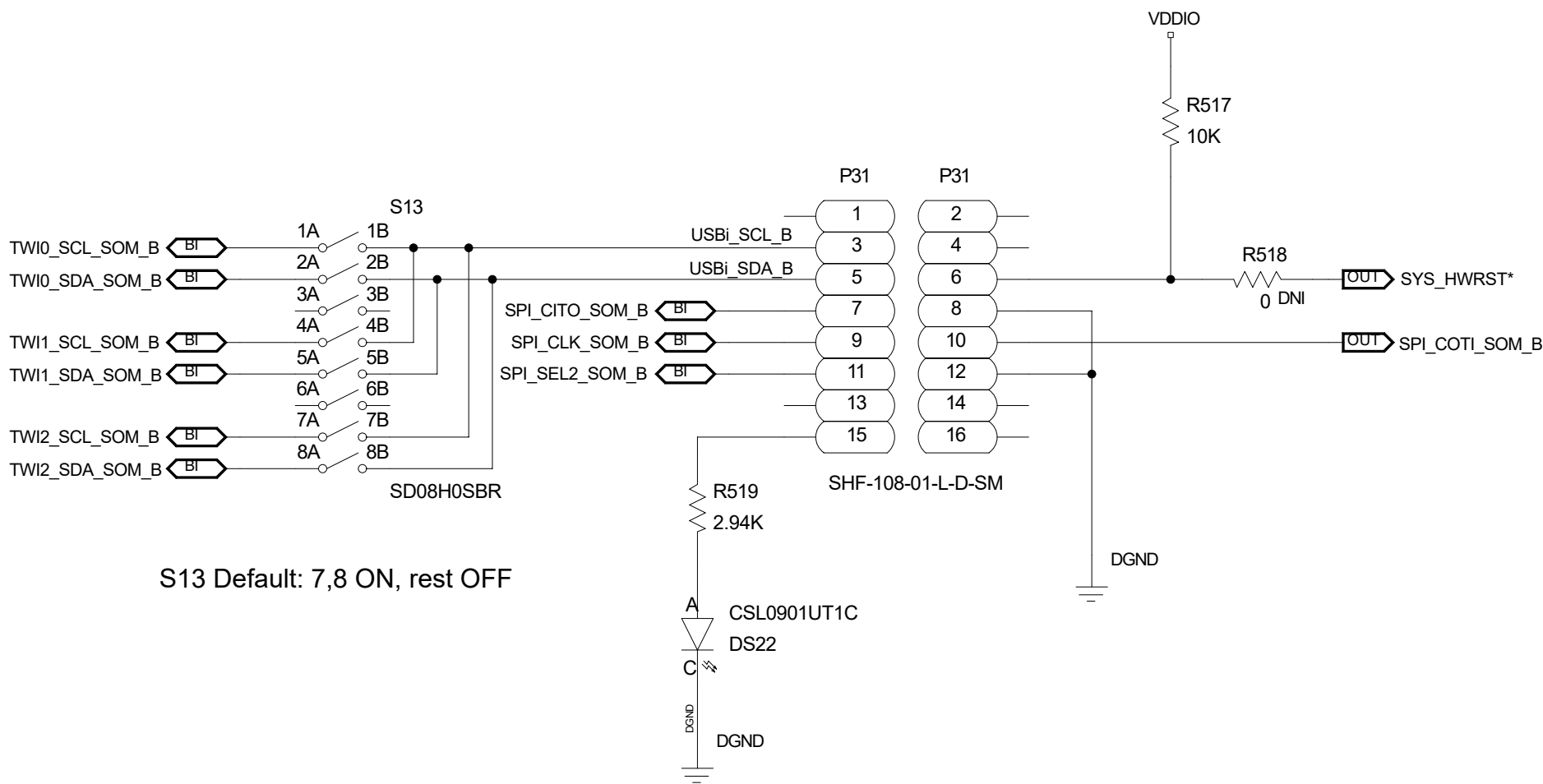
DAC



SigmaStudio Connector for SOM A



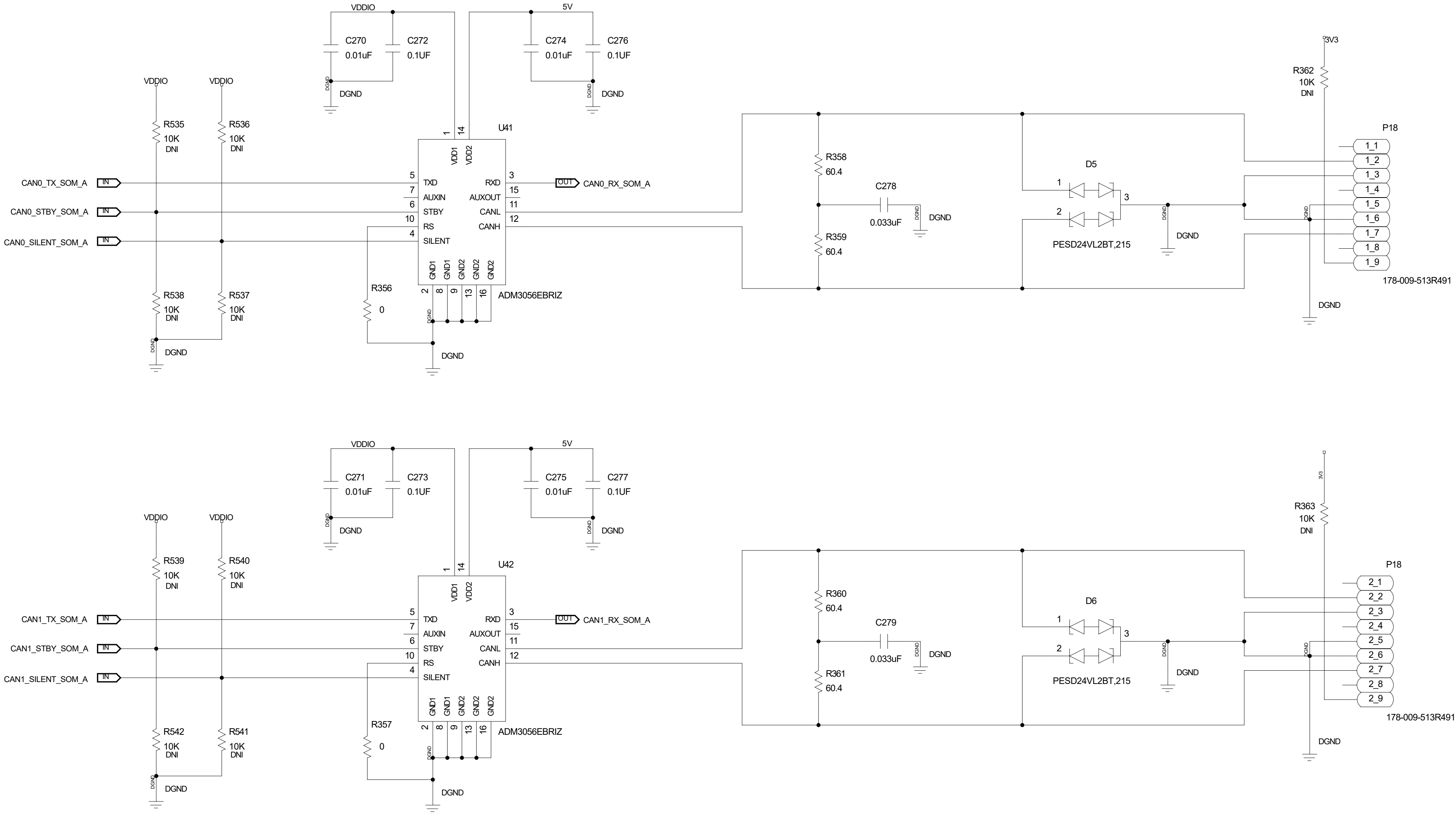
SigmaStudio Connector for SOM B



SCHEMATIC			
	HW TYPE : Customer Evaluation Z		
	Product(s) : SOMCRR2		
	: SOM		
	DESIGN VIEW		
\$DESIGN_VIEW		DRAWING NO.	
PTD ENGINEER		02_081712	
D. HARRINGTON		REV	
SIZE		c	
D		1:1	
SHEET		16 OF 26	

CAN for SOM A

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

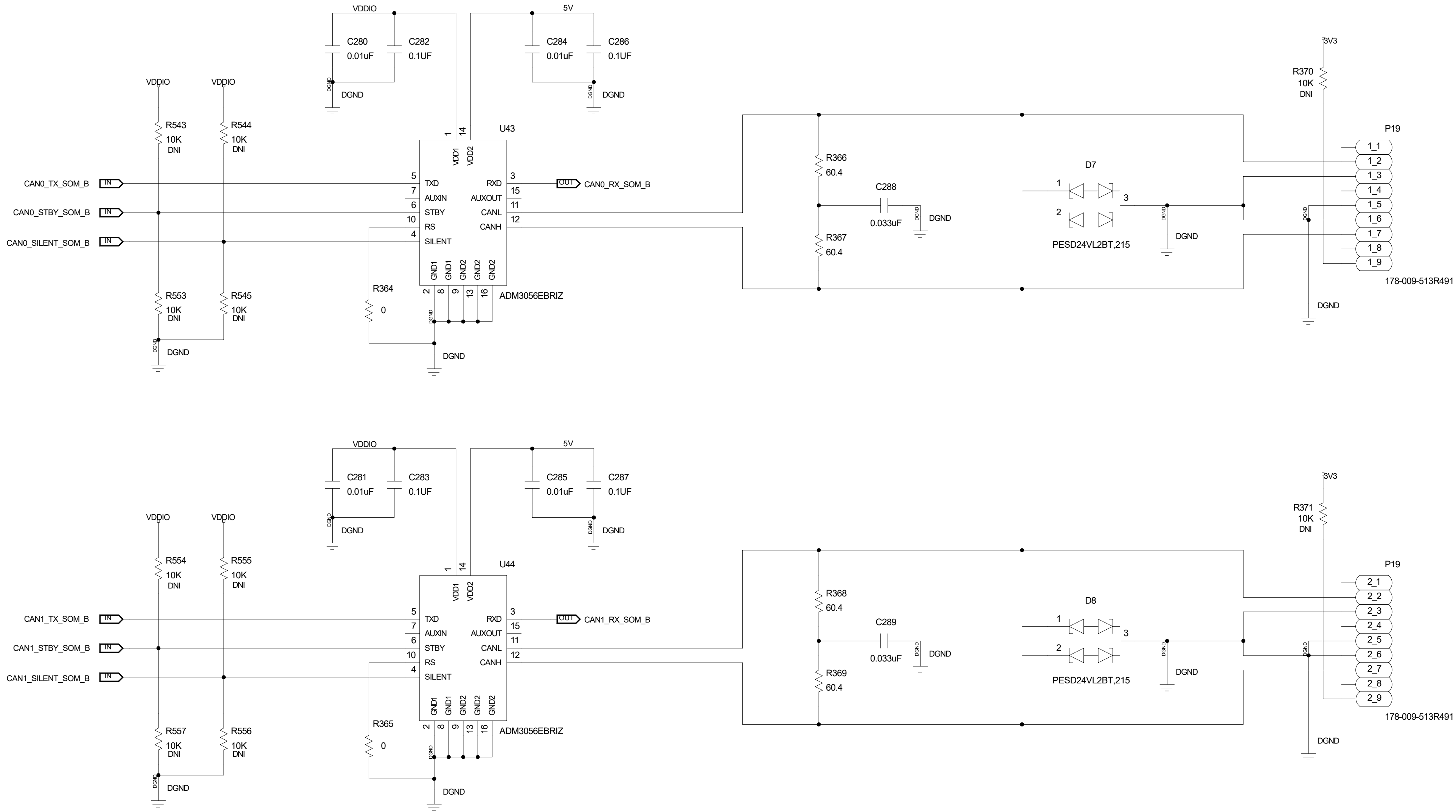


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SCHEMATIC					
HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM					
DESIGN VIEW \$DESIGN_VIEW			DRAWING NO. 02_081712		REV c
PTD ENGINEER D. HARRINGTON			SIZE D	SCALE 1:1	SHEET 17 OF 26

CAN for SOM B

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

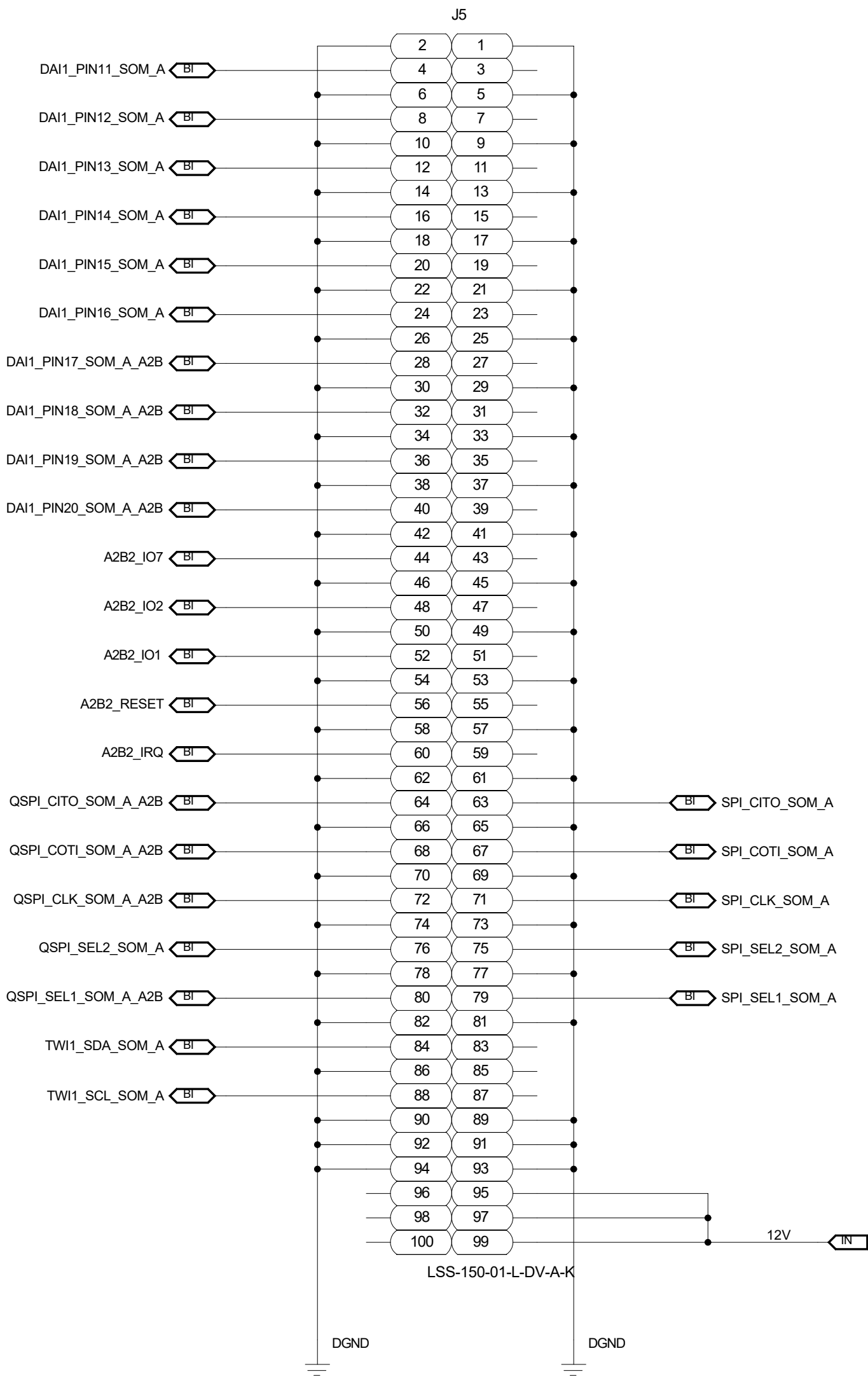
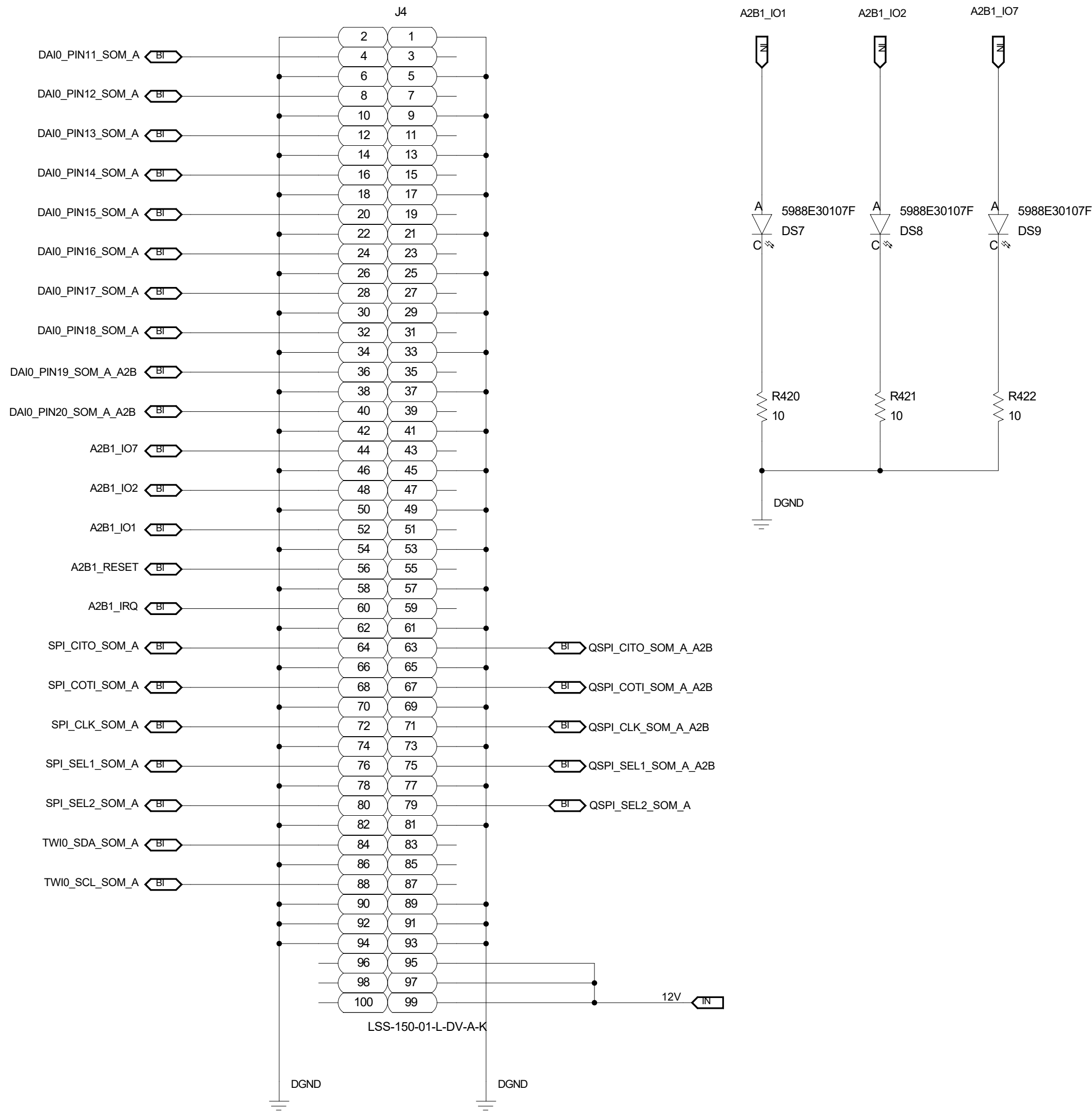


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SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM			
DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_081712		REV c
PTD ENGINEER D. HARRINGTON	SIZE D	SCALE 1:1	SHEET 18 OF 26

A2B Interface for SOM A

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED





HW TYPE : Customer Evaluation Z
Product(s): SOMCRR2
: SOM

DESIGN VIEW
\$DESIGN_VIEW

PTD ENGINEER
D. HARRINGTON

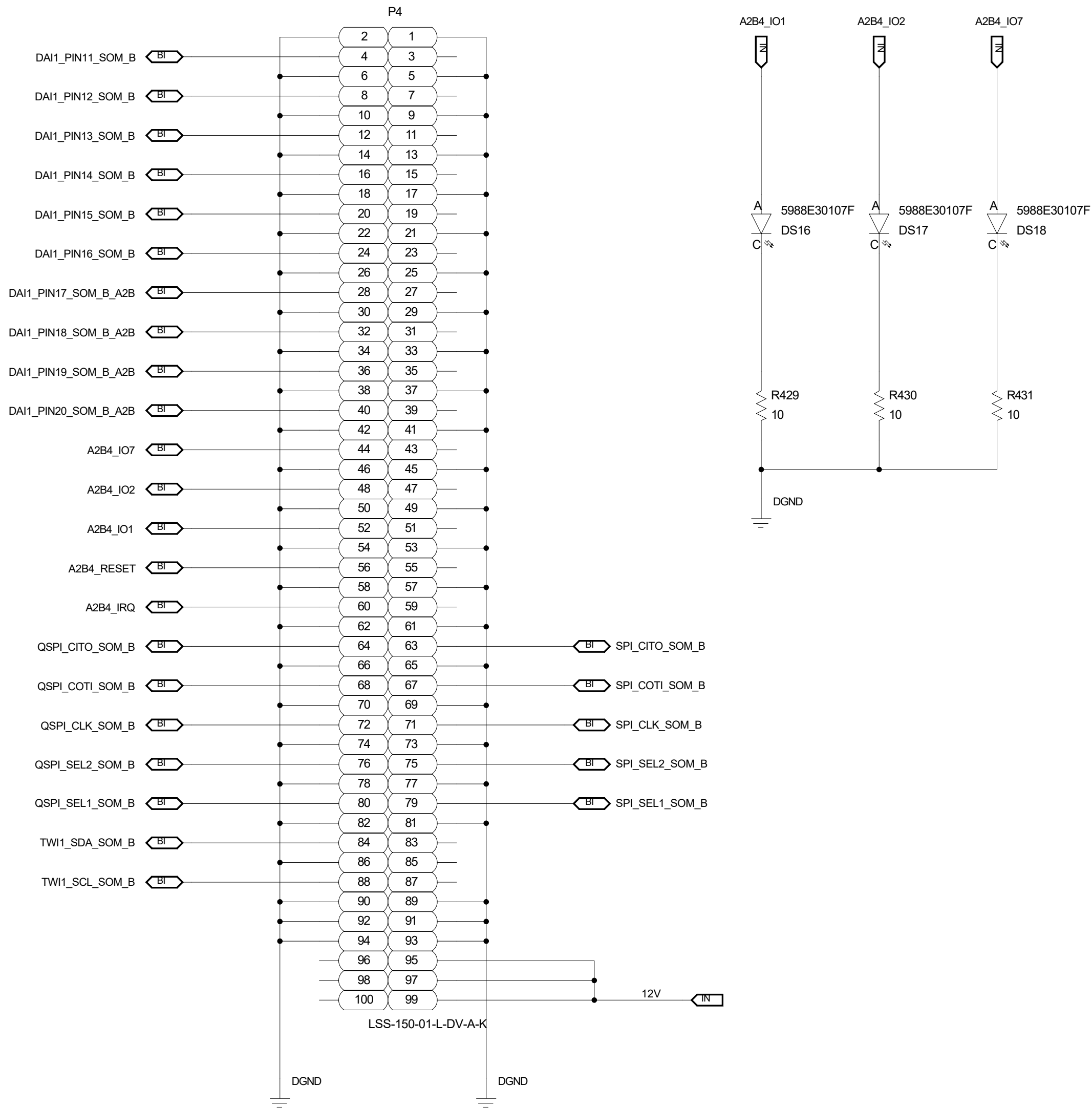
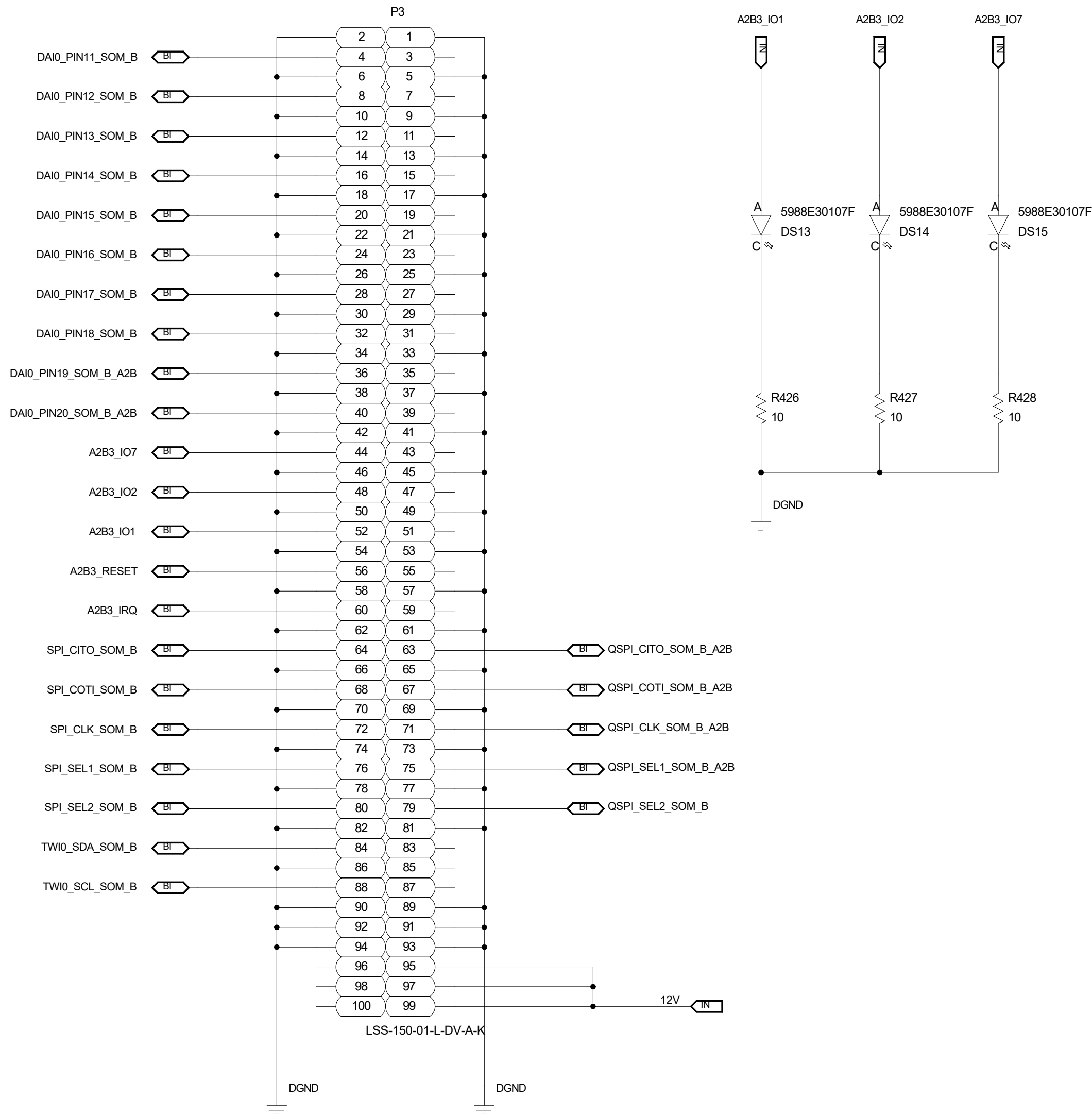
DRAWING NO.
02_081712

REV
c

SIZE D	SCALE 1:1	SHEET 20 OF 26
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A2B Interface for SOM B

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED





ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation Z
Product(s): SOMCRR2
: SOM

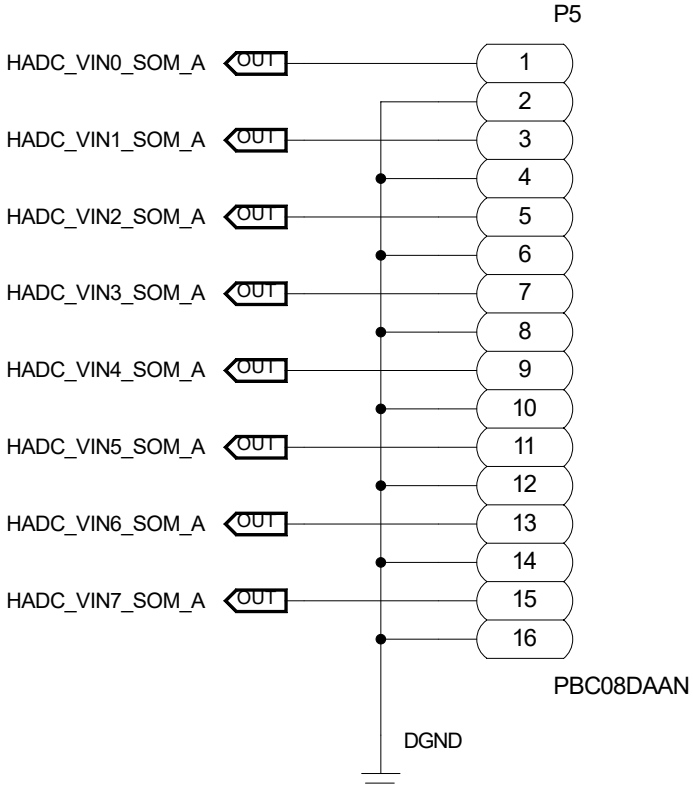
DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_081712	REV c
PTD ENGINEER D. HARRINGTON	SIZE D	SCALE 1:1
SHEET 21 OF 26		

GPIO DAI Headers

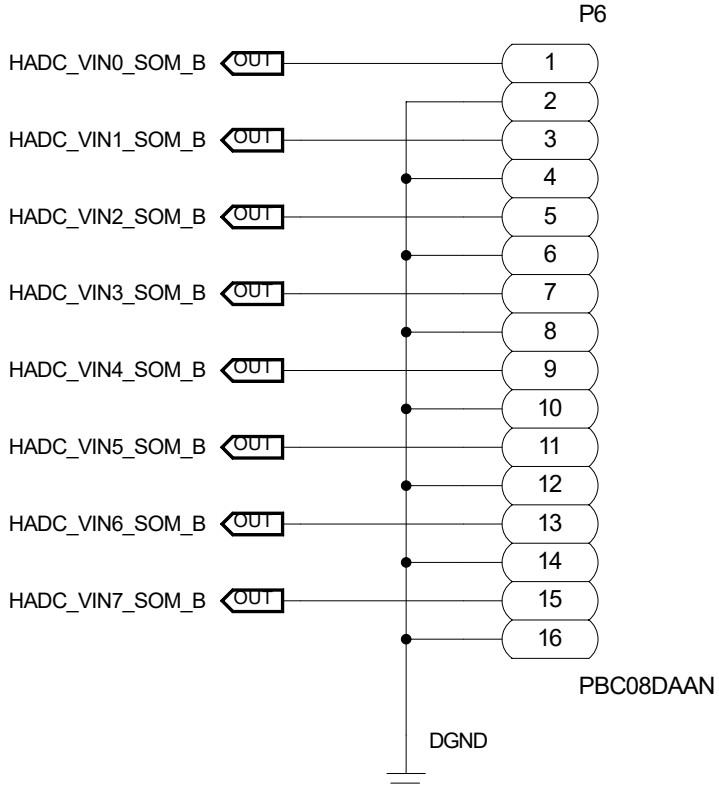
I/O Voltage 1.8v tolerant only.

SOM A

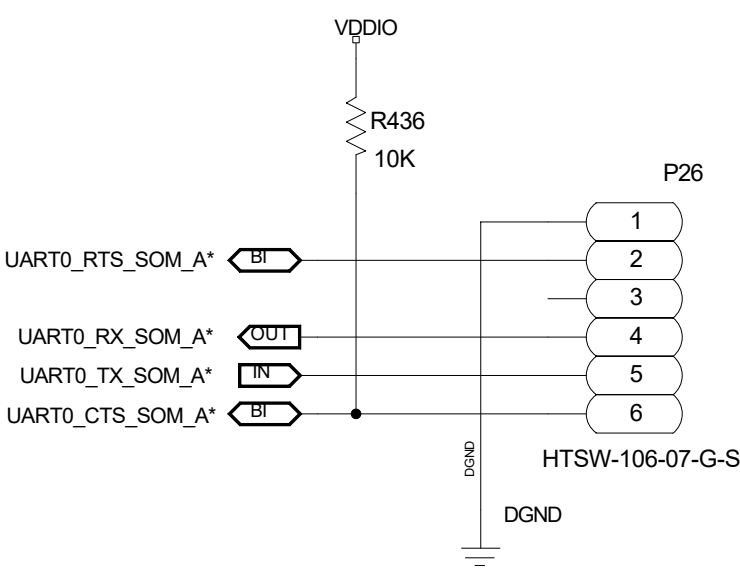
HADC SOM A



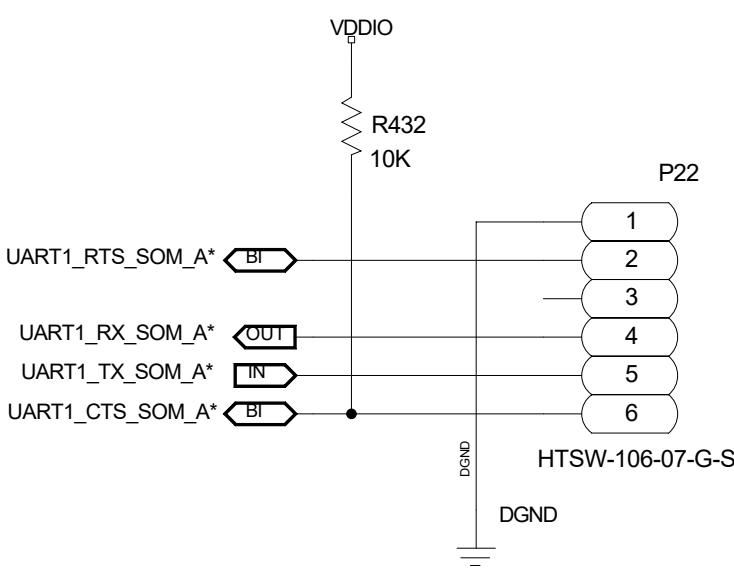
HADC SOM B



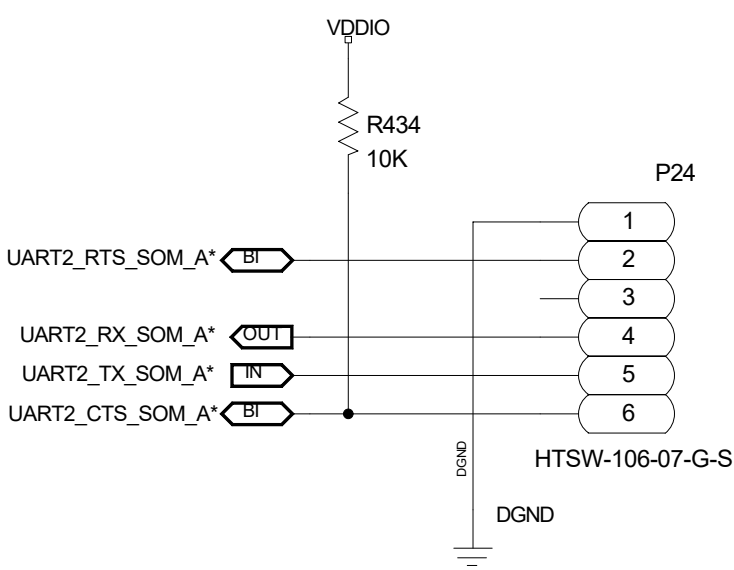
UART0



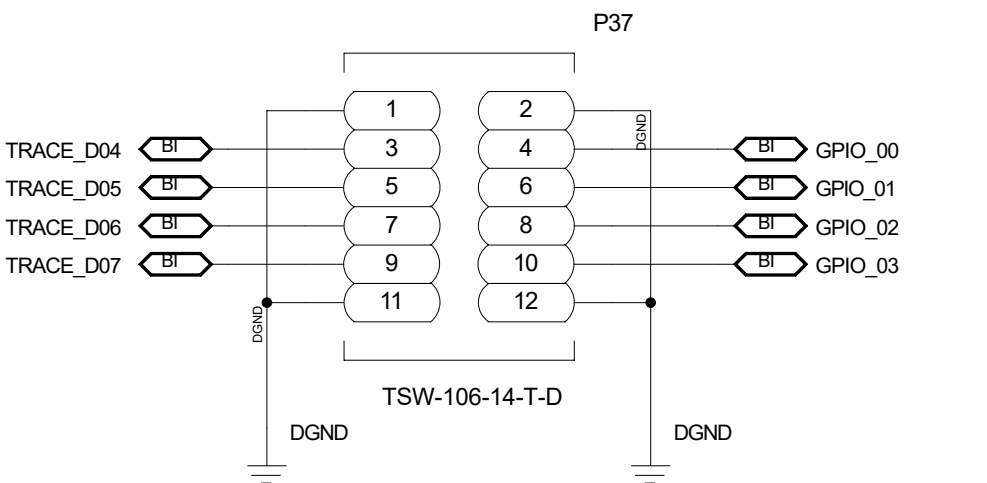
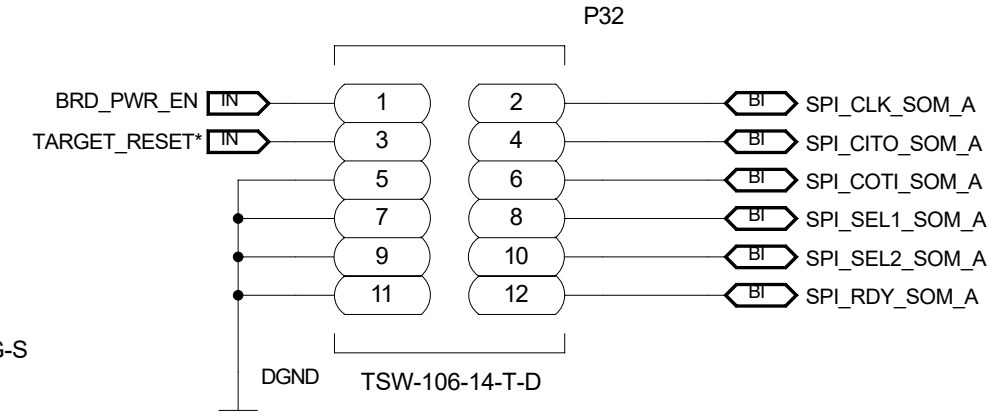
UART1



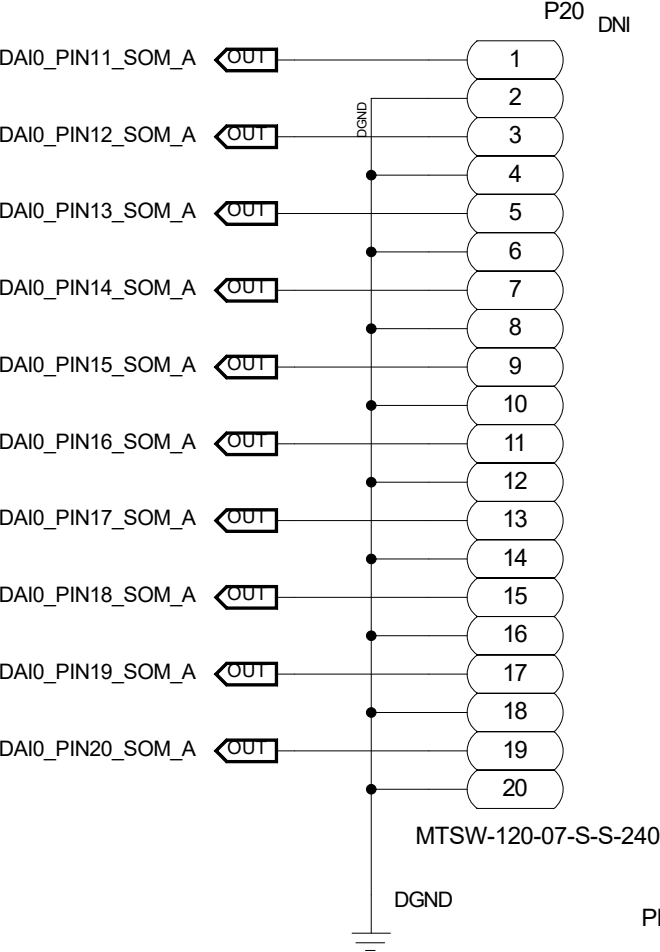
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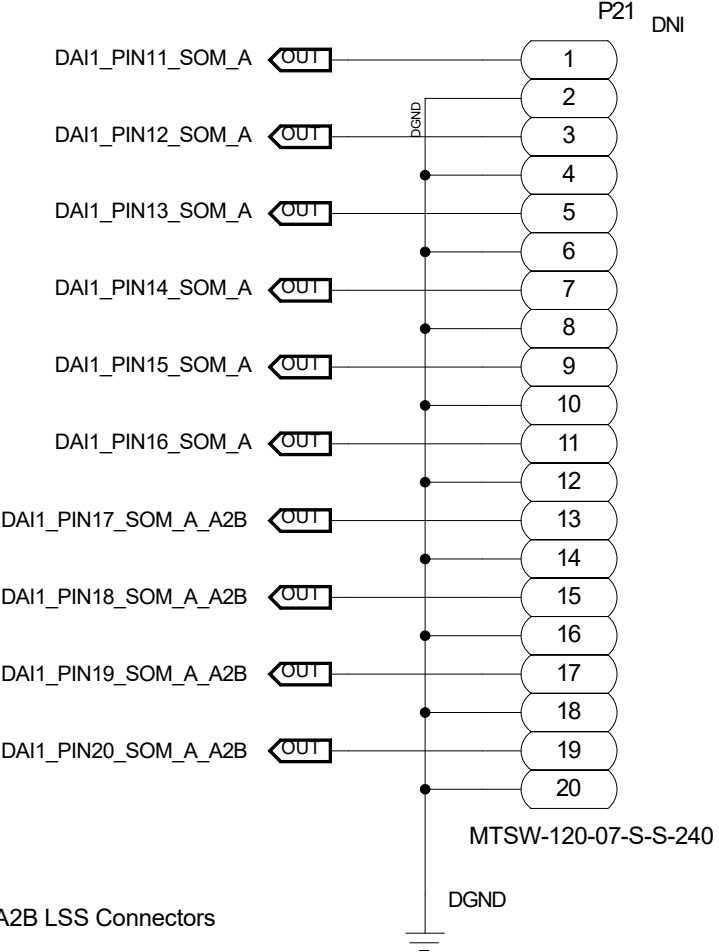
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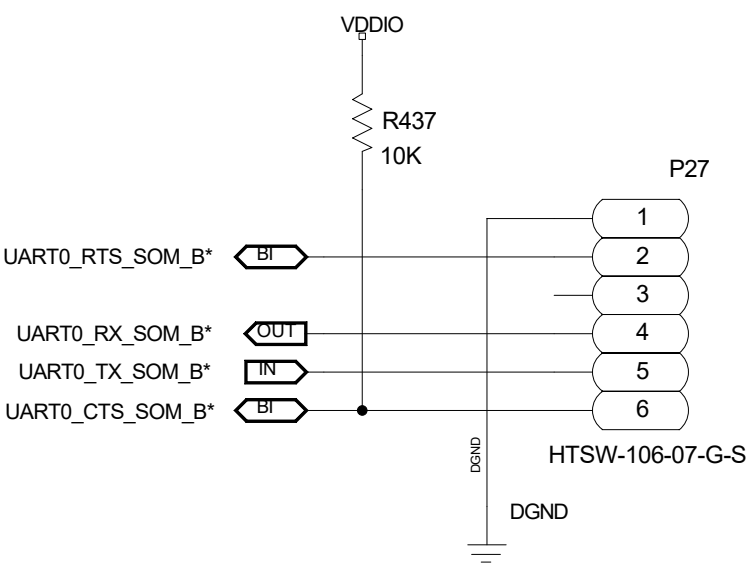
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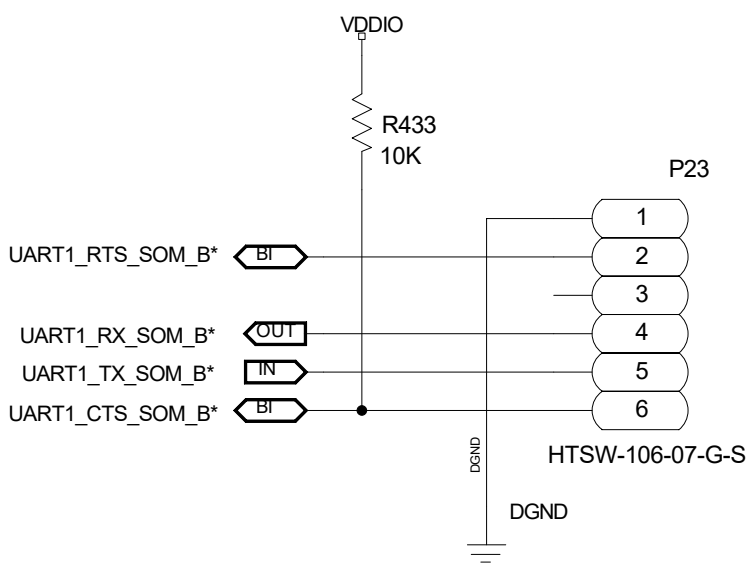
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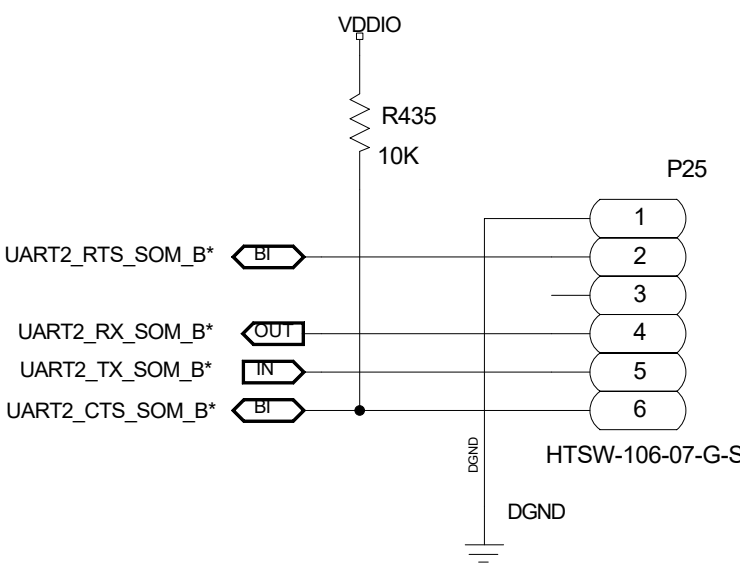
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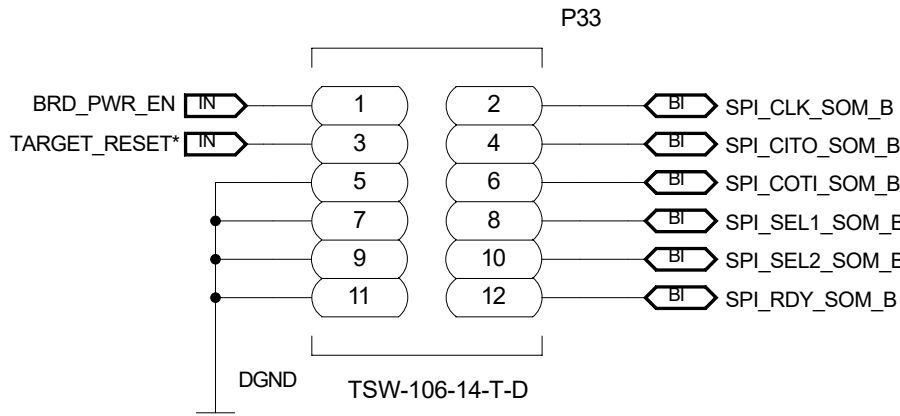
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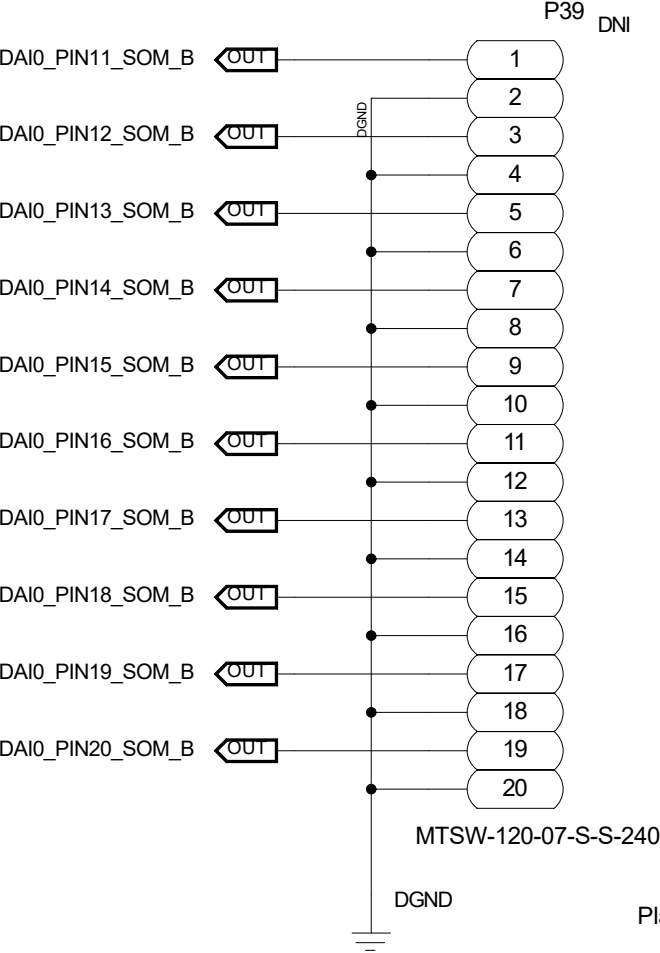
UART2



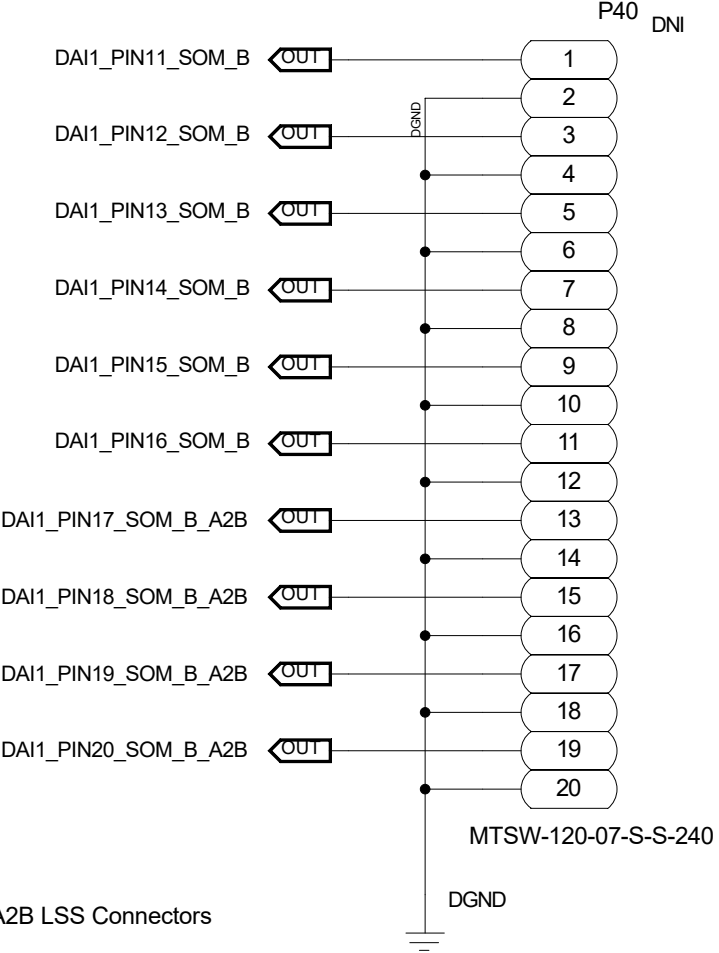
SPI



DAI0 SOM B



DAI1 SOM B



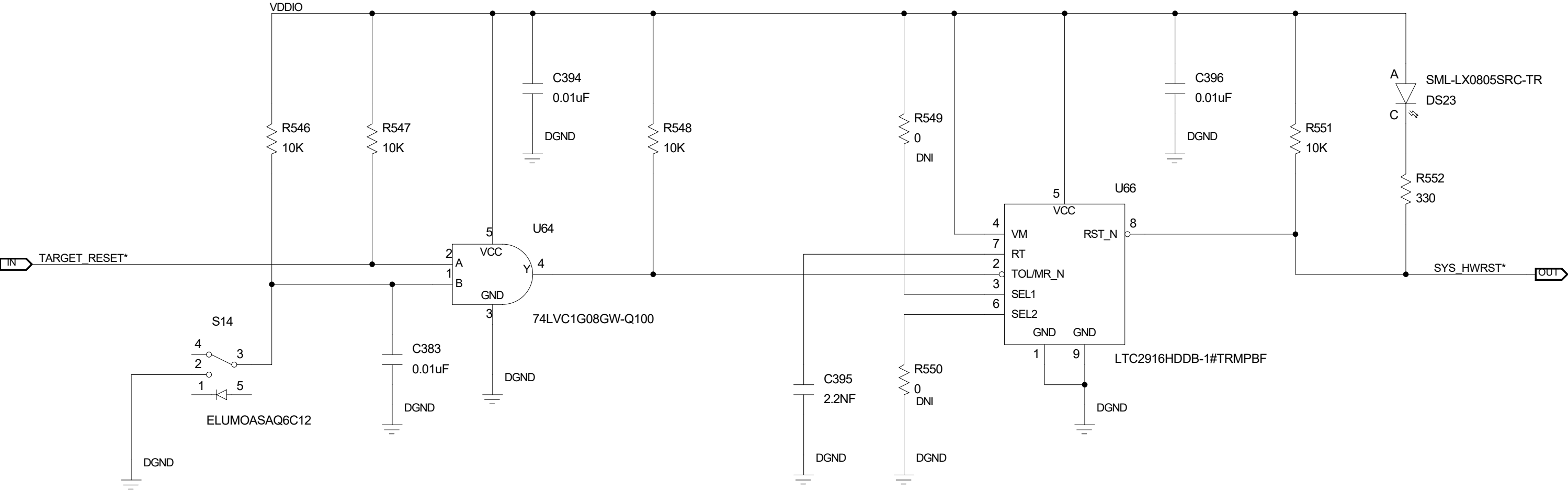
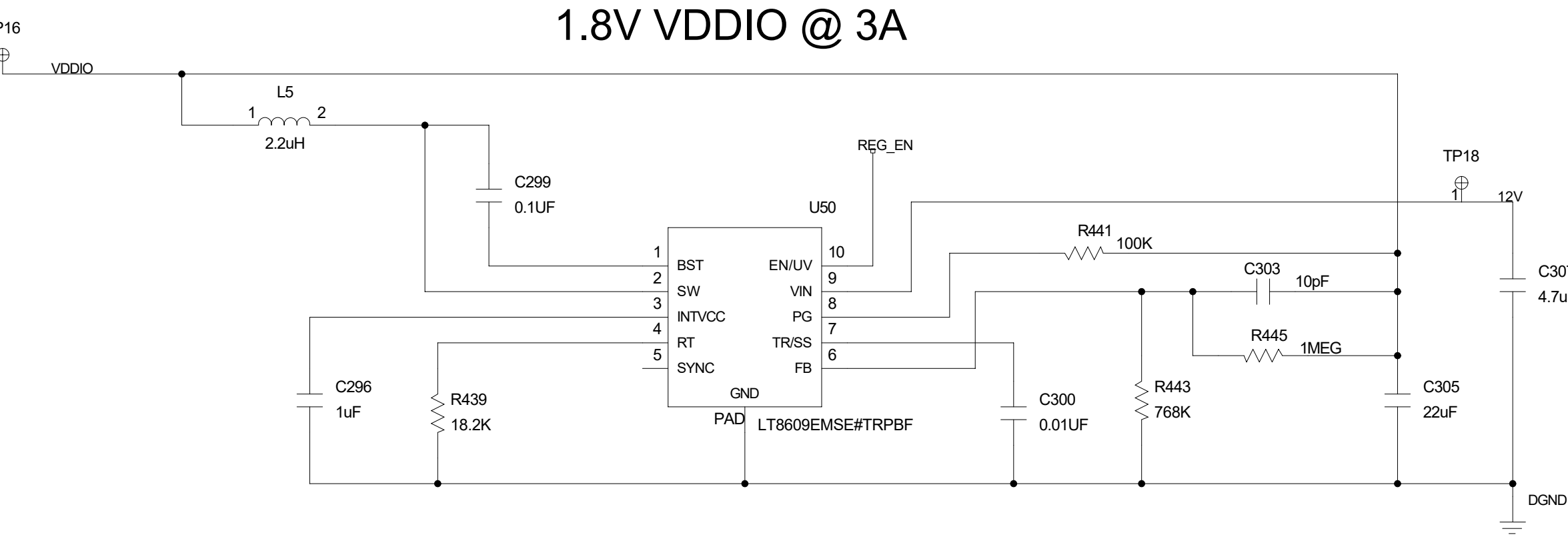
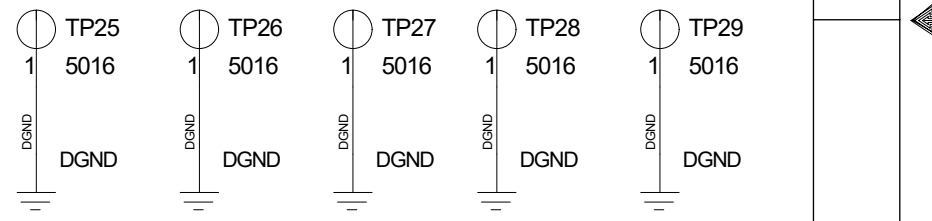
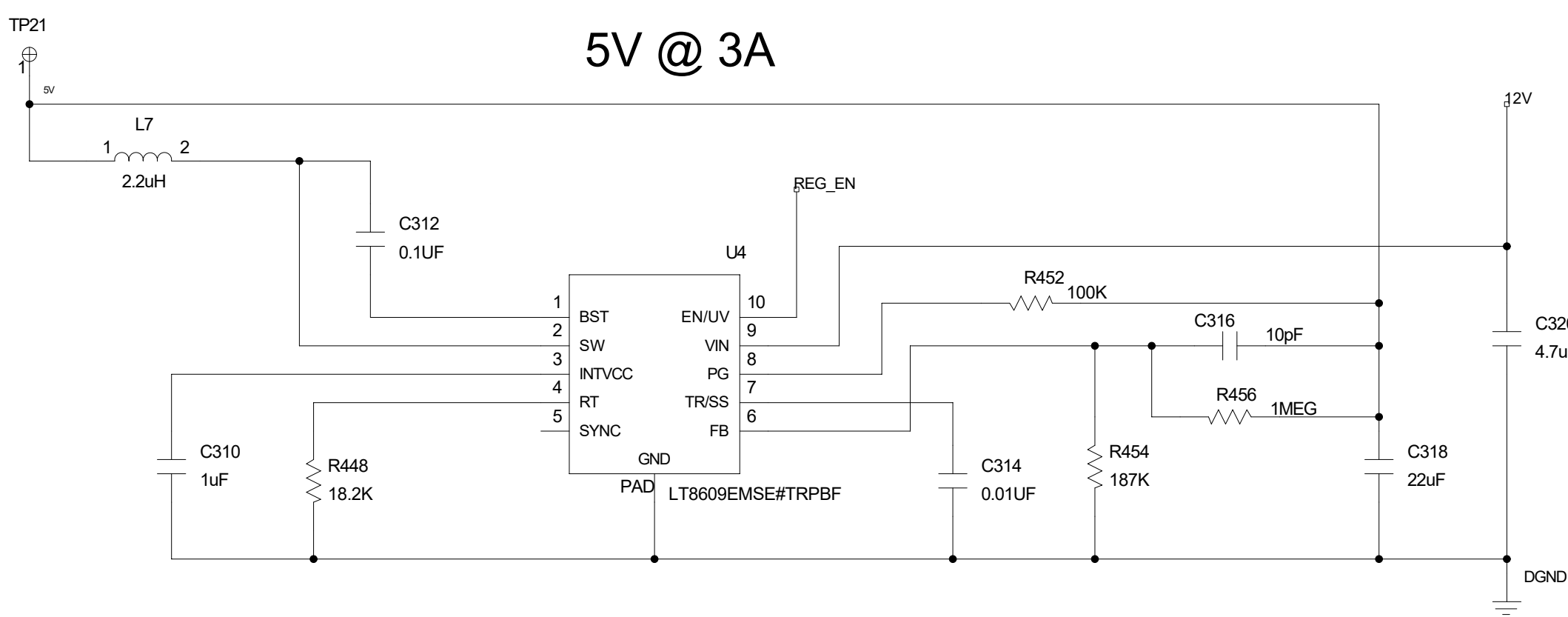
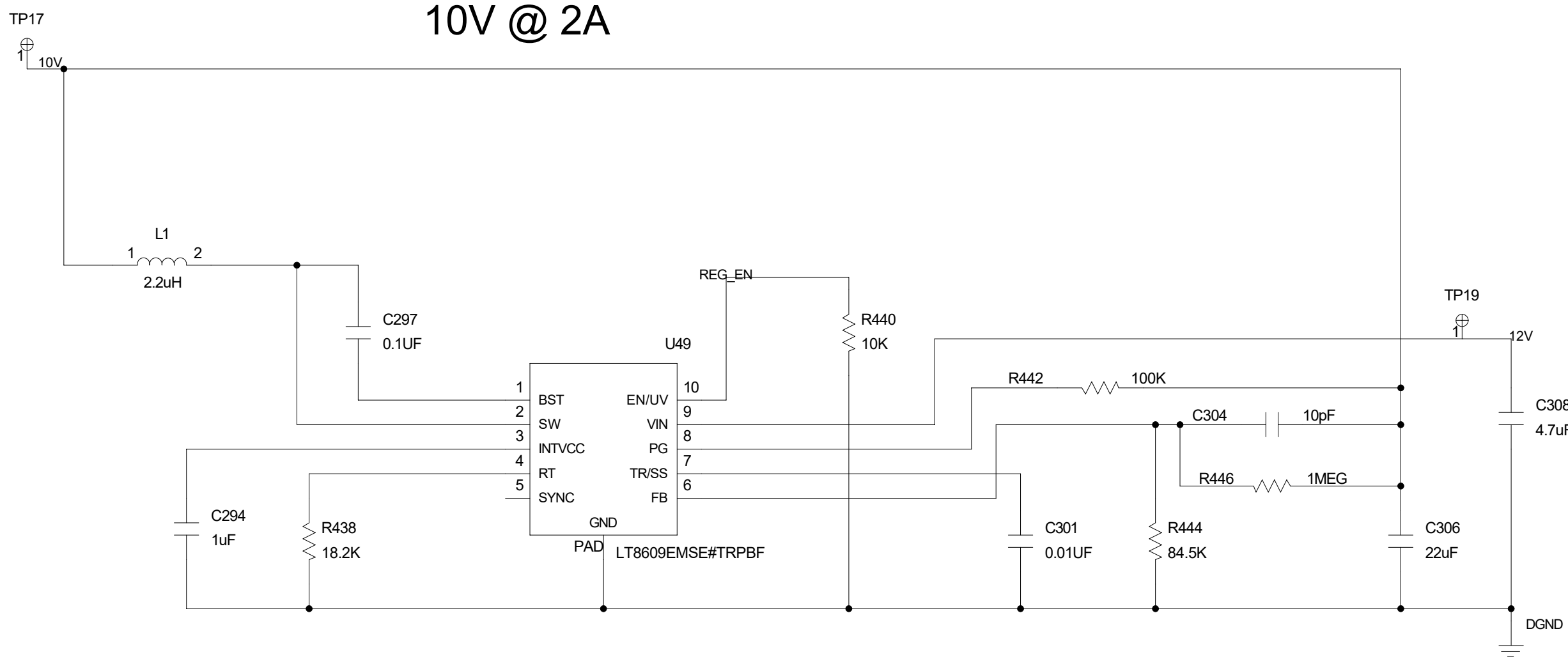
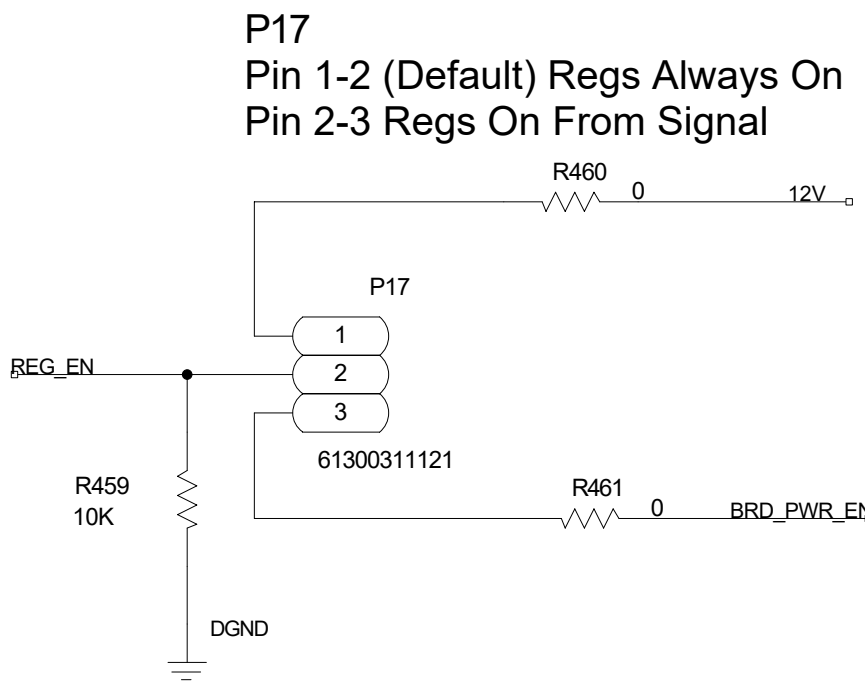
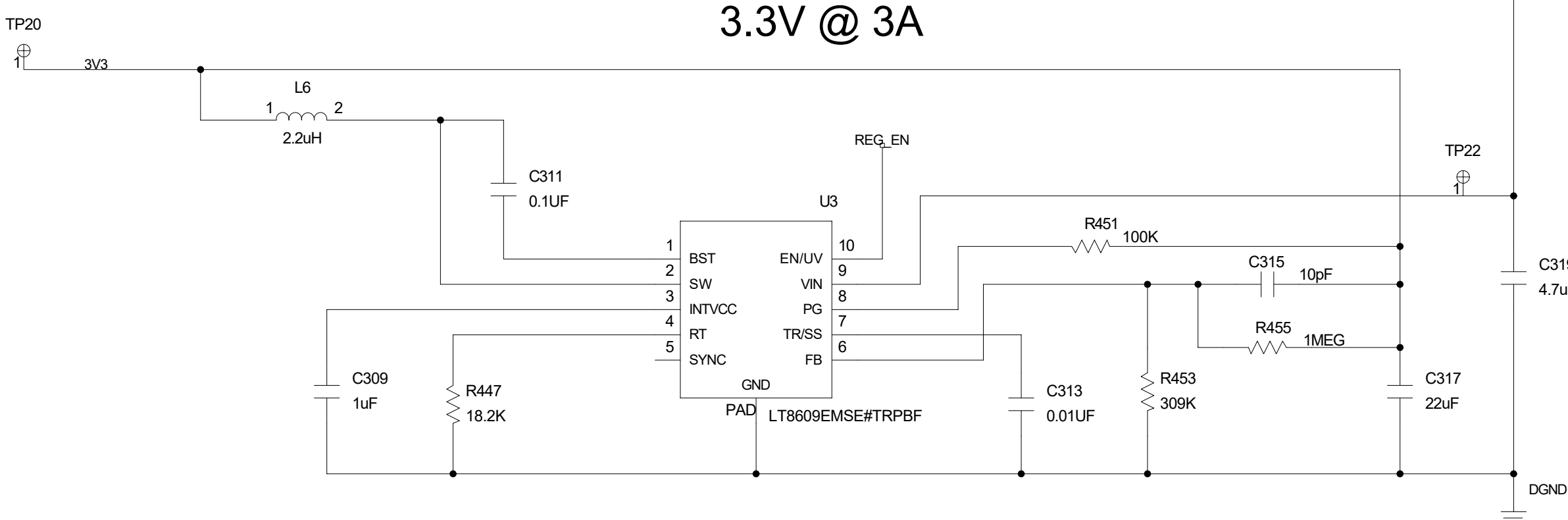
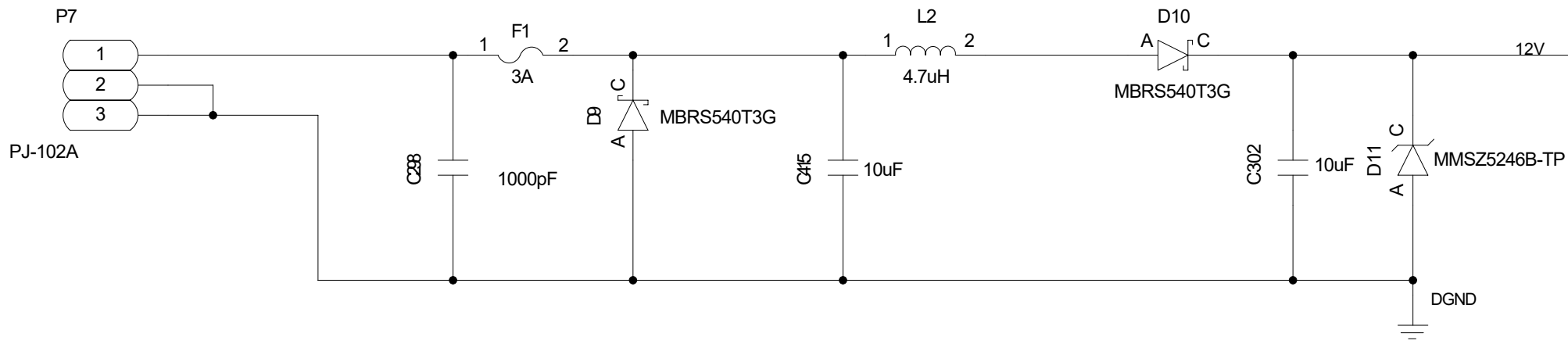
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

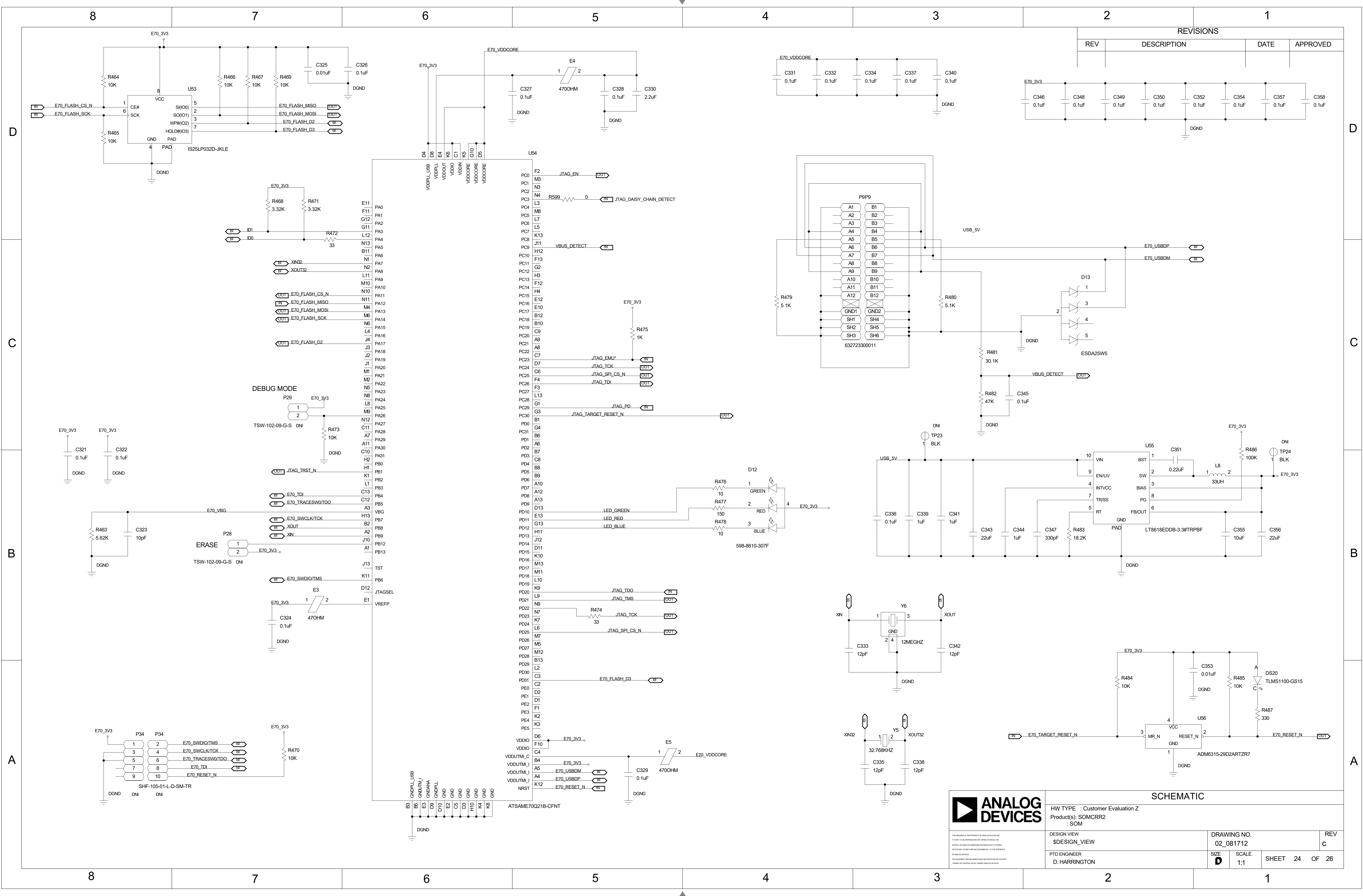
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HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM			
DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_081712		REV c
PTD ENGINEER D. HARRINGTON	SIZE D	SCALE 1:1	SHEET 22 OF 26

POWER SUPPLY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM			
DESIGN VIEW \$DESIGN_VIEW		DRAWING NO. 02_081712	
PTD ENGINEER D. HARRINGTON		REV C	
SIZE D	SCALE 1:1	SHEET 23 OF 26	



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



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SCHEMATIC			
HW TYPE : Customer Evaluation Z			
Product(s) : SOMCRR2			
: SOM			
DESIGN VIEW	DRAWING NO.		
\$DESIGN_VIEW	02_081712		
PTD ENGINEER	SIZE	SCALE	REV
D. HARRINGTON	D	1:1	c
SHEET 24		OF 26	

Schematic	BOM	Layout	Summary of Changes	Date	Production Status
A	A	A	Initial design	03/18/2024	Prototype
B	B	B	U9,U74,U75 changed to TC7MBL3125CFT(EL) P41 removed and replaced with S16 DIP swtich for clock routing ID0, ID1 Connected to U14 and to SOM A and SOM B connectors Added R599 to feed SW1 signal to Debug Agent Board size changed to fit new enclosure	04/1/2025	Prototype
C	C	C	U30, U31 changed to NXU0104GU12-Q100X U81, C432, C433 added to design. R78, R81, R110, R113 Populated R129, R130, R131, R132 DNI	04/29/2026	Prototype

 ANALOG DEVICES	SCHEMATIC HW TYPE : Customer Evaluation Z Product(s): SOMCRR2 : SOM				
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	PTD ENGINEER D. HARRINGTON		SIZE D	SCALE 1:1	SHEET 26 OF 26