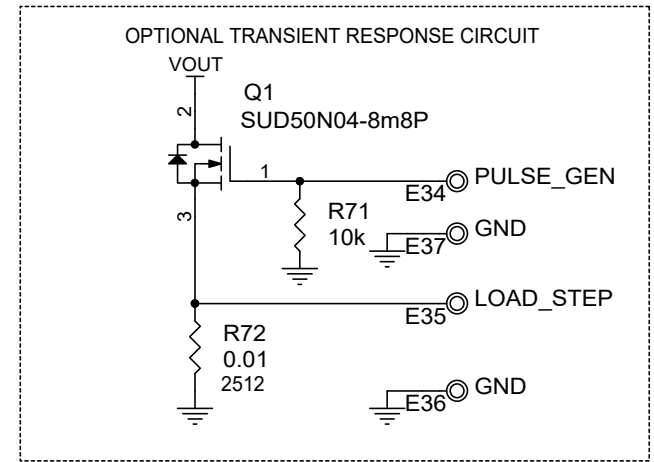
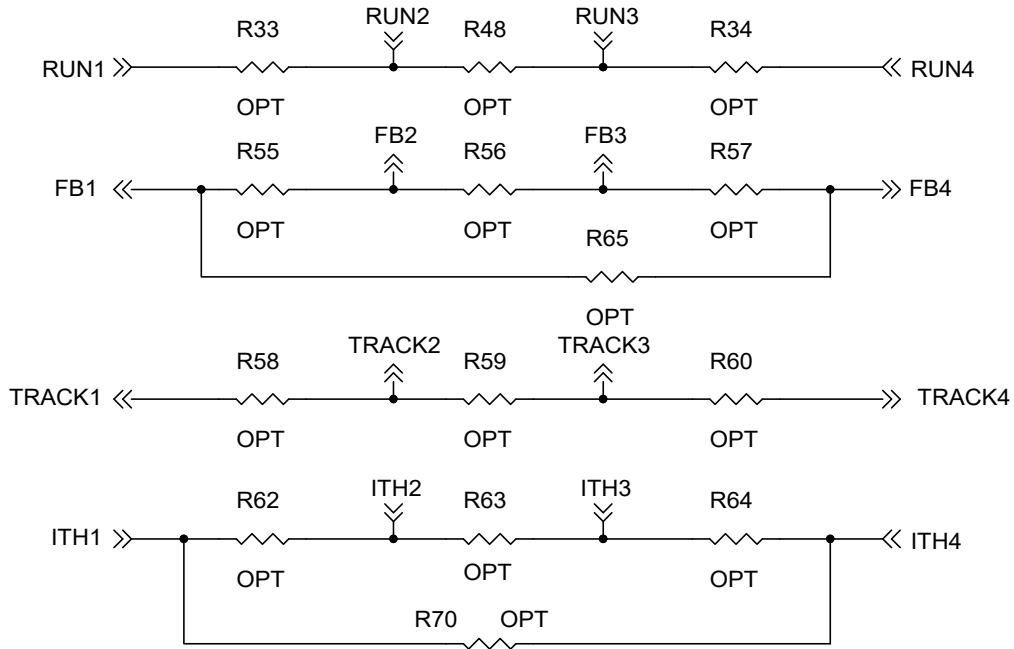
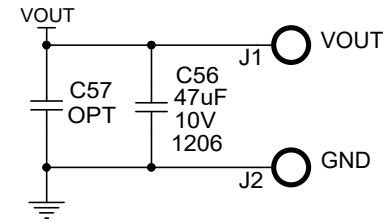
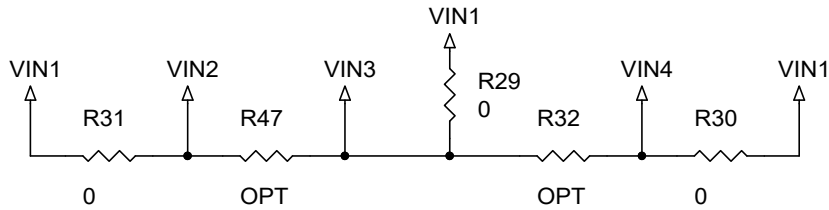


NOTES: UNLESS OTHERWISE SPECIFIED:
 [1] If operation with separate supplies is required, disconnect the desired Vin from Vin1 and other Vin (Vin2, Vin3 or Vin4) and apply appropriate input voltages for normal operation.
 2. All Resistors are 0603 1% and Capacitors are 0603

CUSTOMER NOTICE		APPROVALS		
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES. J.W.	APP ENG. F.Y.	
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.				TITLE: SCHEMATIC
				QUAD 18V ±5A SYNCHRONOUS BUCK REGULATORS
				IC NO. LT7200S
				SKU NO. DC2851A
				SCHEMATIC NO. AND REVISION: DC2851A-4
				SIZE: N/A
				DATE: Tuesday, October 18, 2022
				SHEET 1 OF 2

OPTIONAL JUMPERS FOR PARALLELING PHASES FOR DESIRED NUMBER OF OUTPUT VOLTAGE RAILS



NOTE:
Please refer to the data sheet and demo board manual for more details and examples of paralleling phases to obtain the desired number of output voltage rails.

<p align="center">CUSTOMER NOTICE</p> <p>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.</p>		<p align="center">APPROVALS</p>		
		PCB DES.	J W	
APP ENG.	F Y	IC NO. LT7200S	SCHEMATIC NO. AND REVISION:	
		SKU NO. DC2851A	DC2851A-4	
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		SIZE: N/A	DATE: Tuesday, October 18, 2022	SHEET 2 OF 2