LF & LDO CONNECTIONS

NOTE: CAPS SHOULD BE CLOSEST TO THE PIN OF THE DUT
INPUTS: REFERENCES & SYSCLK

REFERENCE INPUTS

SYSTEM CLOCK INPUT

[Diagram of reference inputs and system clock input with component labels such as C301, R301, etc.]
MPIN SECTION

1. PLACE R623, R628, R633, R638, R643, R648, R653 BELOW J601, J602, J603, J604, J605, J607 RESPECTIVELY TO PREVENT/MINIMIZE STUBS WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

2. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

3. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

4. OVERALL TRACE LENGTH MATCHING UP LESS THAN 1 MILE.

5. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

6. OVERALL MATCH TRACES MATCHING UP LESS THAN 1 MILE.

7. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

8. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

9. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

10. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT WITH A LENGTH DIFFERENCE MUCH LESS THAN 1MIL.

11. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

12. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

13. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

14. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

15. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

16. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

17. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

18. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.

19. MATCH TRACES MP0_DUT, MP1_DUT, MP2_DUT, MP5_DUT MATCHING UP LESS THAN 1 MILE.
SWITCHER SECTION

DC TO DC STEP DOWN - 6V TO 3.3V

6V WALL CONNECTION

8.5V OVER PROTECTION CIRCUIT