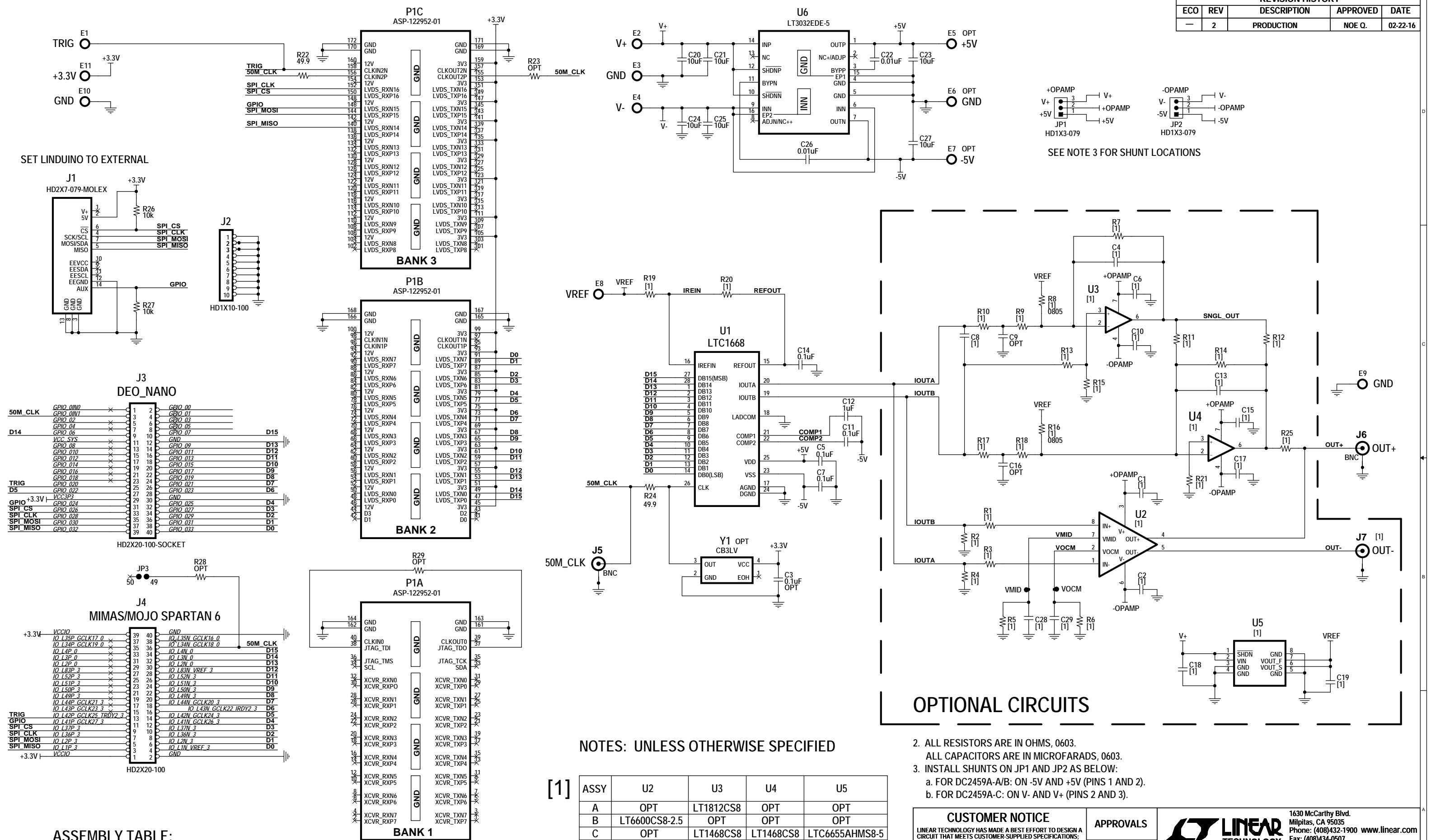


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	NOE Q.	02-22-16



SET LINDUINO TO EXTERNAL

SEE NOTE 3 FOR SHUNT LOCATIONS

NOTES: UNLESS OTHERWISE SPECIFIED

- ALL RESISTORS ARE IN OHMS, 0603.
- ALL CAPACITORS ARE IN MICROFARADS, 0603.
- INSTALL SHUNTS ON JP1 AND JP2 AS BELOW:
 - FOR DC2459A-A/B: ON -5V AND +5V (PINS 1 AND 2).
 - FOR DC2459A-C: ON V- AND V+ (PINS 2 AND 3).

[1]	ASSY	U2	U3	U4	U5
A	OPT	LT1812CS8	OPT	OPT	OPT
B	LT6600CS8-2.5	OPT	OPT	OPT	OPT
C	OPT	LT1468CS8	LT1468CS8	LTC6655AHMS8-5	

ASSEMBLY TABLE:

[1]	ASSY	C1,C2,C28,C29	C4	C6,C10	C8	C13	C15,C17,C18	C19	J7	R1,R3	R2,R4	R5	R6	R7	R8,R11,R14,R16	R9	R10	R12	R13	R15	R17,R18	R19	R20	R21	R25
A	OPT	OPT	0.1uF	62pF	OPT	OPT	OPT	OPT	OPT	OPT	52.3	OPT	OPT	500	OPT	200	0	0	200	500	OPT	OPT	2k	OPT	0
B	0.1uF	OPT	OPT	OPT	OPT	OPT	OPT	OPT	BNC	1.58k	52.3	0	1k	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	2k	OPT	OPT
C	OPT	20pF	0.1uF	OPT	47pF	0.1uF	10uF	OPT	OPT	OPT	OPT	OPT	OPT	1k	1k	25	25	OPT	OPT	0	25	4.02k	OPT	0	0

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS

PCB DES.	KIM T.
APP ENG.	NOE Q.

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TITLE: SCHEMATIC
16-BIT, 50 MSPS DAC

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.	SCALE = NONE	DATE: Monday, February 22, 2016	SHEET 1 OF 1
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