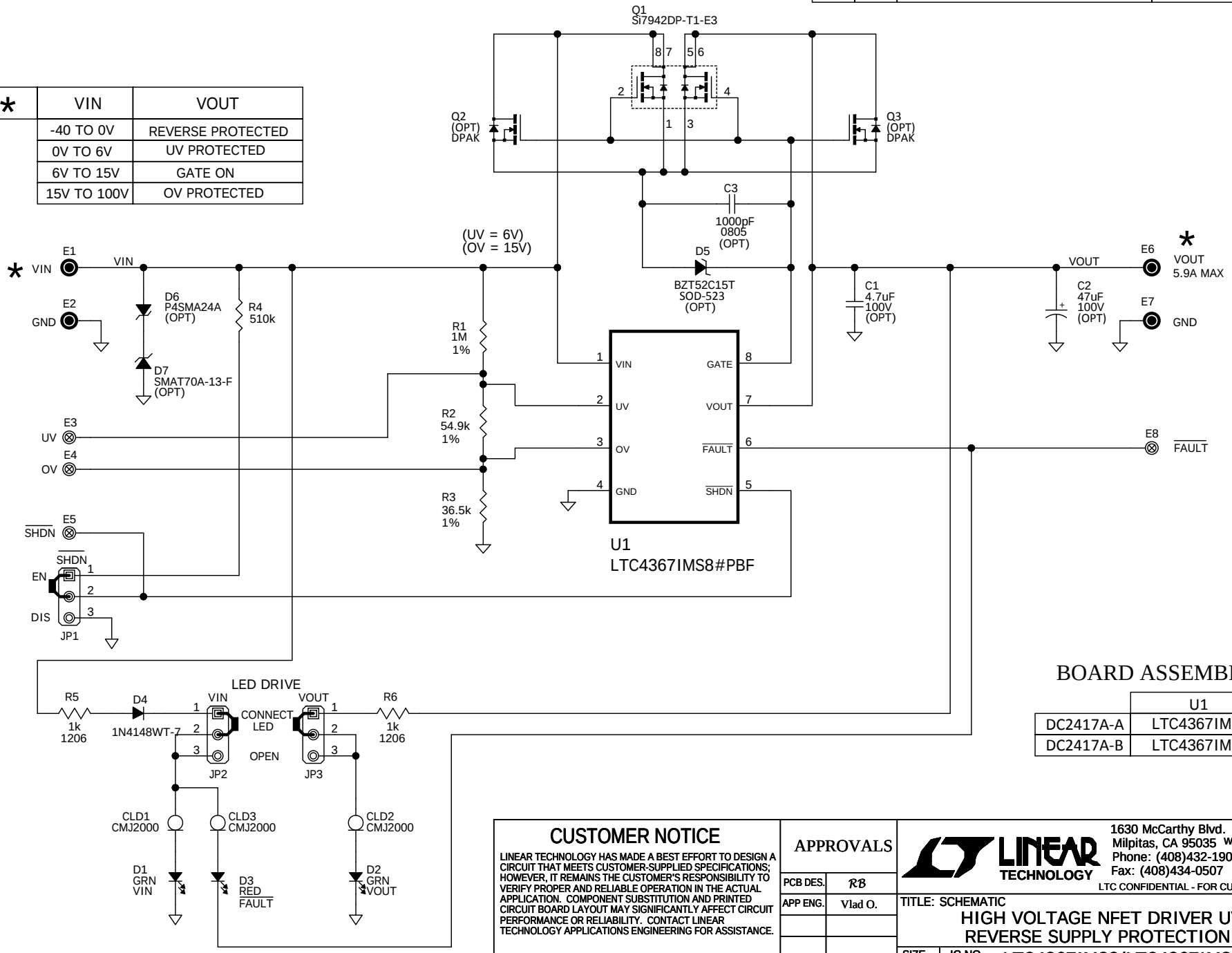
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	1st Prototype	Vlad Ostrerov	May 28, 2015
	2	Production	Vlad Ostrerov	Dec 11, 2015

*	VIN	VOUT
	-40 TO 0V	REVERSE PROTECTED
	0V TO 6V	UV PROTECTED
	6V TO 15V	GATE ON
	15V TO 100V	OV PROTECTED



BOARD ASSEMBLY

	U1
DC2417A-A	LTC4367IMS8
DC2417A-B	LTC4367IMS8-1

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	RB
APP ENG.	Vlad O.

SCALE = NONE

LINEAR TECHNOLOGY
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TITLE: SCHEMATIC

HIGH VOLTAGE NFET DRIVER UV, OV AND REVERSE SUPPLY PROTECTION

SIZE	IC NO.	REV.
N/A	LTC4367IMS8/LTC4367IMS8-1 DEMO CIRCUIT 2417A	2

MODIFY DATE: Dec 11, 2015 2417A_REV2 SHEET 1 OF 1