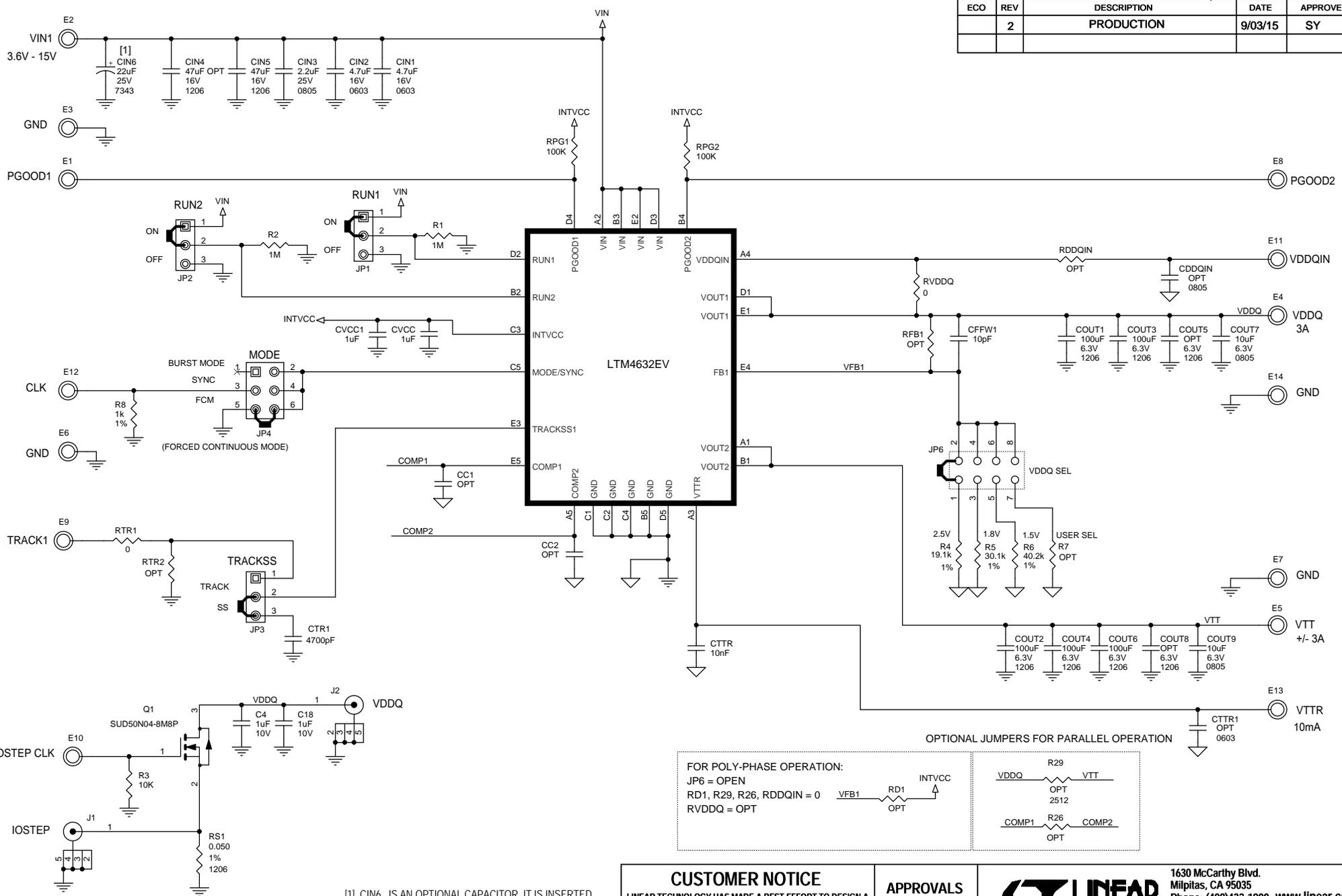
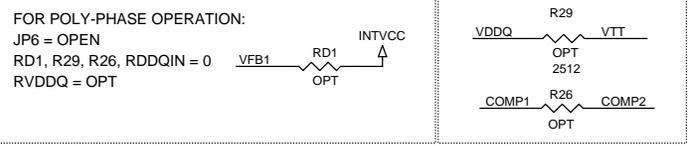


REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	2	PRODUCTION	9/03/15	SY



[1] CIN6, IS AN OPTIONAL CAPACITOR. IT IS INSERTED TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

NOTE: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS AND CAPACITORS ARE 0603



CUSTOMER NOTICE		APPROVALS		LINEAR TECHNOLOGY	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	LT	TITLE: SCHEMATIC	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		APP ENG.	SY		
		SCALE = NONE		15V DUAL 3A DDR μMODULE REGULATOR	
DATE: Thursday, September 03, 2015		SIZE		IC NO.	LTM4632EV
		N/A		DEMO CIRCUIT 2367A	
SHEET 1 OF 1		REV. 2		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	