1. Associated Documents

- BOM
  700-DC2260REV1

- ASY DWG
  705-DC2260REV1

- PCB FAB
  600-DC2260REV1

CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

<table>
<thead>
<tr>
<th>PCB DES.</th>
<th>AK</th>
<th>APP ENG.</th>
<th>JON M</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

TITLE: SCHEMATIC
 IsoSPI 12-CELL BATTERY-STACK MONITOR

IC NO
LTC6811IG-2

N/A
DEMO CIRCUIT 2260A

SCALE = NONE

DATE: Thursday, June 04, 2015

REV.
1