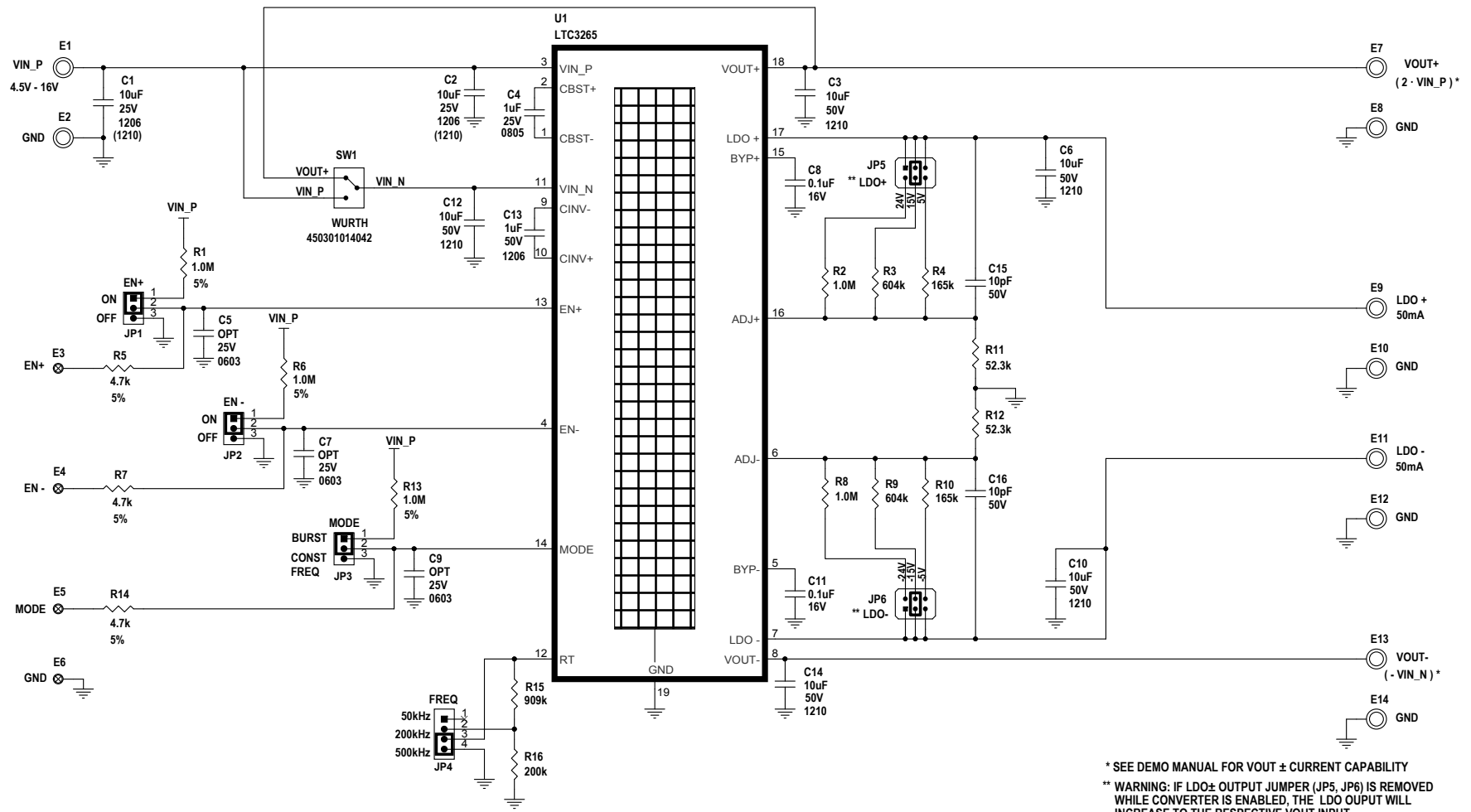


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION FAB	M. MERCHANT	05 - 28 - 15



* SEE DEMO MANUAL FOR VOUT ± CURRENT CAPABILITY
 ** WARNING: IF LDO± OUTPUT JUMPER (JP5, JP6) IS REMOVED WHILE CONVERTER IS ENABLED, THE LDO OUTPUT WILL INCREASE TO THE RESPECTIVE VOUT INPUT.

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE 0402, 1%, 1/16W
ALL CAPACITORS ARE 0402, 10%
2. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS PCB DES: NC APP ENG: MARTY M.		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only
		TITLE: SCHEMATIC LOW NOISE DUAL SUPPLY WITH BOOST AND INVERTING CHARGE PUMPS		
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE	SIZE N/A IC NO. LTC3265EDHC DEMO CIRCUIT 2235A	REV. 3
DATE: 05 - 28 - 15		SHEET 1 OF 1		