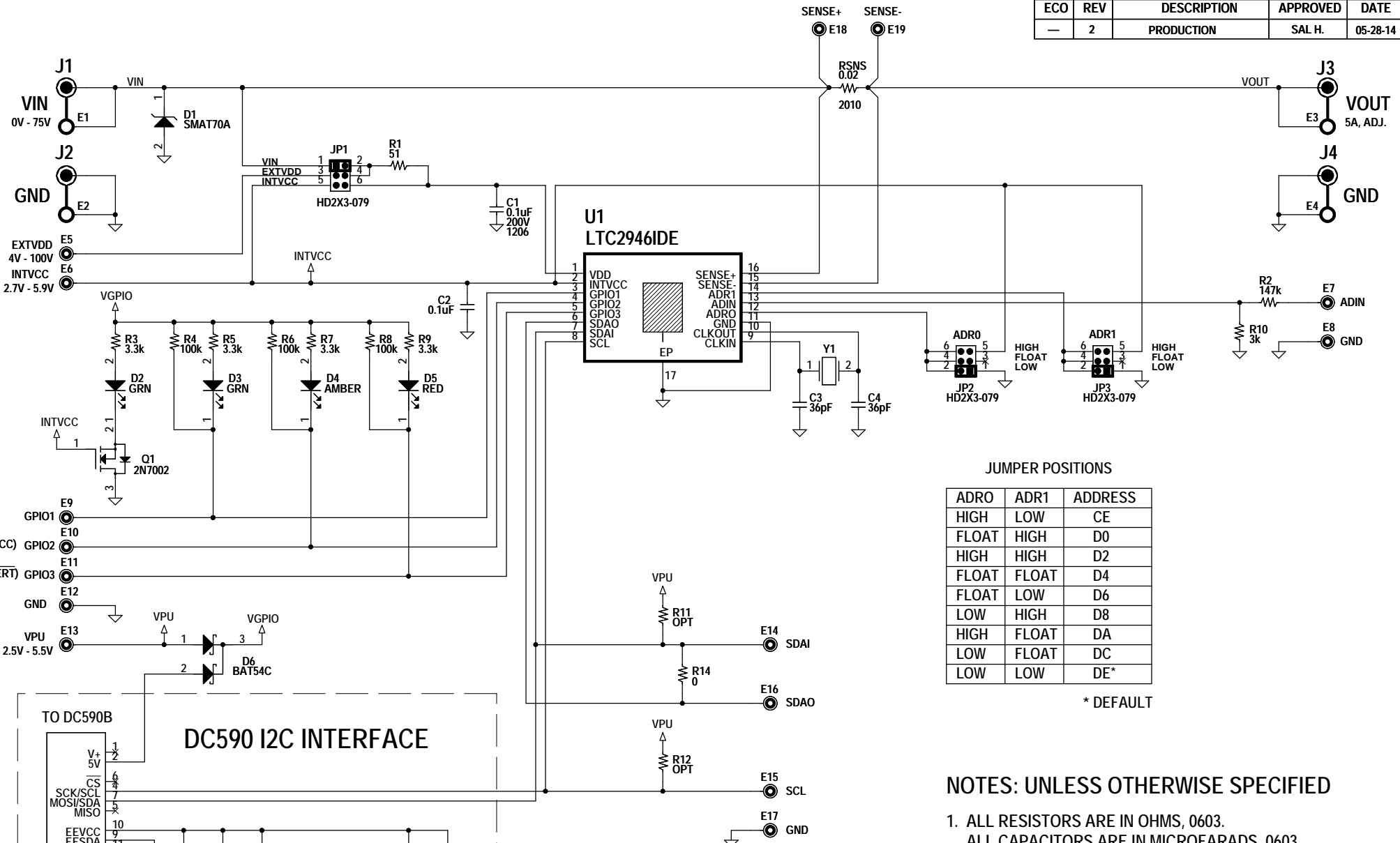


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	SAL H.	05-28-14



JUMPER POSITIONS

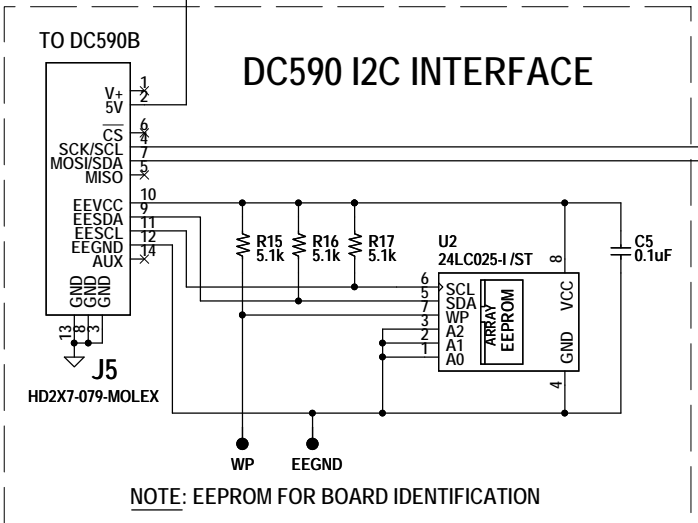
ADR0	ADR1	ADDRESS
HIGH	LOW	CE
FLOAT	HIGH	D0
HIGH	HIGH	D2
FLOAT	FLOAT	D4
FLOAT	LOW	D6
LOW	HIGH	D8
HIGH	FLOAT	DA
LOW	FLOAT	DC
LOW	LOW	DE*

* DEFAULT

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
ALL CAPACITORS ARE IN MICROFARADS, 0603.
2. INSTALL SHUNTS AS SHOWN.

REMOVE R14 TO SEPERATE SDAI AND SDA0



CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS

PCB DES.	KIM T.
APP ENG.	SAL H.

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TITLE: SCHEMATIC
WIDE RANGE I2C POWER MONITOR WITH ENERGY AND COULOMB METER

SIZE: N/A IC NO.: LTC2946IDE
DEMO CIRCUIT 2156A

SCALE = NONE DATE: 05/28/2014, 05:25 PM SHEET 1 OF 1

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.